

REVISED EDITION



SERVICE MANUAL

Model TS-830S, M (M:EXCEPT USA MARKET)

SP-230, VFO-230, AT-230, DS-2 (W) TYPE ONLY



CONTENTS

TS-830S, M

SPECIFICATIONS.....	2
CIRCUIT DESCRIPTION	3
AC VOLTAGE CONVERSION.....	9
INSIDE VIEWS.....	10
PC BOARD VIEWS.....	11
PARTS LIST.....	18
PACKING	26
DISASSEMBLY.....	27
ADJUSTMENTS.....	32
LOCATION OF ADJUSTMENTS	42
LEVEL DIAGRAM	44
TS-830M SCHEMATIC DIAGRAM	61
TS-830S SCHEMATIC ABBREVIATION	62
TS-830S SCHEMATIC DIAGRAM	63
BLOCK DIAGRAM.....	64

VFO-230

BLOCK DIAGRAM.....	45
SPECIFICATIONS.....	46
CIRCUIT DESCRIPTION	46
PC BOARD VIEWS.....	49
PARTS LIST	51
ADJUSTMENTS	53
PACKING	53
AC VOLTAGE CONVERSION.....	56
SCHEMATIC DIAGRAM	57
AT-230.....	58
DS-2	60
SP-230	BACK COVER

HF SSB TRANSCEIVER

Downloaded by □

□
Amateur Radio Directory

[GENERAL]

Frequency Range	160 m Band 1.8 ~ 2.0 MHz 80 m Band 3.5 ~ 4.0 MHz 40 m Band 7.0 ~ 7.3 MHz * 30 m Band 10.1 ~ 10.15 MHz (10.0 MHz WWV) 20 m Band 14.0 ~ 14.35 MHz * 17 m Band 18.068 ~ 18.168 MHz 15 m Band 21.0 ~ 21.45 MHz * 12 m Band 24.89 ~ 24.99 MHz 10 m Band 28.0 ~ 29.7 MHz
Modes	SSB/CW: 830S, SSB/AM/CW: 830M
Power Requirement	120V AC (220V modifiable), 50/60 Hz: 830S (K) 220/240V AC (selectable), 50/60Hz : 830S (T) (W) (X) 120/220V AC (selectable), 50/60Hz : 830M
Power Consumption	Transmit: 295 watts Receive: 32 watts (with heater off)
Dimensions	333 (13.3) x 133 (5.3) x 333 (13.3) mm (inch)
Weight	13.5 kg (29.8 lbs)

[TRANSMITTER]

*Final Power Input	220W PEP for SSB operation : 830S, M 180W DC for CW operation : 830S, M 80W DC for AM operation : 830M
Audio Input Impedance	500Ω ~ 50 kΩ
RF Output Impedance	50Ω ~ 75Ω
Frequency Stability	Within 1 kHz during the first hour after 1 minute of warmup. Within 100 Hz during any 30 minute period after warmup.
Carrier Suppression	Better than 40 dB
Sideband Suppression	Better than 60 dB
Spurious Radiation	Better than 60 dB
Harmonic Radiation	Better than 40 dB
Audio Freq. Response	400 to 2,600 Hz, within -6 dB
3rd Order Intermodulation Distortion	Better than -36 dB

[RECEIVER]

Receiver Sensitivity	0.25 μV at 10 dB S + N/N for SSB, CW : 830S, M 2 μV at 10 dB S + N/N for AM : 830M
Image Ratio	Better than 60 dB
IF Rejection	Better than 80 dB
Receiver Selectivity	
SSB/CW WIDE	2.4 kHz (-6 dB), 3.6 kHz (-60 dB) : 830S, M
CW NARROW	With YK-88C (option) 500 Hz (-6 dB), 1.5 kHz (-60 dB) With YK-88CN (option) 270 Hz (-6 dB), 1.1 kHz (-60 dB) With YG-455C (option) 500 Hz (-6 dB), 820 Hz (-60 dB) With YG-455CN (option) 250 Hz (-6 dB), 500 Hz (-60 dB)
AM	6 kHz (-6 dB), 11 kHz (-60 dB) : 830M
Variable Bandwidth	
SSB with 2.4 kHz filter	500 Hz ~ 2.4 kHz (-6 dB) continuously variable : 830S, M
CW with 500 Hz filter	150 Hz ~ 500 Hz (-6 dB) continuously variable : 830S With optional filter YK-88C and YG-455C added.
AM	4.5 kHz ~ 6 kHz (-6 dB) continuously variable : 830M
Notch-filter Attenuation	Better than 40 dB
Audio Output Impedance	8 ~ 16Ω
Audio Output	1.5W (8Ω)

*Will transmit on the new 30, 17, and 12 meter bands. Diodes installed for preventing accidental transmission before government amateur authorization.

NOTE: The circuit and ratings may change without notice due to developments in technology.

INTRODUCTION

The TS-830S is a dual conversion transceiver using two intermediate frequencies, 8.83 MHz and 455 kHz. However, this differs from transceivers of the so called Collins type (such as the TS-520) in that the bandwidth of both intermediate frequencies is narrowed for performing VBT operation.

Therefore, the TS-830S can basically be considered a single conversion transceiver with an 8.83 MHz IF.

Operation of the transmitter is opposite in process to the receiver. An SSB signal generated at 455 kHz is converted to 8.83 MHz, and is then mixed with the PLL (local oscillator output) to produce the final transmission frequency. The circuitry is hybrid with vacuum tubes used only in the driver (12BY7A) and final-stage power amplifier (6146B's). The PLL circuit generates a heterodyne frequency for each band, a counter reference signal, and a 25 kHz marker, all from a single crystal oscillator.

The TS-830S incorporates an IF SHIFT circuit, VBT, VOX (also used for CW semi break-in), side tone circuit, RF speech processor, transmission monitor circuit, noise blanker, XTAL calibrator, notch filter, and etc.

RECEIVER CIRCUIT

The signal coming from the antenna is routed through step-up antenna coil via an RF ATT switch and IF trap. MOS FET (Q1: 3SK73) amplifies this signal. Approximately 9 dB of negative feedback is applied to the RF amplifier to reduce noise and expand the amplifiers range of linear operation. The signal passes through a buffer amplifier (Q2: 2SK125) and is mixed with the PLL VCO output by a balanced mixer (Q3, Q4: 2SK125). The signal is now converted to the first intermediate frequency, 8.83 MHz. Entering the IF unit, this signal is amplified by Q1 (2SK125), passes through ceramic filter CF1 and the NB gating circuit, and is applied to crystal filter XF1 whose center frequency is 8.83 MHz. The signal leaving the crystal filter is mixed with the VBT local oscillator output at 8.375 MHz by a balanced mixer (Q2, Q3: 3SK73) where it is converted to the second intermediate frequency, 455 kHz. The signal then passes through ceramic filter CF2 whose center frequency is 455 kHz, and is amplified by Q4 (3SK73). It then is fed to the notch circuit, Q5 through Q7 (2SC1815). After being amplified by Q8 (3SK73), it is demodulated to an audio signal by the product detector (D20~D23, IN60 X4).

In the AF unit, the audio signal is amplified by Q3 (2SC2240), passes through the AF GAIN control and after being amplified by the power amplifier Q4 (HA1368R), drives the speaker.

Item	Rating
Center frequency f_0	8.830 MHz
3 dB bandwidth	$f_0 \pm 5 \text{ kHz}$ or more, total 25 kHz or more
30 dB bandwidth	100 kHz or less
Input level (at 80 dB μ output)	93.5 dB or less
Ripple (within 3 dB bandwidth)	1 dB or less
Spurious response	20 dB or more within $f_0 \pm 1.5 \text{ MHz}$
Input and output impedance	330Ω

Table 1. Ceramic filter pair (L72-0310-05) 2 pcs.

SFA8.83MF combined (IF unit, CF1A & B)

Item	Rating
Center frequency f_0	8830 kHz
Center frequency deviation	Within $\pm 150 \text{ Hz}$ at 6 dB
Pass bandwidth	$\pm 1.35 \text{ kHz}$ or more at 6 dB
Attenuation bandwidth	$\pm 1.7 \text{ kHz}$ or less at 20 dB $\pm 2.5 \text{ kHz}$ or less at 60 dB $\pm 3.4 \text{ kHz}$ or less at 80 dB
Ripple	2 dB or less
Loss	6 dB or less
Guaranteed attenuation	80 dB or more within $f_0 \pm 3.4 \text{ kHz}$ to $\pm 1 \text{ MHz}$
Input and output impedance	600Ω / 15 pF

Table 2. Crystal filter (L71-0222-05)
YK88S1 (IF unit, XF1)

Item	Rating
Center frequency f_0	8831.5 kHz
Center frequency deviation	Within $\pm 250 \text{ Hz}$ at 6 dB
Pass bandwidth	$\pm 3.0 \text{ kHz}$ or more at 6 dB
Attenuation bandwidth	$\pm 6 \text{ kHz}$ or less at 60 dB $\pm 10 \text{ kHz}$ or less at 80 dB
Ripple	2 dB or less
Loss	3 dB within $\pm 2 \text{ dB}$
Guaranteed attenuation	80 dB or more
Input and output impedance	600Ω // 15 pF

Table 3. Crystal filter (L71-0222-05)
YK-88A (IF unit, XF2)

Item	Rating
Center frequency	455 kHz $\pm 0.2 \text{ kHz}$
6 dB bandwidth	2.7 kHz or more
60 dB bandwidth	4.5 kHz or less
Guaranteed attenuation ($f_0 \sim 1 \text{ MHz}$)	60 dB or more
Spurious (600 ~ 700 kHz)	40 dB or more
Ripple (within 6 dB bandwidth)	2 dB or less
Loss	6 dB
Input and output impedance	2 kΩ

Table 4. Ceramic filter (L72-0314-15)
CFJ455K5 (IF unit, CF2)

Item	Rating
Center frequency f_0	456.5 kHz
6 dB bandwidth	$\pm 3 \text{ kHz}$ or more
50 dB bandwidth	$\pm 9 \text{ kHz}$ or less
Ripple ($f_0 \pm 2 \text{ kHz}$)	2 dB or less
Loss	6 dB or less
Guaranteed attenuation ($f_0 \sim 100 \text{ kHz}$)	60 dB or more
Input and output impedance	2 kΩ

Table 5. Ceramic filter (L72-0322-05)
CFW 456.5HT (IF unit, CF3)

TRANSMITTER CIRCUIT

Audio input picked up by the microphone comes to the IF unit and is amplified by Q19~21. The input circuit adapts to any microphone impedance of from 500 ohms to 50k ohms. This amplified output is converted to a DSB signal by the balanced modulator D29-32 (1N60 × 4), passes through buffer amplifier Q22 (2SK19), a 455kHz ceramic filter, and the output appears as a SSB signal. This signal passes either buffer amplifier Q23 (2SC1815), or the speech processor consisting of Q24, 26, and 27. Then the first transmit mixer (Q28: 3SK73) converts this to an 8.83 MHz SSB signal. After passing the 8.83 MHz crystal filter and being amplified by Q29 (3SK73), the signal is applied to the second transmit mixer in the RF unit. This double balanced mixer (Q6, Q7: 3SK73), mixes the signal with the PLL VCO output to convert to the final Transmit frequency. Output is amplified by the driver tube (V1: 12BY7A) and then by the final power amplifier (two 6146B's) and is applied to the antenna via a π-matching network.

PLL CIRCUIT

The PLL signal is synthesized from the VFO, CAR, and VCO outputs. The TS-830S employs a programmable divider in the PLL to synthesize the heterodyne frequency from the standard reference oscillator frequency. This simplifies the PLL circuitry by eliminating the need for a separate HET XTAL for each band.

CIRCUIT DESCRIPTION

Frequency organization of the PLL circuit is shown in Figure 1, the circuit diagram in Figure 2, and frequency organization in other circuits is summarized in Table 4. Please refer to Figure 1 when reading the following description on PLL circuit operation. MIX (3), a double balanced mixer, mixes the CAR output with the VFO output. MIX (2) which operates in 18 MHz bands and above, mixes either 10 MHz, in the 14 and 18 MHz bands, or 20 MHz, in the 21, 24.5, and 28 MHz bands, with the output of MIX (3) to generate the appropriate frequency needed for each band, as shown in Table 4. This is mixed with the VCO output for each band by MIX (1) to a frequency given in Table 4, which is then routed through LPF (1), amplified, and wave-shaped to a digital signal. A programmable divider divides this signal into a 500 kHz output. This programmable divider is preset, as shown in Table 4, by the BCD signal which is counter-generated from information coming from the band switch. The phase comparator used is a Motorola MC4044P. The loop filter is made of completely external components to minimize spurious radiation. Six VCOs cover all bands. If the PLL unlocks for any reason, it will be detected from the phase comparator output. This will turn off the VCO output to prevent spurious radiation and, at the same time, the digital display will be blanked to inform the operator of unlock status.

BAND	RX/Tx frequency (MHz)	VCO (MHz)	MIX (1) input (MHz)	MIX (1) output (MHz)	Division ratio	DCBA
1.5	1.5 ~2.0	10.33 ~10.83	14.33 ~14.83	4.0	1/8	1 0 0 0
3.5	3.5 ~4.0	12.33 ~12.83	14.33 ~14.83	2.0	1/4	1 1 0 0
7	7.0 ~7.5	15.83 ~16.33	14.33 ~14.83	1.5	1/3	1 1 0 1
10	10.0 ~10.5	18.83 ~19.33	14.33 ~14.83	4.5	1/9	0 1 1 1
14	14.0 ~14.5	22.83 ~23.33	24.33 ~24.83	1.5	1/3	1 1 0 1
18	18.0 ~18.5	26.83 ~27.33	24.33 ~24.83	2.5	1/5	1 0 1 1
21	21.0 ~21.5	29.83 ~30.33	34.33 ~34.83	4.5	1/9	0 1 1 1
24.5	24.5 ~25.0	33.33 ~33.83	34.33 ~34.83	1.0	1/2	1 1 1 0
28	28.0 ~28.5	36.83 ~37.33	"	2.5	1/5	1 0 1 1
28.5	28.5 ~29.0	37.33 ~37.83	"	3.0	1/6	1 0 1 0
29	29.0 ~29.5	37.83 ~38.33	"	3.5	1/7	1 0 0 1
29.5	29.5 ~30.0	38.33 ~38.83	"	4.0	1/8	1 0 0 0

Table 6. Frequency chart

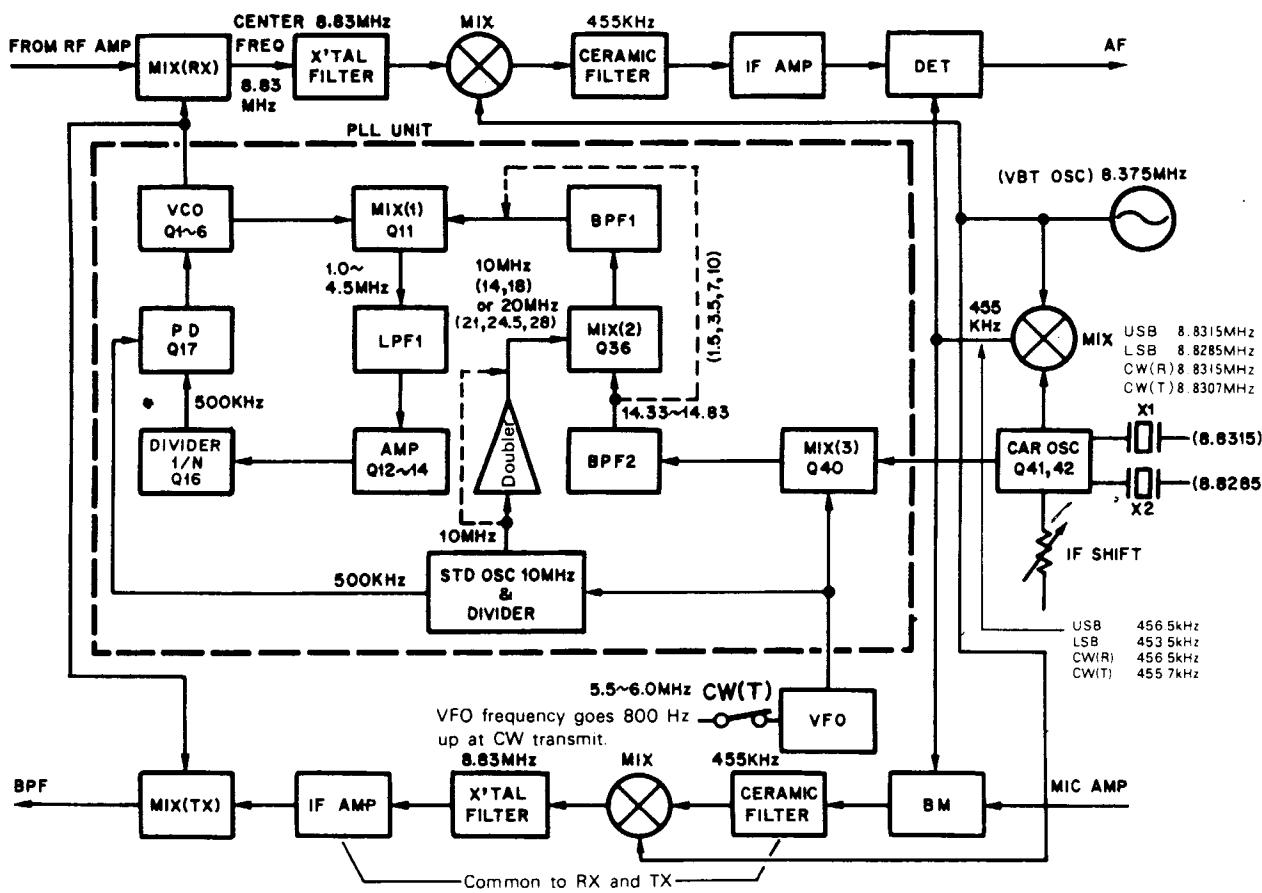


Fig. 1 TS-830 Frequency configuration

CIRCUIT DESCRIPTION

TS-830S, M

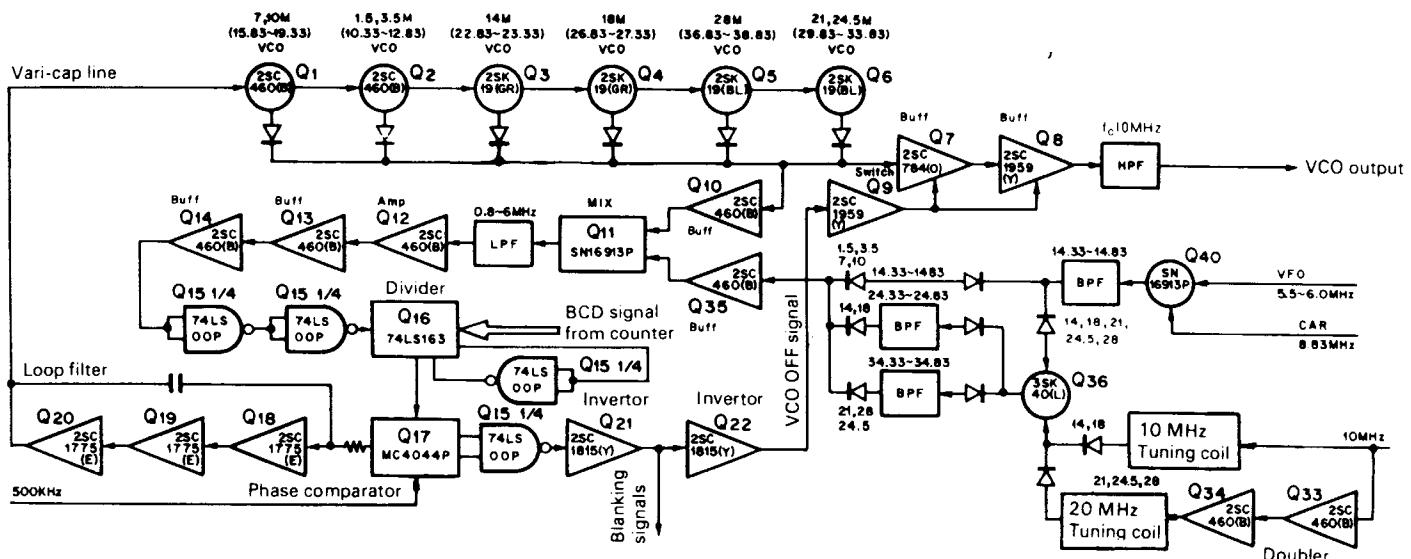


Fig. 2 TS-830 PLL circuit configuration

COUNTER

This counts the 500 kHz to 1 MHz signal which the PLL unit generates by mixing the VFO output with 5 MHz (halved from the 10 MHz reference frequency). Gate, latch, and preset pulses are generated from a 10 Hz pulse generated by dividing 1 kHz, supplied from the PLL, by 100.

BAND	100K	1 M	10M	DCBA	0.5	BAND	100K	1 M	10M	DCBA	0.5
1.5	0	1	0	1 0 0 0	H	21	5	0	2	0 1 1 1	L
3.5	0	3	0	1 1 0 0	H	24.5	0	4	2	1 1 1 0	H
7	5	6	0	1 1 0 1	L	28	5	7	2	1 0 1 1	L
10	5	9	0	0 1 1 1	L	28.5	0	8	2	1 0 1 0	H
14	5	3	1	1 1 0 1	L	29	5	8	2	1 0 0 1	L
18	5	7	1	1 0 1 1	L	29.5	0	9	2	1 0 0 0	H

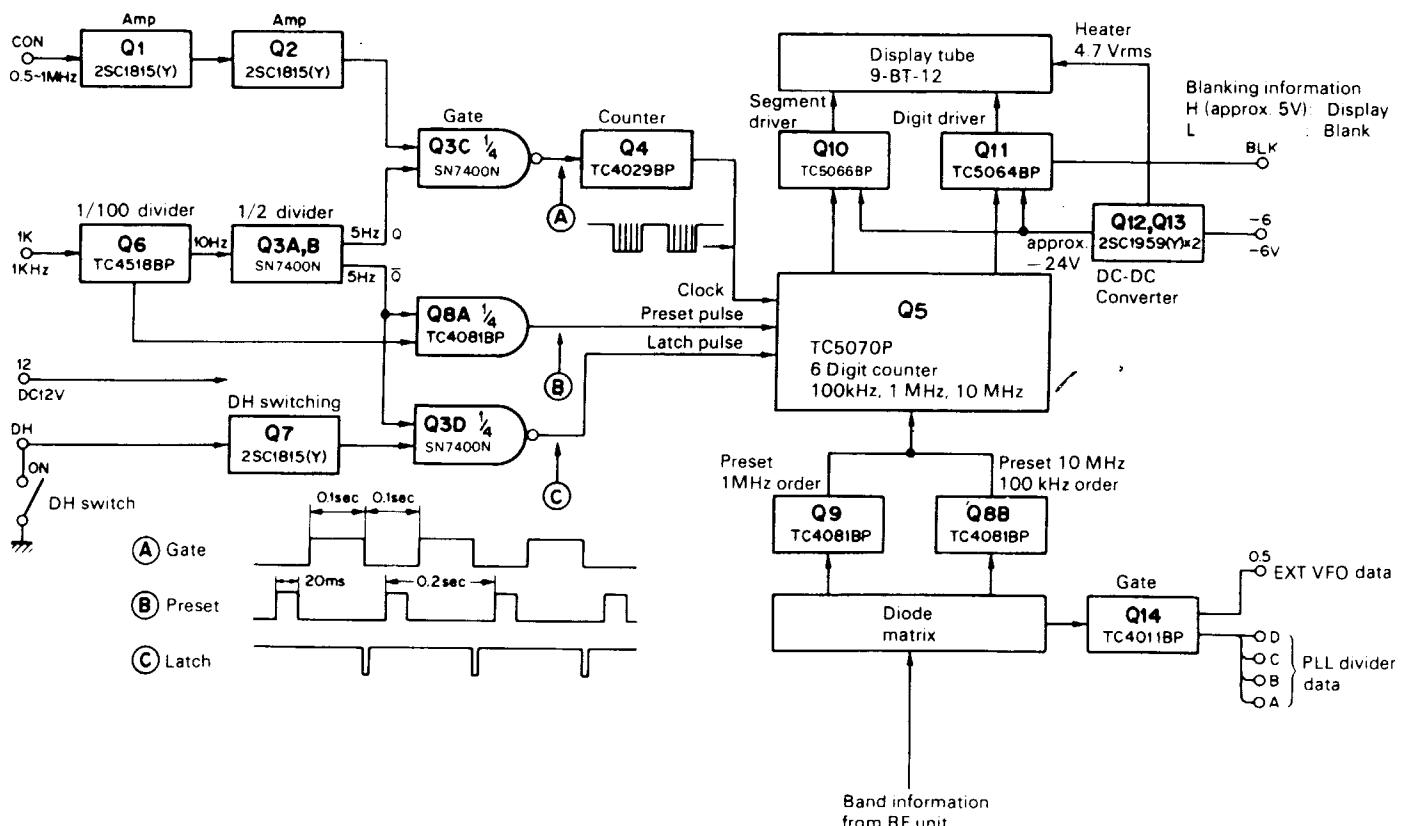


Fig. 3 Counter unit block diagram

CIRCUIT DESCRIPTION

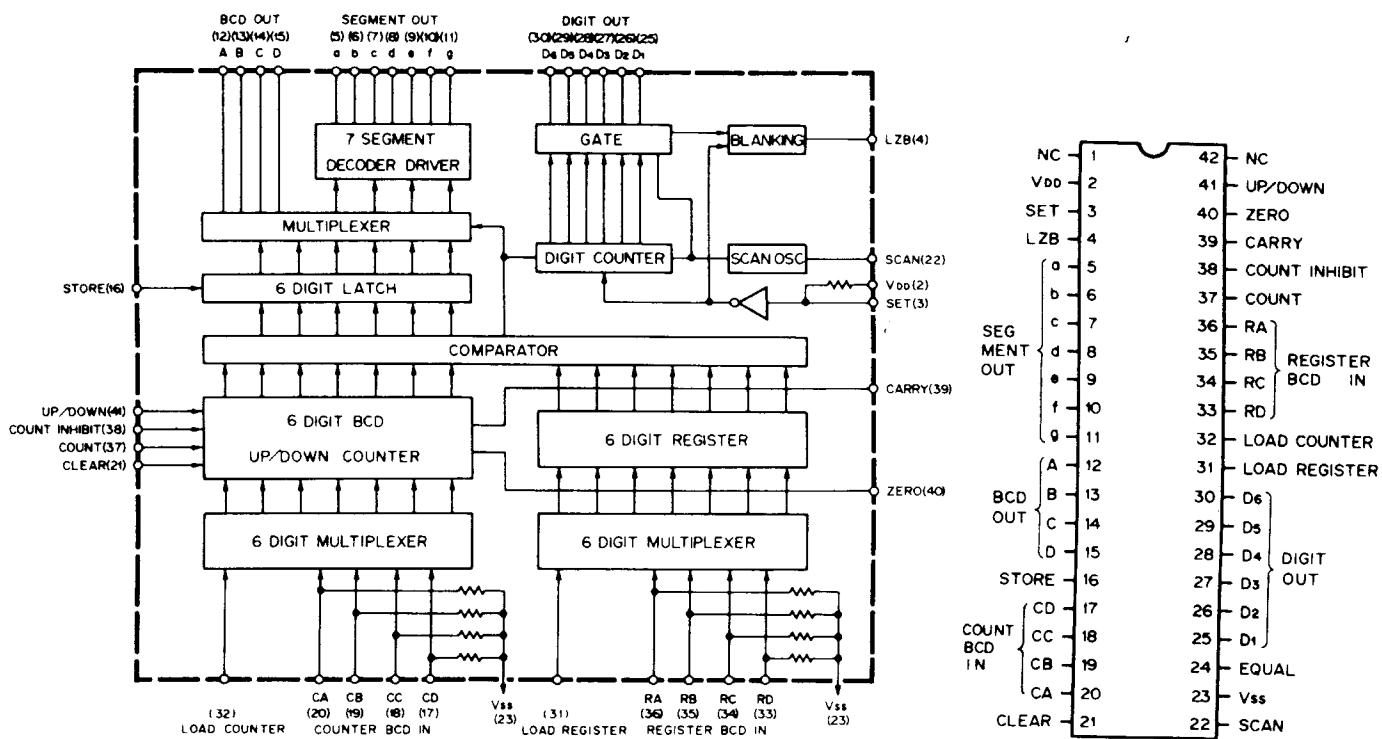


Fig. 4 TC5070P (Counter unit, Q5)

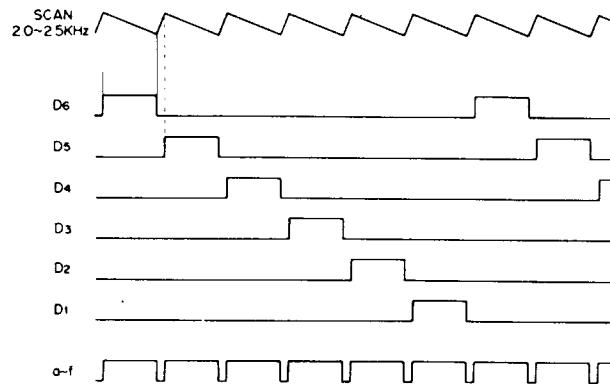
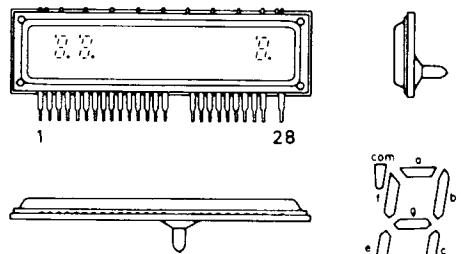


Fig. 5 TC5070P timing chart

Q5 (TC5070P) is a presetable, 6-digit BCD counter which incorporates a 6-digit latch, 6-digit dynamic drive digital counter, and 7-segment decoder/driver. Band information supplied from the RF unit is applied to a diode matrix to preset the 100 kHz, 1 MHz, and 10 MHz digits. Preset values are given in Figure 3. For instance to preset 14.000 MHz, with a 500 kHz counter input signal, 10 MHz value is preset to 1, 1 MHz to 3, and 100 kHz to 5, the 500 kHz counter input signal subtracted from 14.000 MHz. (If no counter input signal were present, 13.500 MHz would be displayed.) Q5 supplies the display tube drivers with 7-segment information and dynamic drive control signals to light the fluorescent display tube.



PIN NO	1	2	3	4	5	6	7	8	9	10	11	12	13	14
CONNECTION	F	N _c	G ₄	N _c	N _c	G ₈	g	f	G ₁	e	d	G ₆	N _c	N _c
	15	16	17	18	19	20	21	22	23	24	25	26	27	28
	G _s	N _p	N _p	G ₄	D _p	G ₃	c	b	G ₂	a	COM	G ₁	N _p	F

Fig. 6 Indicating tube 9-BT-12

In the counter unit, a diode matrix generates frequency division information and supplies the PLL unit with this information. If the PLL unlocks, a BLK (Blanking Low) signal will be applied to the digit drive IC (Q11) to blank the fluorescent display tube. Normally, five high-order digits are displayed. DH (digital hold) locks the display from changing. Digital hold is accomplished by presetting the latch pulse at Low level.

CAR OSCILLATOR

The CAR oscillator is composed of an oscillator and two quartz crystals. The output frequency in each mode is given in Figure 1. During reception, this frequency can be varied by the IF shift.

VFO

The output frequency is 5.5~6.0 MHz. During CW transmission, the frequency will be shifted 800 Hz higher than the reception frequency. Therefore, real operating frequencies will always be displayed.

CIRCUIT FEATURES**1. Optional filters available**

The TS-830S is equipped with 2.7 kHz filters in both the 8.83 MHz and 455 kHz IF's. Narrow filters are separately available for both 8.83 MHz and 455 kHz.

IF jumper pins	8.83 MHz filter	455 kHz filter
CW1	STD (YK88S1)	STD (CFJ455K5)
CW2	OP (option)	STD (CFJ455K5)
CW3	STD (YK-88S1)	OP (option)
CW4	OP (option)	OP (option)
6 dB bandwidth	YK-88S1 = 2.7kHz OP (YK-88C) = 500Hz OP (YK-88CN) = 270Hz	CFJ455K5 = 2.7kHz OP (YG-455C) = 500Hz OP (YG-455CN) = 250Hz

Table 7. STD and OP filter combinations

Item	Rating
Center frequency f_0	8830.7 kHz
Center frequency deviation	$f_0 \pm 150\text{Hz}$ at 6 dB
6 dB bandwidth	$\pm 250\text{ Hz}$ or more
60 dB bandwidth	$\pm 900\text{ Hz}$ or less
Ripple	2 dB or less
Loss	6 dB $\pm 2\text{ dB}$
Guaranteed attenuation	80 dB or more within f_0 $\pm 2\text{ kHz}$ to $\pm 1\text{ MHz}$
Input and output impedance	$600\Omega // 15\text{ pF}$

**Table 8. CW Crystal filter (L71-0211-05)
YK-88C (Option)**

Item	Rating
Center frequency f_0	8830.7 kHz
Center frequency deviation	$f_0 \pm 50\text{ Hz}$ at 6 dB
6 dB bandwidth	$\pm 125\text{ Hz}$ or more
60 dB bandwidth	$\pm 600\text{ Hz}$ or less
Ripple	2 dB or less
Loss	8 dB $\pm 2\text{ dB}$
Guaranteed attenuation	80 dB or more within f_0 $\pm 2\text{ kHz}$ to $\pm 1\text{ MHz}$
Input and output impedance	$600\Omega // 15\text{ pF}$

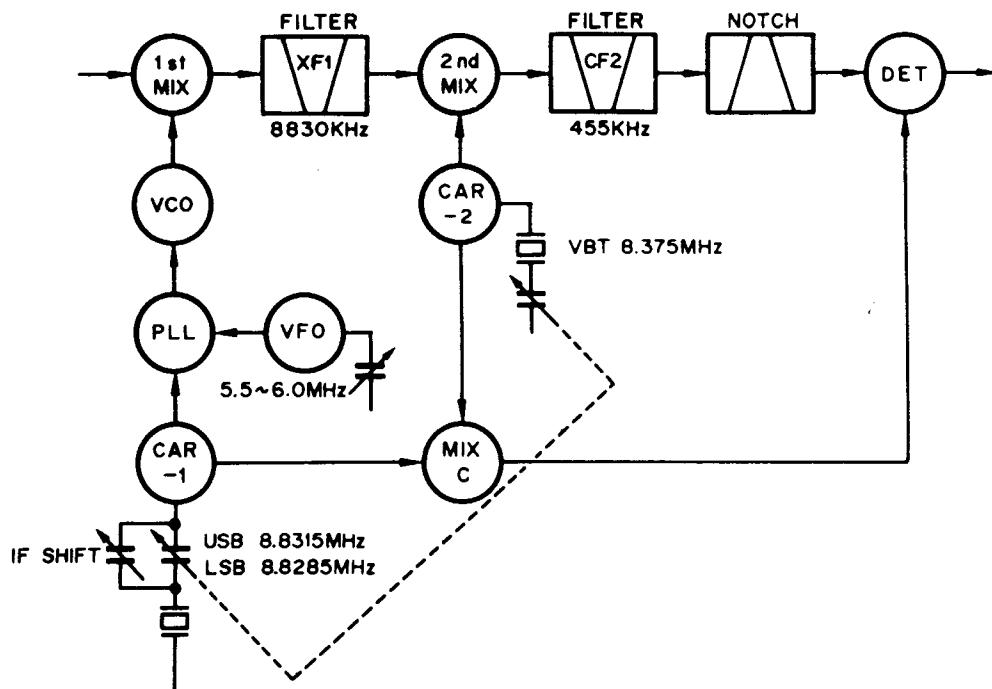
**Table 9. CW Crystal filter (L71-0221-05)
YK-88CN (Option)**

Item	Rating
Center frequency f_0	455.7 kHz
Center frequency deviation	$f_0 \pm 50\text{ Hz}$ at 6 dB
6 dB bandwidth	$\pm 250\text{ Hz}$ or more
60 dB bandwidth	$\pm 425\text{ Hz}$ or less
Ripple	2 dB or less
Loss	6 dB or less
Guaranteed attenuation	80 dB or more at 100 Hz to 455.3 kHz and 456.3 kHz to 2 MHz
Input and output impedance	$2\text{k}\Omega // 15\text{ pF}$

**Table 10. CW Crystal filter (L71-0206-05)
YG-455C (Option)**

Item	Rating
Center frequency f_0	455.7 kHz
Center frequency deviation	$f_0 \pm 50\text{ Hz}$ at 6 dB
6 dB bandwidth	$\pm 125\text{ Hz}$ or more
60 dB bandwidth	$\pm 250\text{ Hz}$ or less
Ripple	2 dB or less
Loss	6 dB or less
Guaranteed attenuation	80 dB or more at 100 Hz to 455.3 kHz and 456.1 kHz to 2 MHz
Input and output impedance	$2\text{k}\Omega // 15\text{ pF}$

**Table 11. CW Crystal filter (L71-0207-05)
YG-455CN**



BAND	VCO MHz
1.5	10.33 ~ 10.83
3.5	12.33 ~ 12.83
7	15.33 ~ 16.33
10	18.33 ~ 19.33
14	22.33 ~ 23.33
18	26.33 ~ 27.33
21	29.33 ~ 30.33
24.5	33.33 ~ 33.83
28	36.33 ~ 37.33
28.5	37.33 ~ 37.83
29	37.33 ~ 38.33
29.5	38.33 ~ 38.83

Fig. 7 Frequency configuration

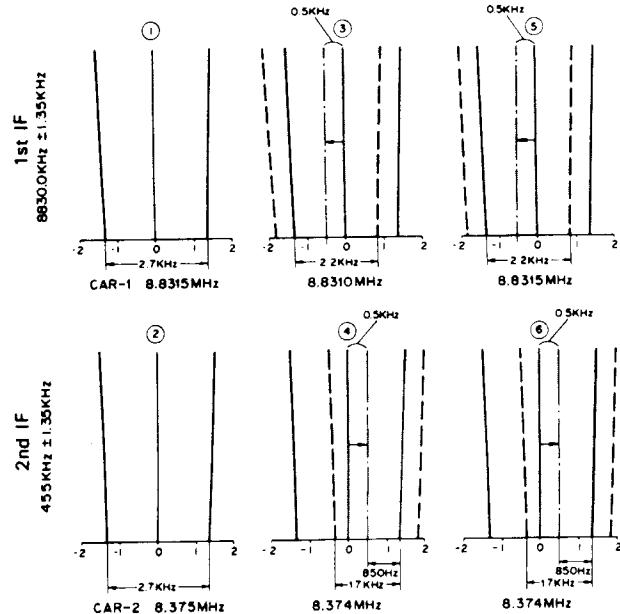


Fig. 8 VBT.IF SHIFT

2. VBT and IF SHIFT

VBT (variable bandwidth tuning) allows varying the bandwidth, operation of which is determined by the 8.83 MHz and 455 kHz filters and by changing the output frequencies of CAR-1 and CAR-2 simultaneously with the VBT control. Frequency organization of the VBT and IF SHIFT is shown in Figure 7. Assuming that a signal centered at 14.0015 MHz is received without IF shift or VBT, the VFO output frequency is 5.5015 MHz, CAR-1 8831.5 kHz, and the VCO output applied to the first mixer is 22.8315 MHz.

The mixer converts this to a signal whose center frequency is 8.830 MHz ($22.8315 \text{ MHz} - 14.0015 \text{ MHz} = 8.830 \text{ MHz}$). The signal frequency is then converted to 455 kHz by the second mixer. To help understand VBT operation, assume the composite bandwidth determined by filters XF1 and CF2 is 2.7 kHz in the normal (unshifted) state of CAR-1 and -2. Frequency organization to obtain a specific bandwidth and IF SHIFT is described below.

Example 1 (no IF shift)

[Bandwidth: 1.7 kHz]
IF SHIFT: 0 kHz

When the CAR-2 frequency is reduced by 1 kHz using the VBT control and a signal centered at 14.0015 MHz is received, the CAR-1 and -2 frequencies are as follows.

CAR-1 8831.0 kHz (= 8831.5 kHz - 0.5 kHz*)
CAR-2 8374.0 kHz (VBT control)

* The system is designed so that only half the amount of frequency shift applied to CAR-2 is applied to CAR-1.

Assume the received signal has a frequency spectrum extending over $14.0015 \text{ MHz} \pm 1.35 \text{ kHz}$. Since CAR-1 output is 8831.0 kHz, the VCO output frequency is 22.8310 MHz and, therefore, the frequency of the first IF signal is $8829.5 \text{ kHz} \pm 1.35 \text{ kHz}$ due to a shift of -500 Hz ($22.8310 \text{ kHz} - 14.0015 \text{ kHz} = 8829.5 \text{ kHz}$). Since the frequency characteristic of the first IF filter is $8.830 \text{ MHz} \pm 1.35 \text{ kHz}$ as noted above, frequency components are cut in the lower side band by 500 Hz more than in the normal state, as illustrated in Fig. 8 (3). The second intermediate frequency generated in the second mixer is $455.5 \text{ kHz} \pm 1.35 \text{ kHz}$.

(8829.5 kHz (No. 1 IF) – 8374.0 kHz (CAR-2) = 455.5 kHz)
Thus the second IF signal is shifted by +500 Hz, and as a result the upper-side frequency components are cut by 500 Hz, as illustrated in (4). The frequency spectrum of the signal which has passed the second IF stage is 455.5 kHz ± 850 Hz and the bandwidth is 1.7 kHz. If we convert the signal frequency to an equivalent one at the ANT input, we obtain 14.0015 MHz ± 850 Hz. In this case IF SHIFT operation is not performed

Example 2

Bandwidth: 1.7 kHz
IF SHIFT: 500 Hz (positive shift)

When the frequency of CAR-2 is lowered by 1 kHz with the VBT control and that of CAR-1 raised by 500 Hz with the SHIFT control, the resulting frequencies of CAR-2 and -1 are:

CAR-1 8831.5 kHz (= 8831.5 kHz – 0.5 kHz * 1 + 0.5 kHz * 2)

CAR-2 8374.0 kHz

*1 Half the amount of frequency varied by VBT

*2 The amount of frequency rise with IF SHIFT

Let us examine the frequency spectrum of the ANT input signal from the second IF component in Example 1.

You will recall that the center frequency of the second IF signal component is 455.5 kHz and the upper limit 455.5 kHz + 850 Hz.

The signal is 455.5 kHz + 8374.0 kHz = 8829.5 kHz in the first IF and the lower limit 8829.5 kHz – 850 Hz. As a result, signal components which have passed an 8830 kHz filter and a 455 kHz filter are the same as in Example 1. But the VCO output is 22.8315 MHz because CAR-1 which was 8831.0 kHz in Example 1 is 8831.5 kHz in Example 2. Since the converted equivalent frequency range at the first IF is 8829.5 kHz ± 850 Hz, its equivalent at the ANT input is 14.002 MHz ± 850 Hz.

(22.8315 MHz – 8.8295 MHz = 14.002 MHz)

This means that a signal 1.7 kHz in bandwidth is received with a shift of +500 Hz. In other words, the filter characteristics have been changed appropriately.

As you may have noted in Examples 1 and 2, the VBT and IF SHIFT controls operate separately. Therefore, it is possible to control the bandwidth alone while keeping the IF SHIFT unchanged, or control IF SHIFT while keeping the bandwidth unchanged.

3. NOTCH [in IF unit (X48-1290-00)]

This is a bridged-T filter consisting of L, C, and R components. The notch is provided in the 455 kHz IF. Normally, the width of the null would be broad at 455 kHz. Actually a sharp notch is provided by adding an active circuit which applies positive feedback to raise the Q. Q5 and Q6 (2SC1815Y) are a Q-multiplier. Q7 (2SC1815Y) is a buffer amplifier.

4. Speech processor [in IF unit (X48-1290-00)]

This speech processor is an RF clipper. The receiver uses two intermediate frequencies and two filters for VBT. In the transmitter, an SSB signal is generated at 455 kHz, is converted to 8830 kHz, and passed through an SSB 8.83 MHz filter after frequency conversion. This configuration is con-

venient for installing RF clippers between the stages. The 455 kHz SSB signal is clipped and then converted to an 8.83 MHz signal, and then routed through an 8.83 MHz SSB filter to remove splatter components generated during clipping. Q24 (2SC1815Y) is a processor amplifier, Q26 (TA7302P) a limiting amplifier, Q27 (3SK73GR) a control amplifier, and Q25 (2SC1815Y) and Q37 (2SA1015Y) compose a compression meter amplifier. The compression meter reads the mean compression level.

5. Final-stage RF NFB

Negative feedback is applied to the driver from the final output stage via C6, a 3PF, 3KV capacitor to reduce intermodulation distortion.

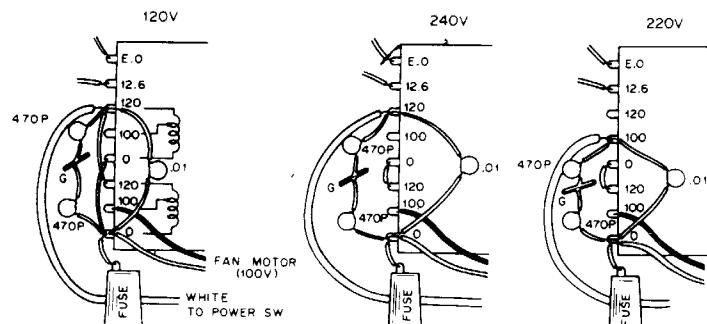
6. RIT/XIT Operations

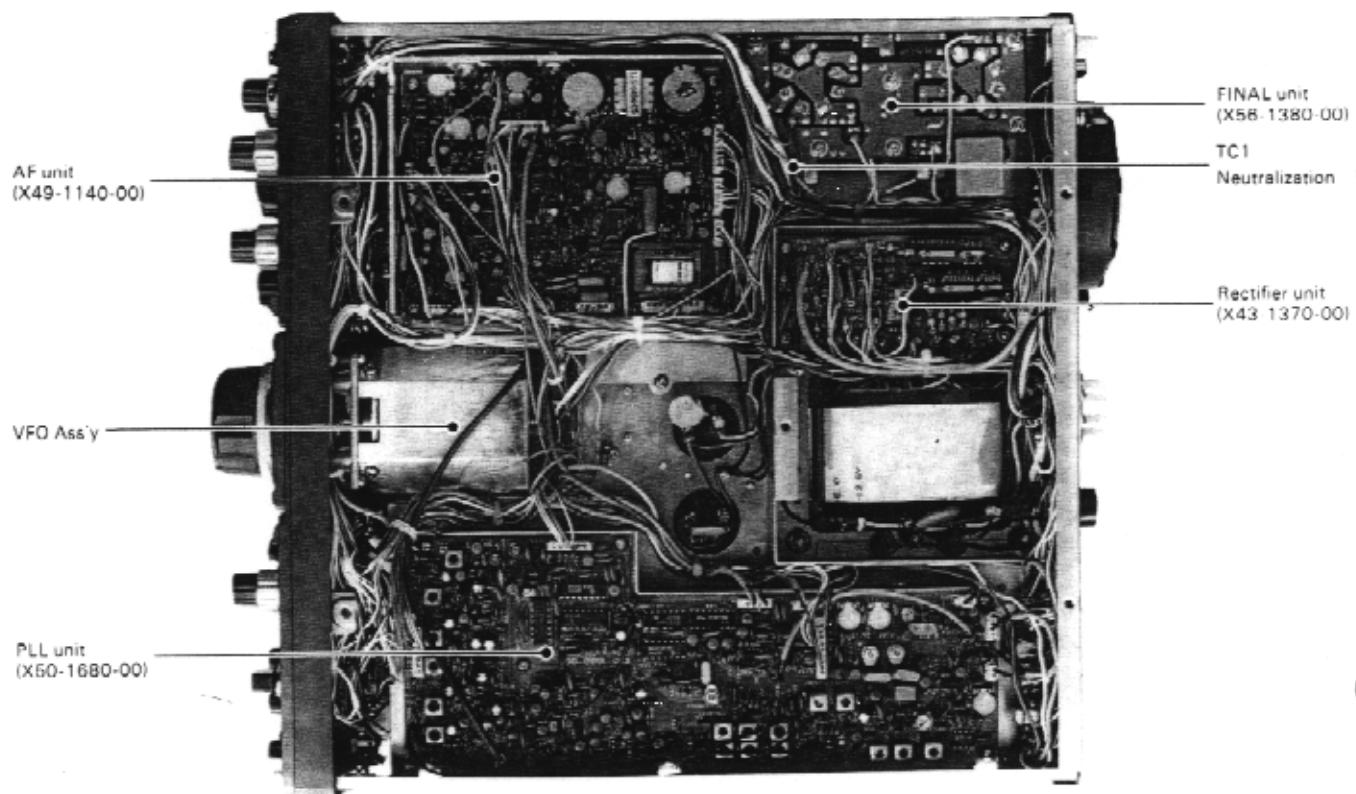
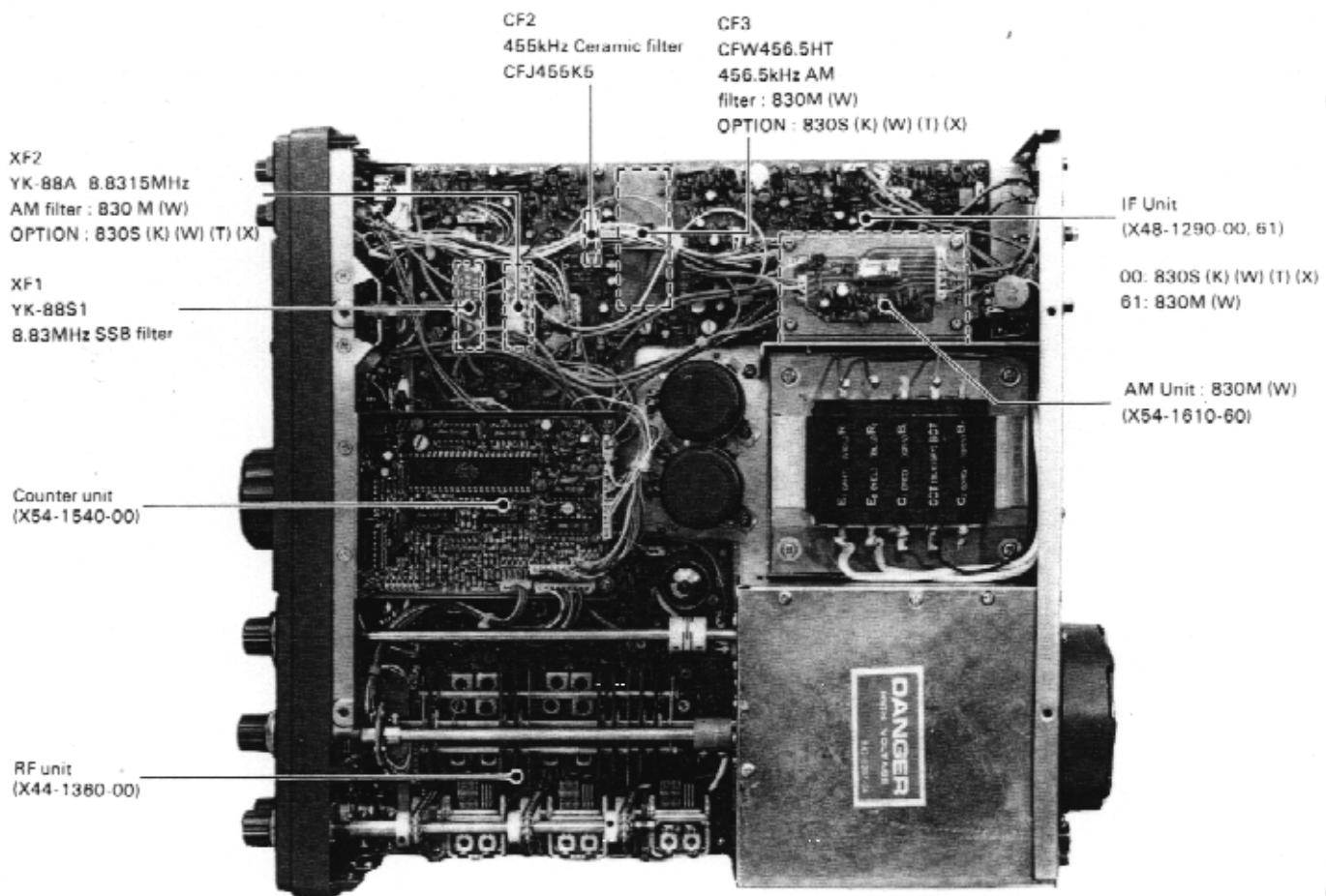
In addition to the conventional RIT, the transmission frequency can be varied with the XIT control.

TS-830S (K) AC Voltage conversion

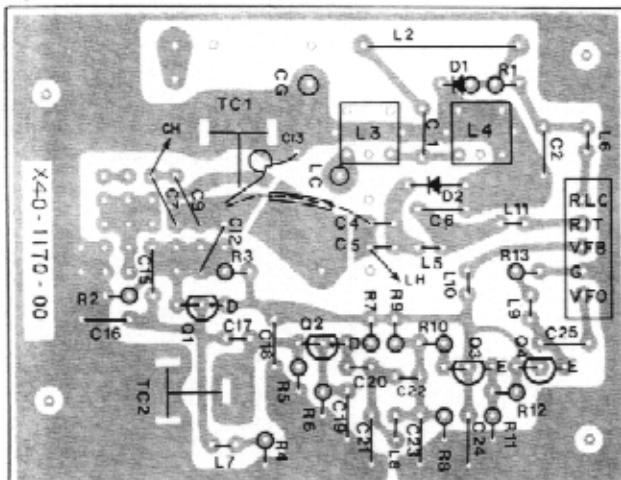
To operate the TS-830S (K) on 240V AC, the power transformer split primaries must be rewired from parallel to series connection.

1. Unplug the AC power cable.
2. Remove the bottom cover.
3. Remove the jumper wires between the two ϕ terminals and two 120 terminals on the bottom of the power transformer.
4. Connect the adjacent 120 and ϕ terminals at the middle of the transformer. This will provide 240V AC operation. For 220V AC operation, change the wires from 120 to 100 winding.
5. Change the AC fuse from 6A to 4A. Tag the power cord at the back of the radio to indicate that the transformer is strapped for 240V AC, and the power fuse should be 4A, and not 6A.
6. Replace the bottom cover and reconnect power to verify your work.





▼ VFO UNIT (X40-1170-00)



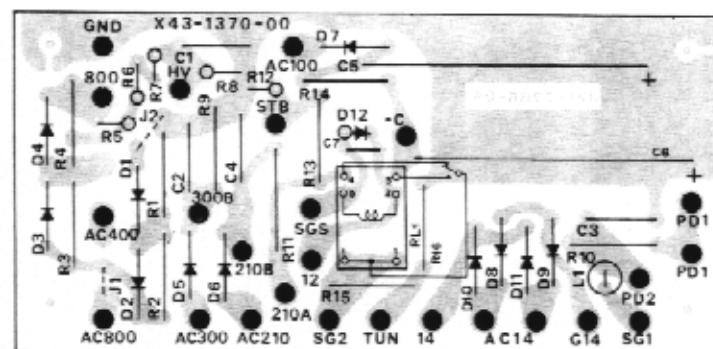
2SK19 (Y)

2SC480 (B)

2SC1959 (Y)



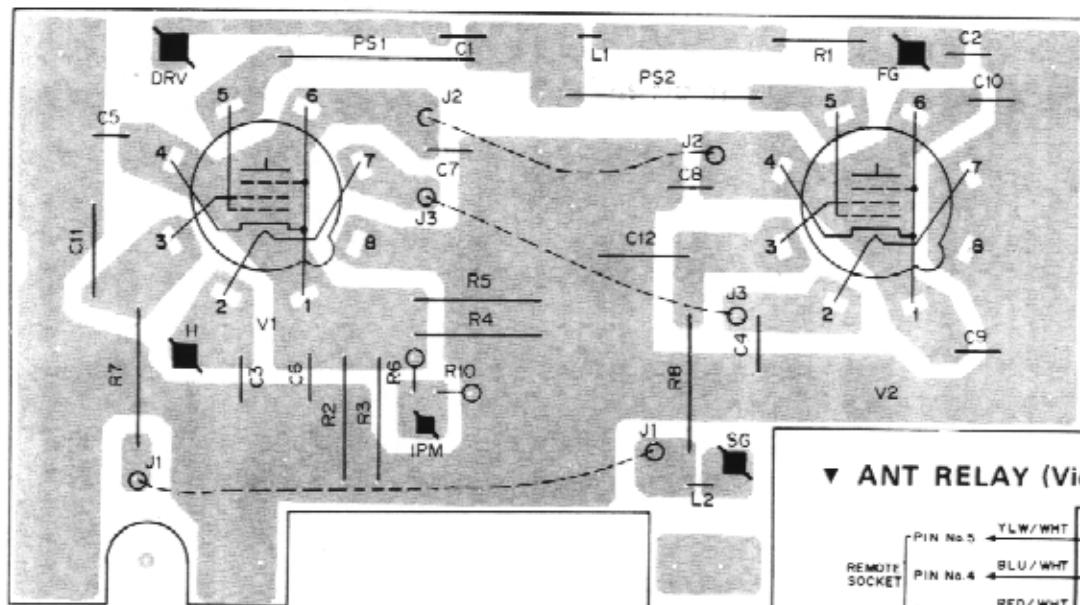
▼ RECTIFIER UNIT (X43-1370-00)



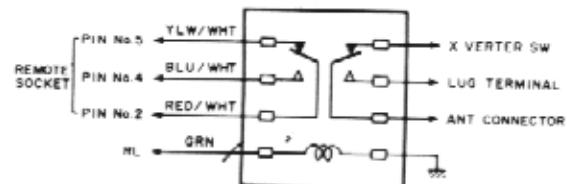
D1~6 V08J D7 V08E DB~11 V03C D12 1S1555

Q1.2 2SK19(Y) Q3 2SC480(B) or 2SC1875(L) Q4 2SC1959(Y)
D1 1S2508 D2 1SV53A

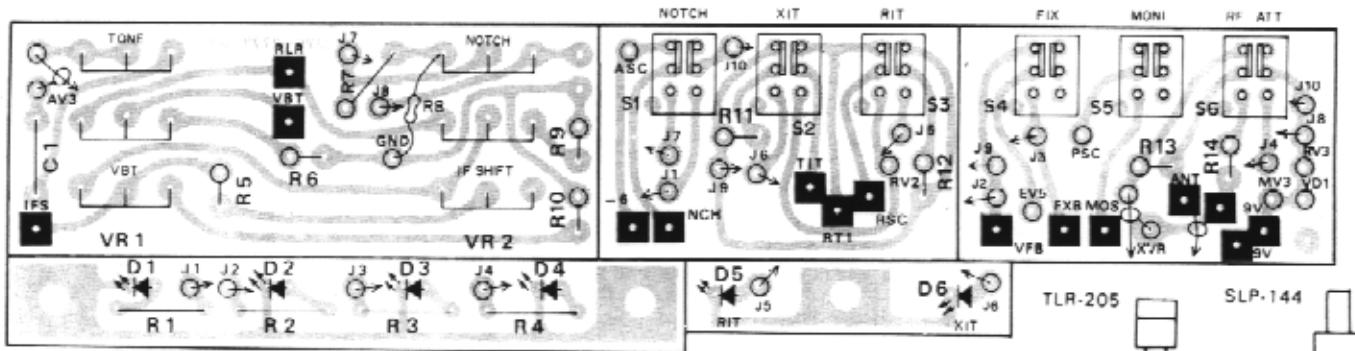
▼ FINAL UNIT (X56-1380-00) (View from foil side)



▼ ANT RELAY (View from terminal side)



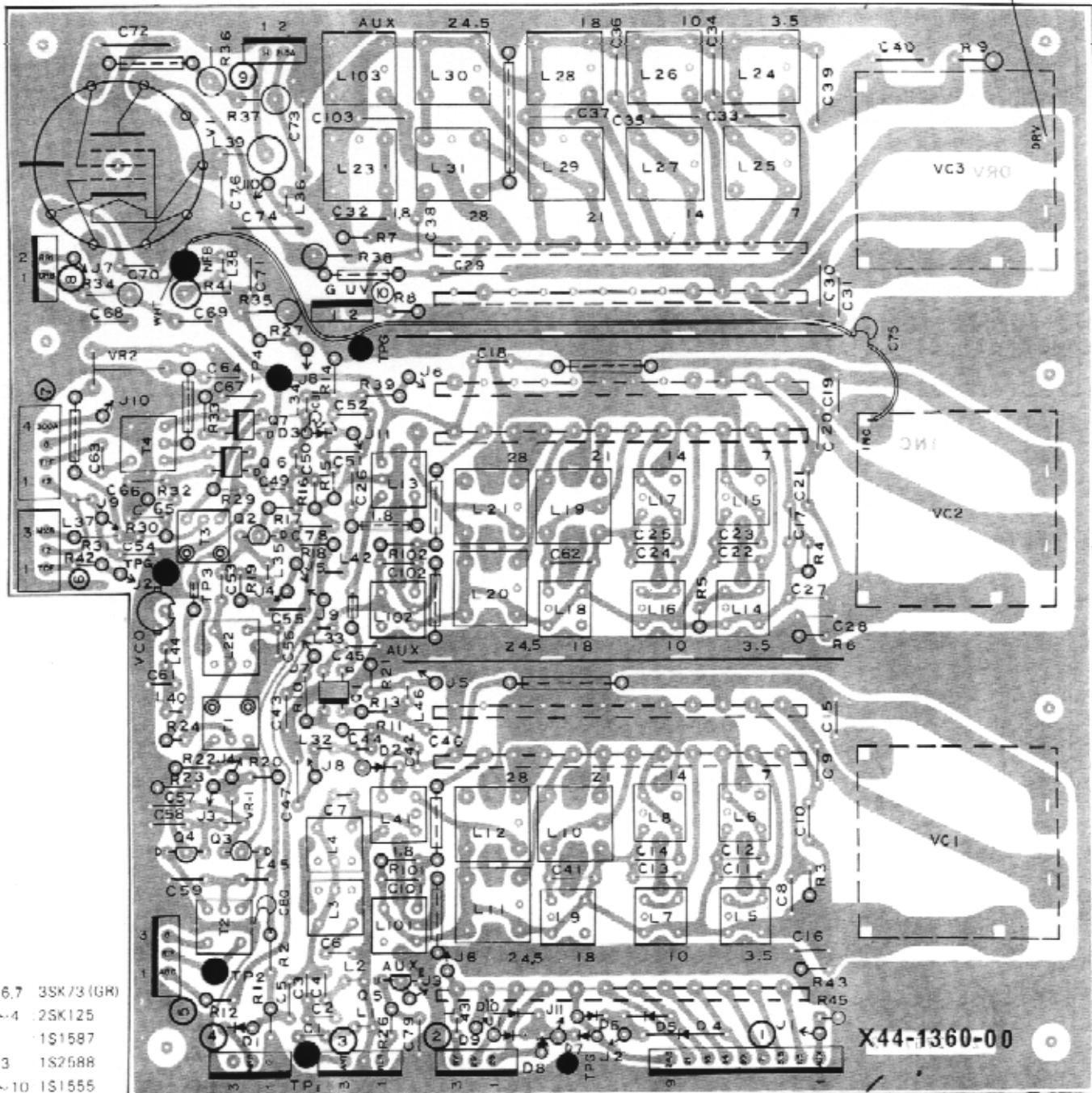
▼ SWITCH BOARD (J25-2773-03)



D1~4 TLR-205 DH-6 SLP-144

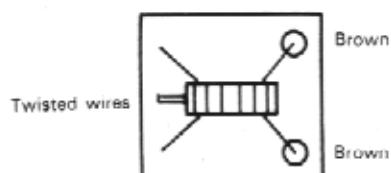
TS-830S, M PC BOARD VIEWS

▼ RF UNIT (X44-1360-00)

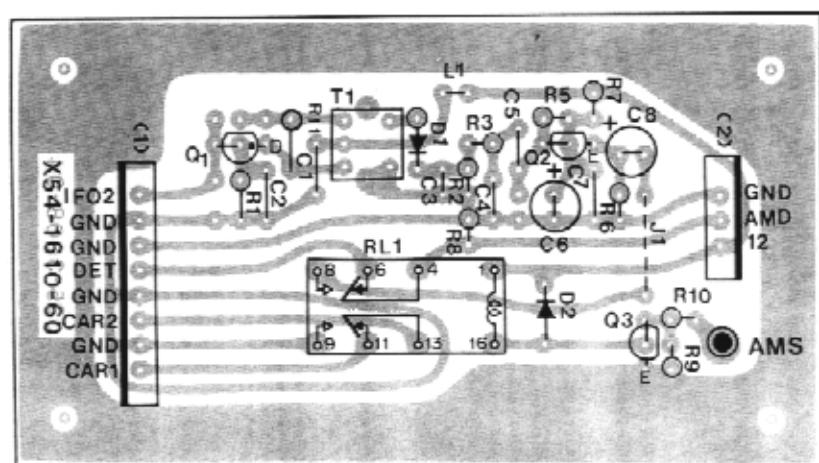


▼ AM UNIT (X54-1610-60): TS-830 M (W)

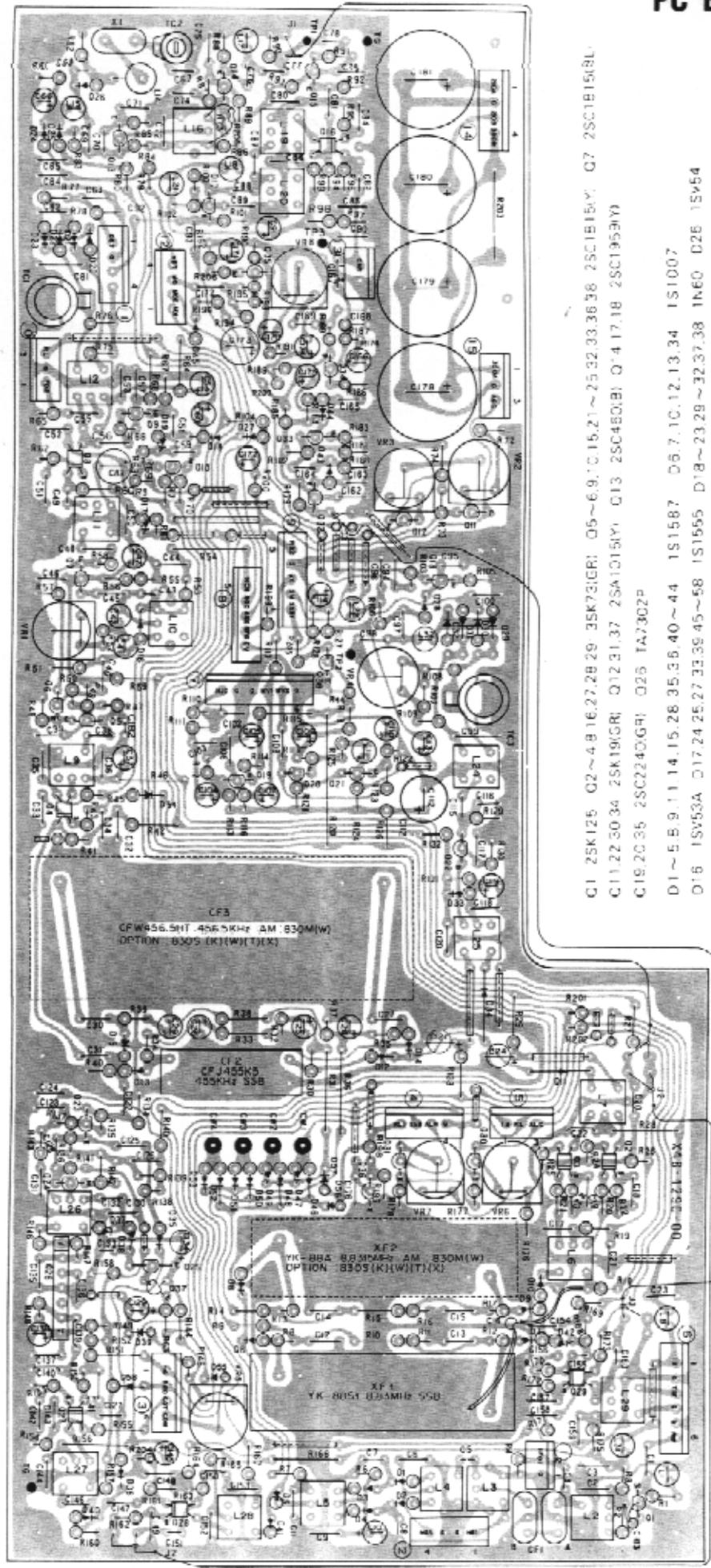
<Attachment method of T1, T2>



Q1 2SK19 (GR) Q2, 3 2SC945 (Q)
D1: 1N60 D2 : 1S1555

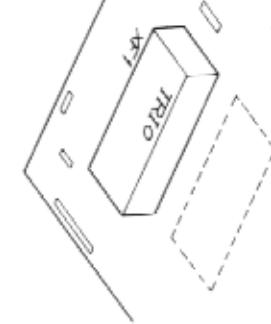


▼ IF UNIT (X48-1290-00) : 830S (K) (W) (T) (X)
 (X48-1290-61) : 830M (W)

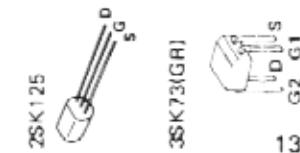


C1 2SK125 Q2~4.816.27.28.29 35K73(GR) Q5~6.9.10.15.21~25.32.33.36.38 2SC1815(Y) Q7 2SC1815(BL)
 C11 2SK19(GR) Q12.23.37 2SA1015(Y) Q13 2SC450(B) Q-4.17.18 2SC1959(Y)
 C18 2SK35 2SC2240(GR) Q26 TA7302P
 D1 ~ 5.8.9.11.14.15.26.35.36.40 ~ 44 1S1587 06.7.10.12.13.34 1S1007
 D1' 6 1SV53A Q17.24.25.27.33.39.45~58 1S1565 D1'8~23.29~32.37.38 1NE0 D26 15v54

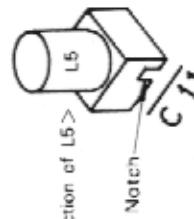
<Attachment direction of X#1>

2SA1015(Y)
2SC1815(Y),(BL)
2SC1959(Y)
2SC2240(GR)2SK125
2SC450(B)
TA7302P

2SK19(GR)



<Attachment method of CF1>
 Attach them so that the lettered sides face each other. [For CF1 supplied as a pair attach 1 to side A and 2 to side B.]

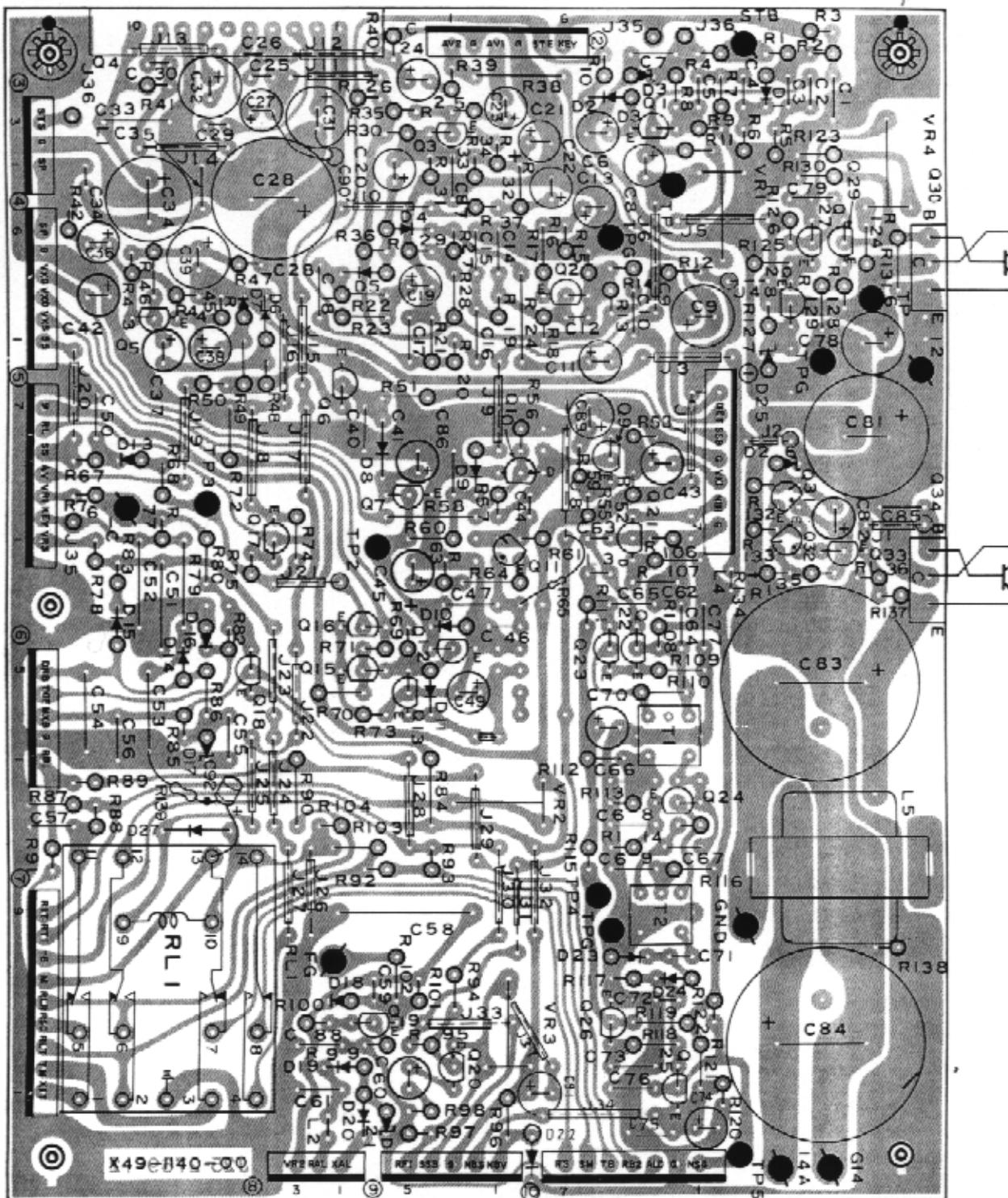


<Attachment direction of L5>
 Match _____ 5mm



TS-830S, M PC BOARD VIEW

▼ AF UNIT (X49-1140-00)



Q1,5,6,7,11,12,16,18,26~29,31,32 2SC945(Q) or 2SC1815(Y) Q2,3 2SC2240(GRI) Q4 HA1368R Q9,13,33 2SA1015(Y) Q10 2SK30A(GR)
 Q15,25 2SC1815(GR) Q17 2SA582(Y) Q19 2SC1515(K) Q20 2SK30A(O) Q21 2SK19(GRI) Q22~24 2SC460(BI) Q30,34 2SA473(Y)
 Q1,2,4~10,13,16,18,27 1S1555 D3,17,21 WZ-090 D11,23,24 IN60 D14,15,19,20 V068 D22 MV 13 D25 WZ 081 D26 XZ-090

2SA1015(Y)

2SC945(Q)

2SC1515(K)

2SC1815(GR), (Y)

2SC1959(Y)

2SC2240(GR)

2SA582(Y)

2SC460(B)

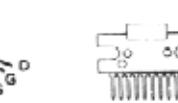
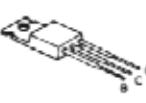
2SA473(Y)

2SK19(GR)

2SK30A(GR), (O)

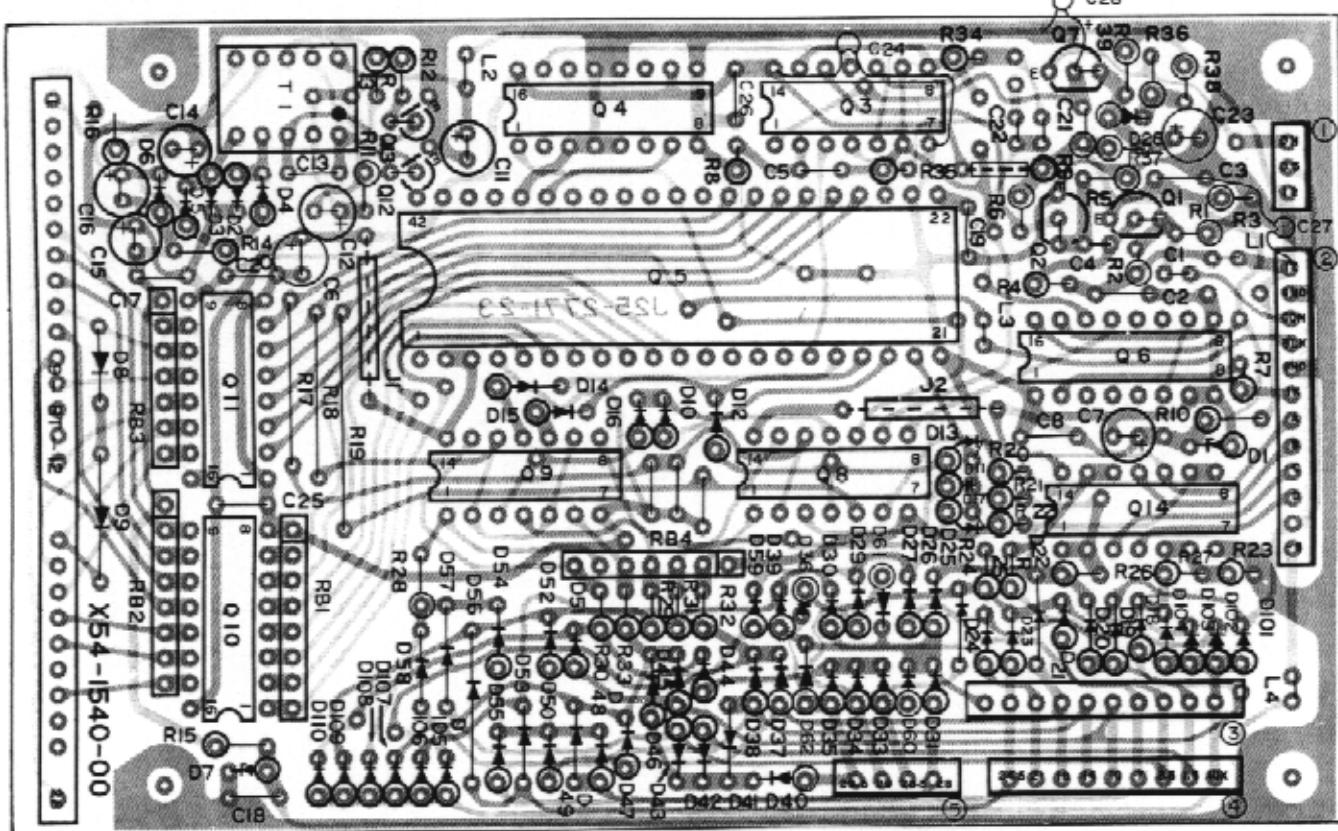
HA1368R

MV-13

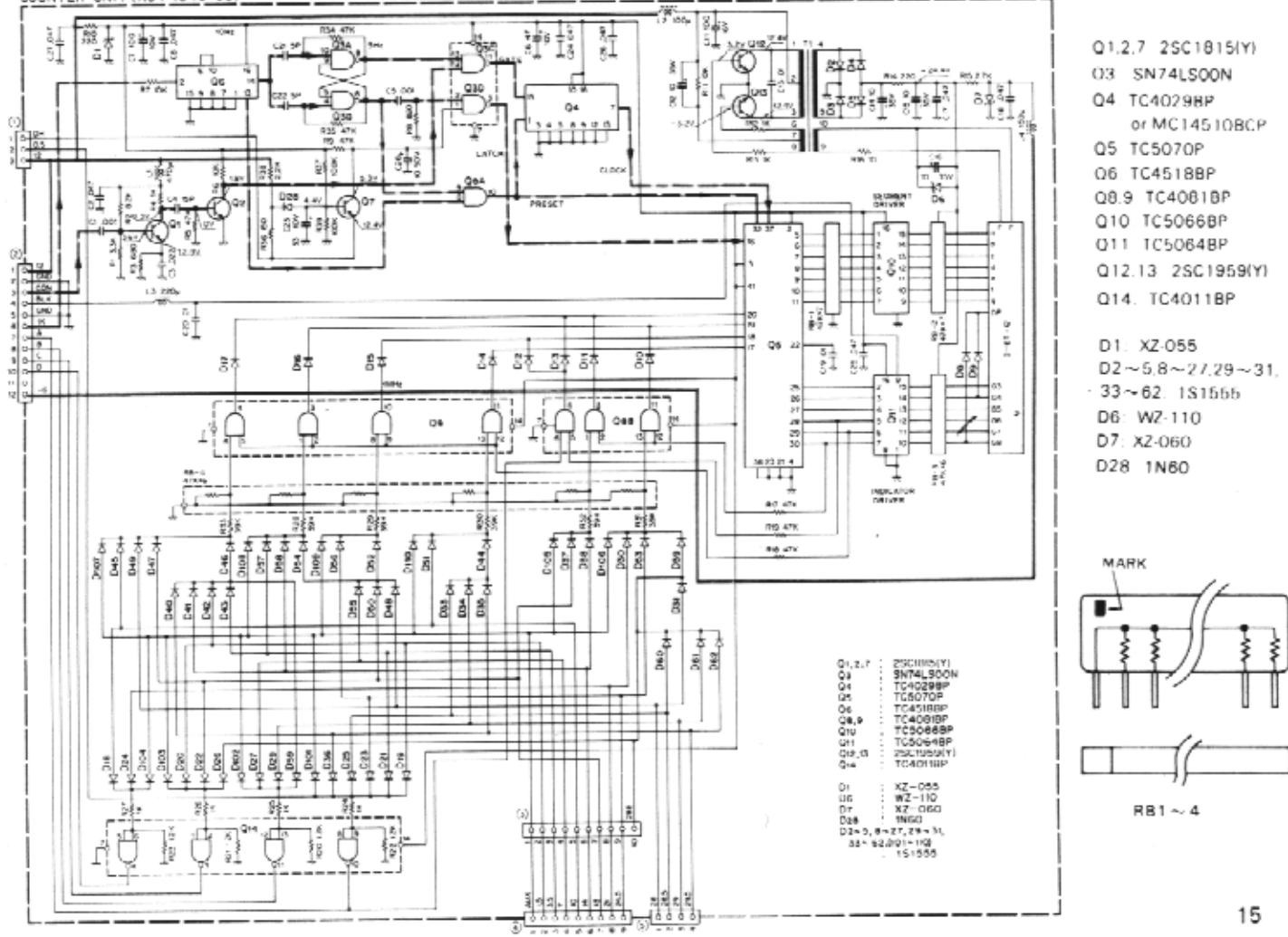


GREEN

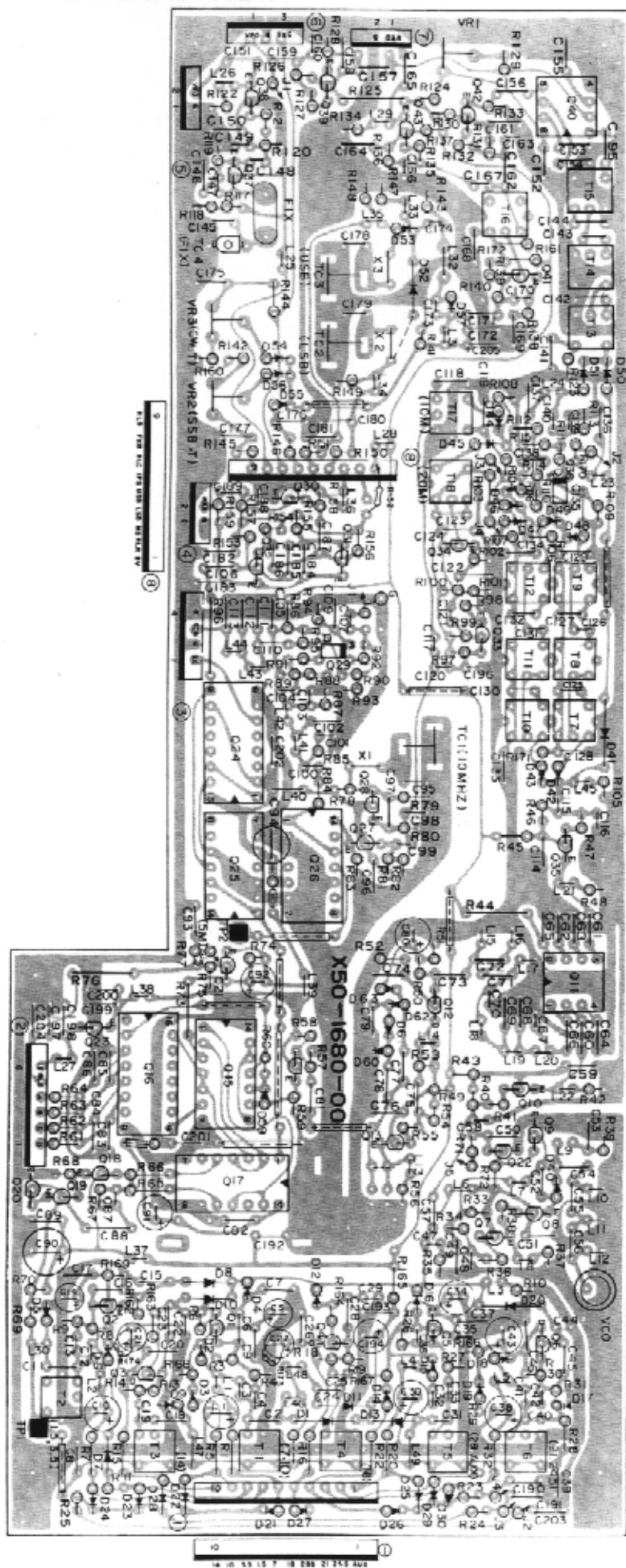
▼ COUNTER UNIT (X54-1540-00)



COUNTER UNIT (X54-1540-00)



TS-830S, M PC BOARD VIEW

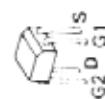
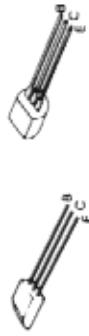


2SC1815(BL1,1W)
2SC1923(10)
2SC1959(Y)

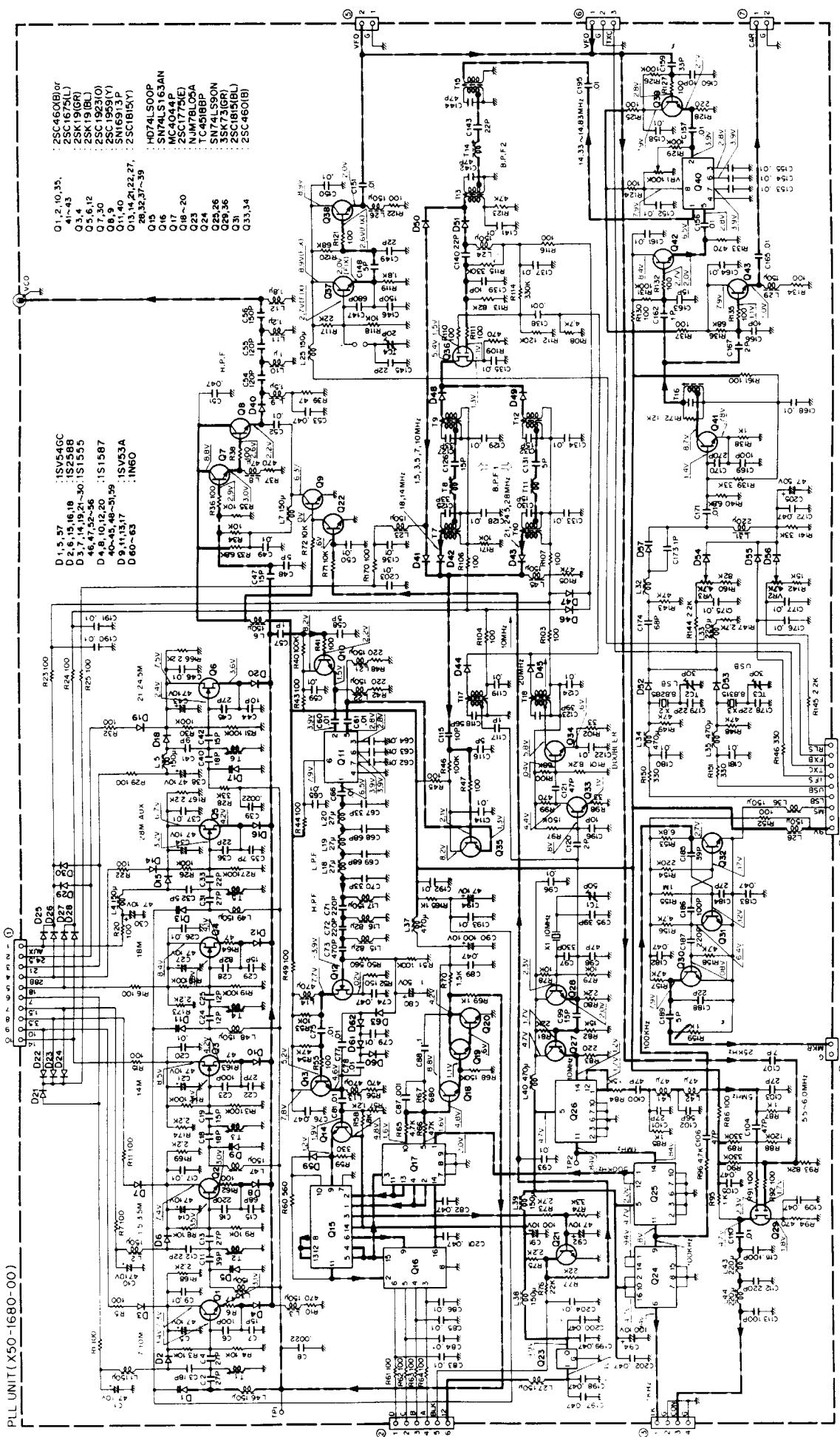
2SC460(B)

Q1 2.1C 35.4~43 2SC460(B) or 2SC1676L Q3 4 2SK1916L Q5 6 12 2SK1916L Q7.30 2SC1523(O) Q8.9 2SC1959(Y)
Q11 4.0 SN16913P
Q13.14.21.22.27.28.32.37~39 2SC1815(Y) Q15 HC74LS0P or SN74LS0N Q16 74LS163N or SN74-S163AN or HD74LS163P

Q17 MC4044P G16~2C 2SC1725
Q23 NLM7B05A Q24 1C45,BEP Q29.36 35V73(G) Q25.25 SN74-S90N or HD74-S90P Q31 2SC181513L Q33.34 2SC45C3
C1 5.57 1SV34GC D2.6.15.18.15 1S2588 D3.7.14.19.21~3C 46.47.52~56 1S.55E D4.8.1C.12.20.40~45.48~51.59 1S1587
D9.11.13.17.15.9.3A D60~63 1N50



▼ PLL UNIT (X50-1680-00)



PARTS LIST

Note 1:
K USA T Britain W Europe X Australia

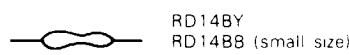
Note 2:
Only special type of resistors (example cement, metal film, etc) and capacitors (example electrolytic, tantalum, mylar, temp. coeff. capacitors) are detailed in the PARTS LIST. For the value of all common type components, refer to the schematic diagram of the PC board illustration. Resistors not otherwise detailed are carbon type (1/4W or 1/8W). Order carbon resistors and capacitors according to the following example.

A carbon resistor's part number is RD14BY 2E222J

A ceramic capacitor's number is CK45F1H103Z CC45TH1H220J

RESISTOR

1. Type of the carbon resistor



RD14CY
RD14CB (small size)

2. Wattage

1W → 3A 3W → 3F 5W → 3H
2W → 3D 4W → 3G

3' = CC45 ○ ○ ...

Ceramic capacitor (type I) temperature coeff. capacitor 1' 3'

1st word (Color)	C (Black)	L (Red)	P (Orange)	R (Yellow)	S (Green)	T (Blue)	U (Violet)
ppm/°C	0	-80	-150	-220	-330	-470	-750

3 = CK45 ○

Ceramic capacitor (type II) 3

Cord	B	D	E	F
Operating temperature °C	-30 +85	-30 +85	-30 +85	-10 +70

6 = Tolerance

Cord	C	D	G	J	K	M	X	Z	P	No cord
(%)	±0.25	±0.5	±2	±5	±10	±20	+40 -20	+80 -20	+100 -0	More than 10 μF -10 ~ +50 Less than 4.7 μF -10 ~ +75

Less than 10 pF

Cord	B	C	D	F	G
(pF)	±0.1	±0.25	±0.5	±1	±2

Abbreviation		Abbreviation	
Cap. C E MC	Capacitor Ceramic Electrolytic Mica	ML S T	Mylar Styren Tantalum

3. Resistance value

② ② → means $22 \times 10^2 = 2200\Omega$ (2.2 kΩ)

Example 221 → 220Ω 223 → 22 kΩ 225 → 2.2 MΩ
222 → 2.2 kΩ 224 → 220 kΩ

4. Tolerance

J = ±5% (Gold) K = ±10% (Silver)

CAPACITORS**Type I**

CC	45	TH	1H	220	J	CK	45	F	1H	103	Z
1'	2	3'	4	5	6	1	2	3	4	5	6

1 = Type ... ceramic, electrolytic, etc 4 = Voltage rating

2 = Shape ... round, square, etc 5 = Value

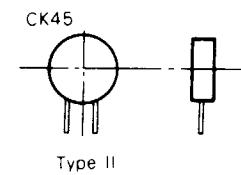
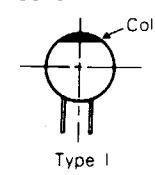
3 = Temp range 6 = Tolerance

3' = Temp coefficient

Ex CC45TH = $-470 \pm 60 \text{ ppm}/^\circ\text{C}$

2nd Word	G	H	J	K	L
ppm/°C	±30	±60	±120	±250	±500

CC45



5 = Capacitor value

Example: 010 → 1 pF
100 → 10 pF
101 → 100 pF
102 → 1000 pF = 0.001 μF
103 → 0.01 μF

TS-830 Semiconductor, Tube

☆ New Parts

Item	Name	Parts No.	Re-marks
Diode	1N60	V11-0051-05	
	1S1007	V11-4160-66	
	1S1555	V11-0076-05	
	1S1587	V11-0370-05	
	1S2588	V11-0414-05	
	V03C	V11-0290-05 200V 1.3A	
	V06B	V11-0219-05 100V 1.1A	
	V06E	V11-0285-05 400V 1.1A	
	V08J	V11-0282-05 800V 1.1A	
Varistor	MV-13	V21-0004-05	
Vari-cap diode	1SV53A	V11-4161-36	
	1SV54GC	V11-4173-46	

Item	Name	Parts No.	Re-marks
Zener diode	WZ-061	V11-0243-05	
	WZ-071	V11-4160-86	
	WZ-090	V11-0240-05	
	XZ-055	V11-4105-50	
	XZ-060	V11-4101-20	
	XZ-090	V11-4167-06	
LED	SLP-144	V11-6172-76	
	TLR-205	V11-3162-96	
Display Tube	9-BT-12	V40-7760-86	
	6146B	V40-0138-00	
	12BY7A	V40-0114-00	☆
TR	2SA473 (Y)	V01-0473-06	
	2SA562 (Y)	V01-0032-05	
	2SA1015 (Y)	V01-1015-06	

PARTS LIST

TS-830S, M

Item	Name	Parts No	Re-marks	Ref. No	Parts No	Description	Re-marks
FET	2SC460 (B)	V03-0079-05			B43-0654-04	Name plate (T)	☆
	2SC945 (Q)	V03-0293-05			B43-0656-04	Name plate (K)(W)(X)	☆
	2SC1515 (K)	V03-0450-05			B43-0657-04	Name plate M (W)	☆
	2SC1675 (L)	V03-1675-10			B46-0058-10	Warranty card (K)	☆
	2SC1775 (E)	V03-1775-06			B50-2738-00	Operating manual (K)	☆
	2SC1815 (BL)	V03-1815-26			B50-2739-00	Operating manual (T)	☆
	2SC1815 (GR)	V03-1815-16			B50-2740-00	Operating manual (W)(X)	☆
	2SC1815 (Y)	V03-1815-06			B58-0625-00	Transmit warning paper	☆
	2SC1923 (O)	V03-1923-06					
	2SC1959 (Y)	V03-1959-06			C01-0084-05	Variable cap VC2 Load	
	2SC2240 (GR)	V03-2240-06			C03-0002-05	Trimmer TC1	
					C03-0060-05	Variable cap VC1 Final	
IC	2SK19 (BL)	V09-0013-05		C1	CQ92M1H104K	ML 0.1μF 50V	
	2SK19 (GR)	V09-0012-05		C6	C91-0016-05	C 3pF 3kV	
	2SK19 (Y)	V09-0011-05		C7	C90-0186-05	C 0.001μF 3kV	
	2SK30A (GR)	V09-0060-05		C8	C91-0079-05	C 0.01μF 2kV	
	2SK30A (O)	V09-0056-05		C9	C91-0401-05	C 100pF 3kV	
	2SK125	V09-0136-10		C10	C91-0017-05	C 390pF 3kV	
	3SK73 (GR)	V09-1002-46		C12	CC45SL2H101J	C 100pF 500V	
	74LS163N	V30-1037-06		C13	CC45SL2H271J	C 270pF 500V	
	HA1368R	V30-1129-06		C14	CC45SL2H681J	C 680pF 500V	
	HD74LS00P	V30-0192-16		C15	CC45SL2H121J	C 120pF 500V	
	HD74LS163P	V30-1047-06		C16	CC45SL2H821J	C 820pF 500V	
	HD74LS90P	V30-1083-06		C17	CC45SL2H102J	C 1000pF 500V	
	MC14510BCP	V30-1227-16		C18.19	C90-0300-05	C 470pF AC150V	
	MC4044P	V30-0173-05		C20	C91-0079-05	C 0.01μF 2kV	
	NJM78L05A	V30-1020-16		C22.24	C90-0327-05	E 100μF 500V	
	SN16913P	V30-1048-06		C26	C91-0467-05	C 22pF 3kV	
	SN74LS00N	V30-0301-30		C30	CC45SL1H030C	C 3pF ±0.25pF	
	SN74LS163AN	V30-1154-06		C31	CC45SL2H101J	C 100PF 500V	
	SN74LS90N	V30-1005-26			D22-0402-05	Universal coupling, Band Load	
	TA7302P	V30-1134-06			D22-0405-04	Coupling, PLATE	
	TC4011BP	V30-0301-70			D22-0407-04	Shaft coupling, φ6	
	TC4029BP	V30-1051-06			D32-0051-04	Shaft stopper, M3 × 10	
	TC4081BP	V30-0299-10			D40-0206-05	Fan ass'y	
	TC4518BP	V30-1039-06			D40-0613-05	Vernier mechanism	
	TC5064BP	V30-1056-06			E04-0152-05	UHF type receptacle, ANT	
	TC5066BP	V30-1057-06			E06-0451-15	4P male socket, MIC	
	TC5070P	V30-1172-06			E06-0751-05	7P DIN socket, REMOTE	
					E06-0851-05	8P DIN socket, EXT VFO, XVTR	
					E07-0751-05	7P DIN plug	
					E08-0204-05	2P connector	
					E08-1202-05	12P connector (T)(W)(M)	
					E08-1207-05	12P plug	
					E09-0204-05	2P plug	
					E11-0404-05	3P Phone jack, Key, Phone	
					E11-0410-05	Phone jack, Ext SP	
					E12-0001-05	Phone plug, Ext. SP	
					E13-0205-05	2P Pin jack, IF OUT 1, 2	
					E20-0512-05	5P terminal	
					E22-0207-05	Lug plate	
					E22-0215-05	Lug plate	
					E23-0046-04	Square terminal	
					E29-0417-04	Gnd plate	
					E30-0181-05	AC cord (K)	
					E30-0185-05	AC cord (X)	
					E30-0585-05	AC cord (W)	
					E30-0602-05	AC cord (T)	
					E90-0004-15	Plate cap	
					E31-2086-05	Coax cable with plug	

TS-830 GENERAL

M(W): TS-830M (W) type

Ref. No.	Parts No	Description	Re-marks
	A01-0778-11	Case (Upper)	☆
	A01-0779-01	Case (Lower)	☆
	A20-2388-13	Panel	☆
	B03-0519-04	Switch mask (A)	☆
	B03-0520-04	Switch mask (B)	☆
	B05-0708-04	Speaker grill cloth	
	B09-0003-05	Coupling	
	B10-0630-04	Front glass (large)	☆
	B10-0631-04	Front glass (small)	☆
	B21-0501-04	Pointer Plate	☆
	B30-0817-15	Pilot lamp Meter 14V, 80mA	☆
	B31-0627-05	Meter	☆
	B42-1691-04	Adj seal	☆

PARTS LIST

Ref. No.	Parts No.	Description	Re-marks
	F05-4022-05	Fuse, 4A (W)(T)(X)	
	F05-6021-05	Fuse, 6A (K)	
G01-0801-04		GND spring	
G09-0405-05		D spring, Mode	
G09-0410-05		D spring, RIT, Plate	
H01-2692-14		Carton case (inside)(K)(W)(X)	☆
H01-2695-14		Carton case (inside)(T)	☆
H03-1763-14		Carton case (outside)	☆
H10-1276-04		Accessory box	
H10-2539-02		Packing fixture (F)	☆
H10-2540-02		Packing fixture (R)	☆
H20-0439-03		Protective cover	
H25-0120-04		Protective bag	
J02-0022-05		Foot (small)	
J02-0049-14		Foot (large)	
J13-0407-15		Fuse holder	☆
J32-0159-04		Hex. boss	
J32-1030-14		Round boss	
J41-0006-05		Cord bush (K)	
J41-0024-15		Cord bush (W)(T)(X)	
J42-0409-04		Knob bush, +0.5 Shift	
K01-0402-25		Handle	
K21-0723-04		Knob, Band	
K23-0738-04		Knob 1, Meter, AGC	☆
K23-0739-04		Knob 2, COMP.GAIN.NB.DELAY	☆
K23-0740-04		Knob 3, MIC.VBT.IF Shift, AF	☆
K27-0406-04		Push knob, DH	
K27-0421-04		Push knob	
K29-0713-04		Push knob, 0.5 Shift	
K29-0715-04		Knob, Mode	
K29-0736-14		Knob 4, Notch,CAR,RF,Tone	☆
K29-0737-04		Knob 5, Plate	☆
K29-0738-04		Knob 6, RIT,Drive,Load	☆
L01-8106-15		Power Trans (K)	☆
L01-8116-15		Power trans (W)(T)(X)	☆
L33-0259-05		Choke coil, 470μH, L5	
L33-0635-05		Final choke, L2	☆
L34-0560-25		Final coil A	
L34-1004-05		Final coil B, 28MHz	
L39-0046-05		PS coil, Plate	
L40-1511-03		Ferri-inductor, 150μH	
L40-6891-13		Choke coil, 6.8μH, L1	
N08-0505-04		Ornamental screw, DH	☆
N14-0517-05		Protective nut, BIAS	☆
N99-0306-04		Hex. head screw, VFO	
VR1	R24-3402-05	Pot, VBT-Tone	☆
VR2	R24-9401-05	Pot, IF Shift-Notch	☆
VR3	R19-3407-05	Pot, AF-RF	☆
VR4	RO1-2405-05	Pot, RIT/XIT, 5K (B)	☆
VR5	RO1-6401-05	Pot, Delay, 250K (B)	☆
VR6	RO1-3412-05	Pot, Vox Gain, 10K (C) S	☆
VR7	RO1-3410-05	Pot, Processor, 10K (A)	☆
VR8	RO1-0404-05	Pot, NB, 300Ω (B)	☆
VR9	R24-3401-05	Pot, MIC-CAR	☆
VR10~12	RO1-3411-05	Pot, Bias, RFOUT, 10K (B)	☆
R22	RC05GF2H221J	Solid 220Ω 1/2W	

Ref. No.	Parts No.	Description		Re-marks
R24	RC05GF3A103K	Solid	10kΩ	1W
R25.26	RC05GF2H474J	Solid	470kΩ	1/2W
R28	RC05GF2H150J	Solid	15Ω	1/2W
S1~6	S40-2419-05	Push switch	PC board type	☆
S7	S40-2403-05	Push switch	+ 0.5 SHIFT	
S8, 9	S59-2020-05	Seesaw switch	Power, Heater	
S11	S01-1423-05	Rotary switch	AGC	
S12	S40-2403-05	Push switch	DH	
S13	S01-1424-05	Rotary switch	Meter	☆
S15, 16	S40-2415-05	Push switch	Lead type	
S17	S44-1407-05	Paddle switch	STBY	☆
S18	S01-3405-15	Rotary switch	Mode	☆
S20	S31-2007-05	Slide switch	SG SW	
S22	S01-3406-05	Rotary switch	FINAL	☆
S23	S31-2027-05	Slide switch	AC Volt (W)(T)(X)	
RL1	S51-2409-05	Relay	MX-2P	☆
	T03-0027-15	Speaker		
	X40-1170-00	VFO unit		☆
	X43-1370-00	Rectifier unit		☆
	X44-1360-00	RF unit		☆
	X48-1290-00	IF unit		☆
	X48-1290-61	IF unit M (W)		☆
	X49-1140-00	AF-AVR unit		☆
	X50-1680-00	PLL unit		☆
	X54-1540-00	Counter unit		☆
	X54-1610-60	AM unit M (W)		☆
	X56-1380-00	Final unit (S)		☆

Rectifier unit (X43-1370-00)

Ref. No.	Parts No.	Description		Re-marks
C5, 6	CE02W2C330	E 33μF	160V	
	E23-0047-04	Square terminal		
	J31-0502-04	PC board collar		
	J42-0404-05	PC board bush		
L1	L40-1511-03	Ferri-inductor	150μH	
R1~4	RC05GF2H474J	Solid	470kΩ	1/2W
R9	RC05GF2H334J	Solid	330kΩ	1/2W
R10	RC05GF2H153J	Solid	15kΩ	1/2W
R11	RS14AB3A471J	Metal film	470Ω	1W
R13	RC05GF2H104J	Solid	100kΩ	1/2W
R14	RC05GF2H102J	Solid	1kΩ	1/2W
R15, 16	RC05GF2H563J	Solid	56kΩ	1/2W
RL1	S51-1404-05	Relay	G2E	

RF unit (X44-1360-00)

Ref. No.	Parts No.	Description		Re-marks
C1	CQ92M1H181K	ML 0.0015μF	50V	
C4	CQ92M1H151K	ML 0.0015μF	50V	
C6	CC45R1H101U	C 100pF		
C7	CQ09S1H122U	C 100pF		

Downloaded by

Amateur Radio Directory

PARTS LIST

TS-830S, M

Ref. No.	Parts No.	Description	Re-marks	Ref. No.	Parts No.	Description	Re-marks
C8	CC45RH1H221J	C 220pF			J31-0502-04	PC board collar	
C9	CC45RH1H330J	C 33pF			J42-0404-05	PC board bush	
C10	CC45RH1H121J	C 120pF		T1	L19-0303-05	Wide band trans	
C11	CC45RH1H560J	C 56pF		T2	L30-0509-05	IFT	
C12	CC45RH1H330J	C 33pF		T3	L19-0303-05	Wide band trans	
C13	CC45RH1H101J	C 100pF		T4	L30-0509-05	IFT	☆
C14. 15	CC45RH1H330J	C 33pF		L1	L40-2792-02	Ferri-inductor	2.7μH
C16	CC45SL1H561J	C 560pF		L2	L40-4791-02	Ferri-inductor	4.7μH
C17	CC45RH1H221J	C 220pF		L3	L34-0559-05	Trap coil	
C19	CC45RH1H100D	C 10pF ±0.5pF		L4	L34-0558-05	Trap coil	
C20	CC45RH1H330J	C 33pF		L5	L34-0930-05	Tuning coil	3.5MHz
C21	CC45RH1H121J	C 120pF		L6	L34-0931-05	Tuning coil	7MHz
C22	CC45RH1H560J	C 56pF		L7	L34-0932-05	Tuning coil	10MHz
C23	CC45RH1H330J	C 33pF		L8	L34-0933-05	Tuning coil	14MHz
C24	CC45RH1H101J	C 100pF		L9	L34-0934-05	Tuning coil	18MHz
C25	CC45RH1H330J	C 33pF		L10	L34-0990-05	Tuning coil	21MHz
C26	C91-0456-05	C 0.047μF		L11	L34-0935-05	Tuning coil	24.5 MHz
C28	CC45SL1H561J	C 560pF		L12	L34-0995-05	Tuning coil	28MHz
C29	CK45E2H103P	C 0.01μF 500V		L13	L34-0936-05	Tuning coil	MIX 1.5 MHz
C30	CC45RH2H330J	C 33pF 500V		L14	L34-0930-05	Tuning coil	3.5MHz
C31	CC45RH2H121J	C 120pF 500V		L15	L34-0931-05	Tuning coil	7MHz
C32	CC45RH2H181J	C 180pF 500V		L16	L34-0932-05	Tuning coil	10MHz
C33	CC45RH2H470J	C 47pF 500V		L17	L34-0933-05	Tuning coil	14MHz
C34	CC45RH2H270J	C 27pF 500V		L18	L34-0934-05	Tuning coil	18MHz
C35	CC45RH2H101J	C 100pF 500V		L19	L34-0990-05	Tuning coil	21MHz
C36	CC45RH2H390J	C 39pF 500V		L20	L34-0935-05	Tuning coil	24.5MHz
C37	CC45RH2H180J	C 18pF 500V		L21	L34-0995-05	Tuning coil	28MHz
C38	CC45SL2H100D	C 10pF ±0.5pF 500V		L22	L34-0559-05	Trap coil	
C39	CK45E2H103P	C 0.01μF 500V		L23	L34-0552-15	Tuning coil	1.5MHz
C40	CC45SL2H561J	C 560pF 500V		L24	L34-0553-15	Tuning coil	3.5MHz
C41	CC45RH1H150J	C 15pF		L25	L34-0554-05	Tuning coil	7MHz
C42	CC45SL1H101J	C 100pF		L26	L34-0937-05	Tuning coil	10MHz
C45. 46	C91-0456-05	C 0.047μF		L27	L34-0555-05	Tuning coil	14MHz
C49	CC45SL1H100D	C 10P ±0.5pF		L28	L34-0938-05	Tuning coil	18MHz
C50	CC45SL1H470J	C 47pF		L29	L34-0556-05	Tuning coil	21MHz
C55	C91-0456-05	C 0.047μF		L30	L34-0939-05	Tuning coil	24.5MHz
C56	CC45RH1H101J	C 100pF		L31	L34-0557-05	Tuning coil	28MHz
C57	C91-0456-05	C 0.047μF		L32~34	L40-4711-03	Ferri-inductor	470μH
C58	C91-0456-05	C 0.047μF		L35	L40-1511-03	Ferri-inductor	150μH
C61	CC45SL1H470J	C 47pF		L36	L40-4711-03	Ferri-inductor	470μH
C62	CC45RH1H150J	C 15pF		L37, 38	L40-1511-03	Ferri-inductor	150μH
C63	CC45SL1H151J	C 150pF		L39	L33-0074-05	Heater choke	0.3μH
C66	C91-0456-05	C 0.047μF		L40	L40-2282-01	Ferri-inductor	0.22μH
C67	C91-0456-05	C 0.047μF		L41	L34-2004-05	Tuning coil	ANT 1.5MHz
C68	CK45E2H222P	C 0.0022μF 500V		L42	L40-4711-03	Ferri-inductor	470μH
C69	CC45SL2H151J	C 150pF 500V		L43	L40-1511-03	Ferri-inductor	150μH
C70	CK45B2H102K	C 0.001μF 500V		L44	L40-2282-01	Ferri-inductor	0.22μH
C72~74	CK45E2H103P	C 0.01μF 500V		L45, 46	L40-1511-03	Ferri-inductor	150μH
C75	CC45CH2H680J	C 68pF 500V		VR1	R12-0416-05	Trim. pot	470Ω
C77	CC45CH2H010C	C 1pF ±0.25pF 500V		VR2	R12-6404-05	Trim. pot	470kΩ
C78	C91-0456-05	C 0.047μF		R34	RC05GF2H104J	Solid	100kΩ 1/2W
C81	CC45SL1H100D	C 10pF ±0.5pF		R35	RC05GF2H101J	Solid	100Ω 1/2W
VC1~3	C01-0127-15	Variable capacitor		R36	RC05GF2H104J	Solid	100kΩ 1/2W
	D13-0404-04	Sprocket, large	☆	R37	RS14AB3A332J	Metal film	3.3kΩ 1/2W
	D13-0405-04	Sprocket, small	☆	R38	RC05GF2H474J	Solid	470kΩ 1/2W
	D16-0403-04	Chain ass'y	☆	R41	RC05GF2H680J	Solid	68Ω 1/2W
	E04-0154-05	Coax connector		R92-0150-05	R92-0150-05	Short jumper	
	E10-1902-05	9P tube socket			S29-7401-05	Rotary wafer ass'y	
	F11-0249-05	Tube shield					☆

PARTS LIST

TS-830S, M

Ref No.	Parts No.	Description		Re-marks
L13	Not Used			
L14	L33-0636-05	Choke coil		
L15	L40-2211-03	Ferri-inductor 220 μ H		
L16	L32-0201-05	Oscillating coil		
L17	L40-3391-03	Ferri-inductor 3.3 μ H		
L18	L40-1511-03	Ferri-inductor 150 μ H		
L19, 20	L34-0540-05	Tuning coil 455kHz		
L21, 22	L40-1021-03	Ferri-inductor 1mH		
L23	Not Used			
L24	L34-0539-15	Tuning coil 455kHz BM		
L25	L34-0664-05	Tuning coil 455kHz		
L26, 27	L34-0540-05	Tuning coil 455kHz		
L28	L34-0536-05	Tuning coil 8.83MHz		
L29	L34-0946-05	Tuning coil 8.83MHz	☆	
L31	L40-1511-03	Ferri-inductor 150 μ H		
L32	L40-1021-03	Ferri-inductor 1mH		
CF1	L72-0310-05	Ceramic filter 8.83MHz NB		
CF2	L72-0314-15	Ceramic filter 455kHz SSB		
CF3	L72-0322-05	Ceramic filter 456.5 kHz AM M(W)	☆	
XF1	L71-0222-05	MCF 8.83MHz SSB YK-88S1	☆	
XF2	L71-0223-05	MCF 8.8315MHz AMYK-88AM(W)	☆	
X1	L77-0801-05	Crystal 8.375MHz		
VR1, 2	R12-3045-05	Trim. pot 10k Ω		
VR3	R12-6401-05	Trim. pot 470k Ω		
VR4	R12-0401-05	Trim. pot 100 Ω		
VR5	R12-1039-05	Trim. pot 2.2k Ω		
VR6	R12-3045-05	Trim. pot 10k Ω		
VR7	R12-6401-05	Trim. pot 470k Ω		
VR8	R12-3046-05	Trim. pot 47k Ω		
	R92-0150-05	Short jumper		
R203	RS14GB3D471J	Metal film 470 Ω 2W		

Ref. No.	Parts No.	Description		Re-marks
C37, 38	CE04W1A470Q	E 47 μ F 10V		
C39	CE04W1C470Q	E 47 μ F 16V		
C41	CE04W1H3R3	E 3.3 μ F 50V		
C42	CE04W1E100Q	E 100 μ F 25V		
C43	CE04W1H010	E 1 μ F 50V		
C44	CQ92M1H103K	ML 0.01 μ F		
C45	CE04W1E100Q	E 10 μ F 25V		
C46	CQ92M1H472K	ML 0.0047 μ F 50V		
C47	CQ92M1H473K	ML 0.047 μ F 50V		
C49	CE04W1H3R3	E 3.3 μ F 50V		
C51, 52	CK45E2H103P	C 0.01 μ F 500V		
C53, 54	CQ93M2A473K	ML 0.047 μ F 100V		
C58	CQ93M2A224M	ML 0.22 μ F 100V		
C60	CE04W1HR47	E 0.47 μ F 50V		
C67	CC45SL1H100D	C 10pF ±0.5pF		
C70	CE04W1H010	E 1 μ F 50V		
C74	CE04W1H010	E 1 μ F 50V		
C78	CE04W1C470Q	E 47 μ F 16V		
C81	CE04W1C102M	E 1000 μ F 16V		
C82	CE04W1H3R3	E 3.3 μ F 50V		
C83	CE04AW1HR22M	E 0.22 μ F 50V		
C84	C90-0807-05	E 2200 μ F 25V		
C89	CE04W1A470Q	E 47 μ F 10V		
C90	C91-0456-05	C 0.047 μ F		
C91	CE04W1E100Q	E 10 μ F 25V		
C92	CE04W1A330M	E 33 μ F 10V		
C48, 80	Not Used			
	E23-0047-04	Square terminal		
	F20-0516-05	Insulating sheet		☆
	F29-0014-05	Shoulder washer		☆
	J31-0502-04	PC board collar		
	J42-0404-05	PC board bush		
T1	L34-0535-05	Tuning coil NB Red		
T2	L34-0536-05	Tuning coil NB Blue		
L1	L40-3392-02	Ferri-inductor 3.3 μ H		
L2 ~ 4	L40-1511-03	Ferri-inductor 150 μ H		
L5	L15-0016-05	Choke trans		
VR1	R12-3411-05	Trim. Pot 47k Ω		
VR2	R12-3413-05	Trim. Pot 10k Ω		☆
VR3	R12-3411-05	Trim. Pot 47k Ω		
VR4	R12-0413-05	Trim. Pot 470 Ω		
	R92-0150-05	Short jumper		
RL1	S51-4401-05	Relay DC12V		

PLL unit (X50-1680-00)

Ref. No.	Parts No.	Description		Re-marks
C1	CE04W1A470Q	E 47 μ F 10V		
C2	CC45TH1H270J	C 27pF		
C3	CC45TH1H180J	C 18pF		
C4	CC45TH1H270J	C 27pF		
C5	CE04W1A470Q	E 47 μ F 10V		
C6	CC45UJ1H101J	C 100pF		
C7	CC45UJ1H150J	C 15pF		
C10	CE04W1A470Q	E 47 μ F 10V		
C11	CC45TH1H330J	C 33pF		
C12	CC45TH1H220J	C 22pF		
C13	CC45TH1H270J	C 27pF		

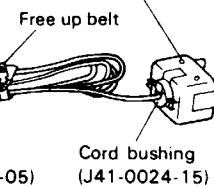
PARTS LIST/PACKING

Final unit (X56-1380-00)

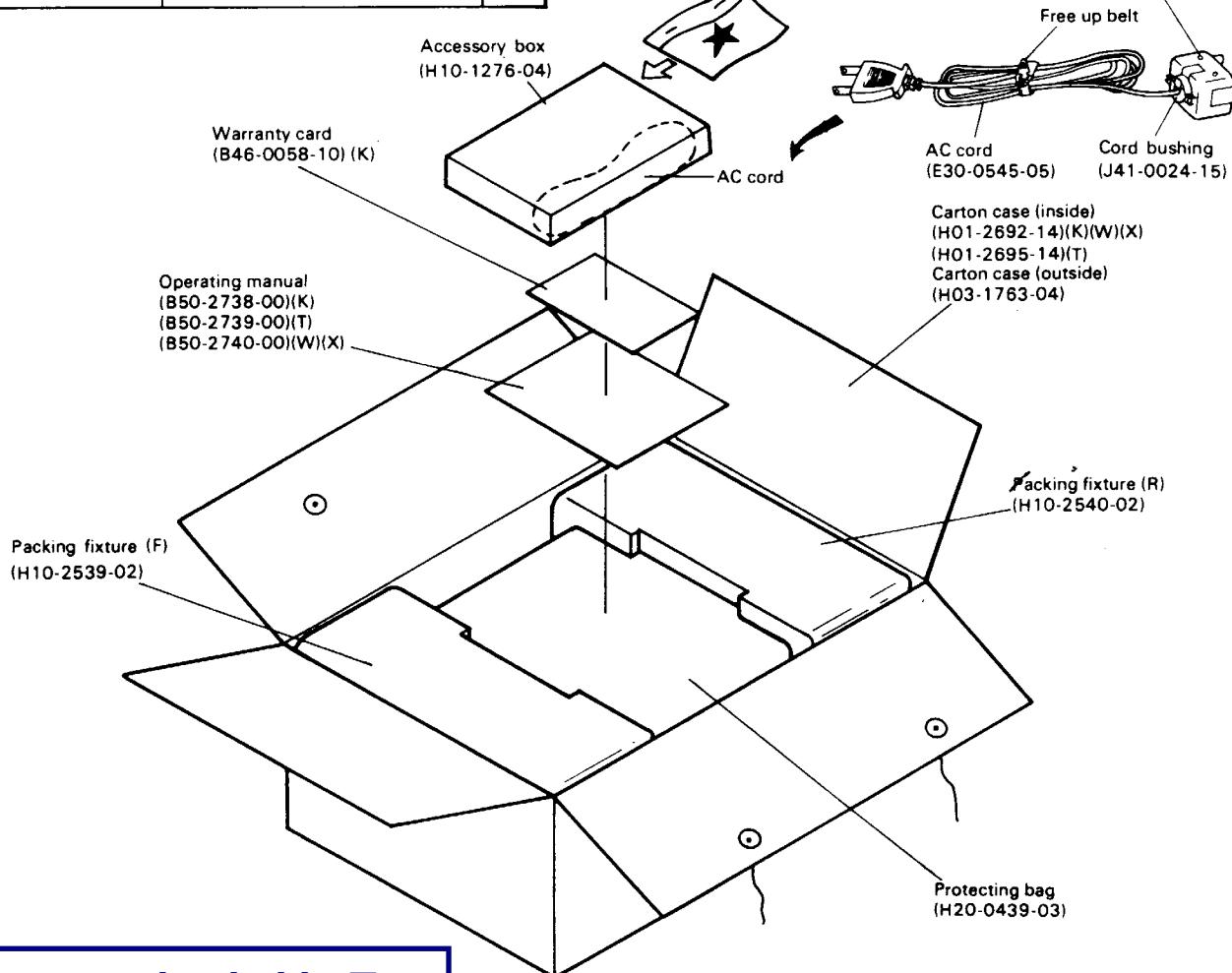
Ref. No.	Parts No.	Description	Re-marks
C13	CC45LG1H220J	C 22pF	
C14	CC45CG1H100D	C 10pF ±0.5pF	
C15	CC45LG1H151J	C 150pF	
C16	CC45RG1H151J	C 150pF	
C17	CC45CH1H020C	C 2pF ±0.25pF	
C18	C91-0456-05	C 0.047μF	
C21	CC45SL1H390J	C 39pF	
C22	CC45CH1H100D	C 10pF ±0.5pF	
C23	CC45SL1H390J	C 39pF	
C24	C91-0456-05	C 0.047μF	
VC1	CO2-0019-05	Variable capacitor 30pF	☆
TC1	C05-0009-15	Ceramic trimmer 6pF	
TC2	C05-0013-15	Ceramic trimmer 20pF	
	D40-0614-05	Gear ass'y	☆
L1	L32-0628-05	Oscillating coil	☆
L2	L33-0025-05	Choke 1μH	☆
L3	L32-0629-05	Oscillating coil	☆
L4	L32-0609-05	Oscillating coil	
L5	L40-1021-03	Ferri-inductor 1mH	
L6	L40-4711-03	Ferri-inductor 470μH	
L7	L40-1021-03	Ferri-inductor 1mH	
L8	L40-1501-03	Ferri-inductor 15μH	
L9~11	L40-4711-03	Ferri-inductor 470μH	
	N09-0617-04	Screw with washer	

Ref. No.	Parts No.	Description	Re-marks
C1	CC45SL2H101J	C 100pF 500V	
C2	CK45E2H102P	C 0.001μ 500V	
C11.12	CK45E2H103P	C 0.01μF 500V	
C13	CC45CH2H150J	C 15pF 500V	
	E01-0002-05	8P (octal) socket	
	E23-0046-04	Square terminal	☆
PS1. 2	L33-0010-05	Parastic suppressor GRID	
L1	L40-1511-03	Ferri-inductor 150μH	
L2	L40-4711-03	Ferri-inductor 470μH	
R2 ~ 5	RC05GF2H200J	Solid 20Ω 1/2W	
R7. 8	RC05GF2H101J	Solid 100Ω 1/2W	

- ★ Protective bag (H25-0120-04)
 Fuse (6A) × 1 (F05-6021-05)(K)
 Fuse (4A) × 1 (F05-4022-05)(W)(T)(X)
 Phone plug × 1 (E12-0001-05)
 7P Din plug × 1 (E07-0751-05)
 Foot (large) × 2 (J02-0049-14)
 Screw (4 × 12) × 2 (N30-4012-46)
 4P MIC plug × 1 (E07-0403-05)

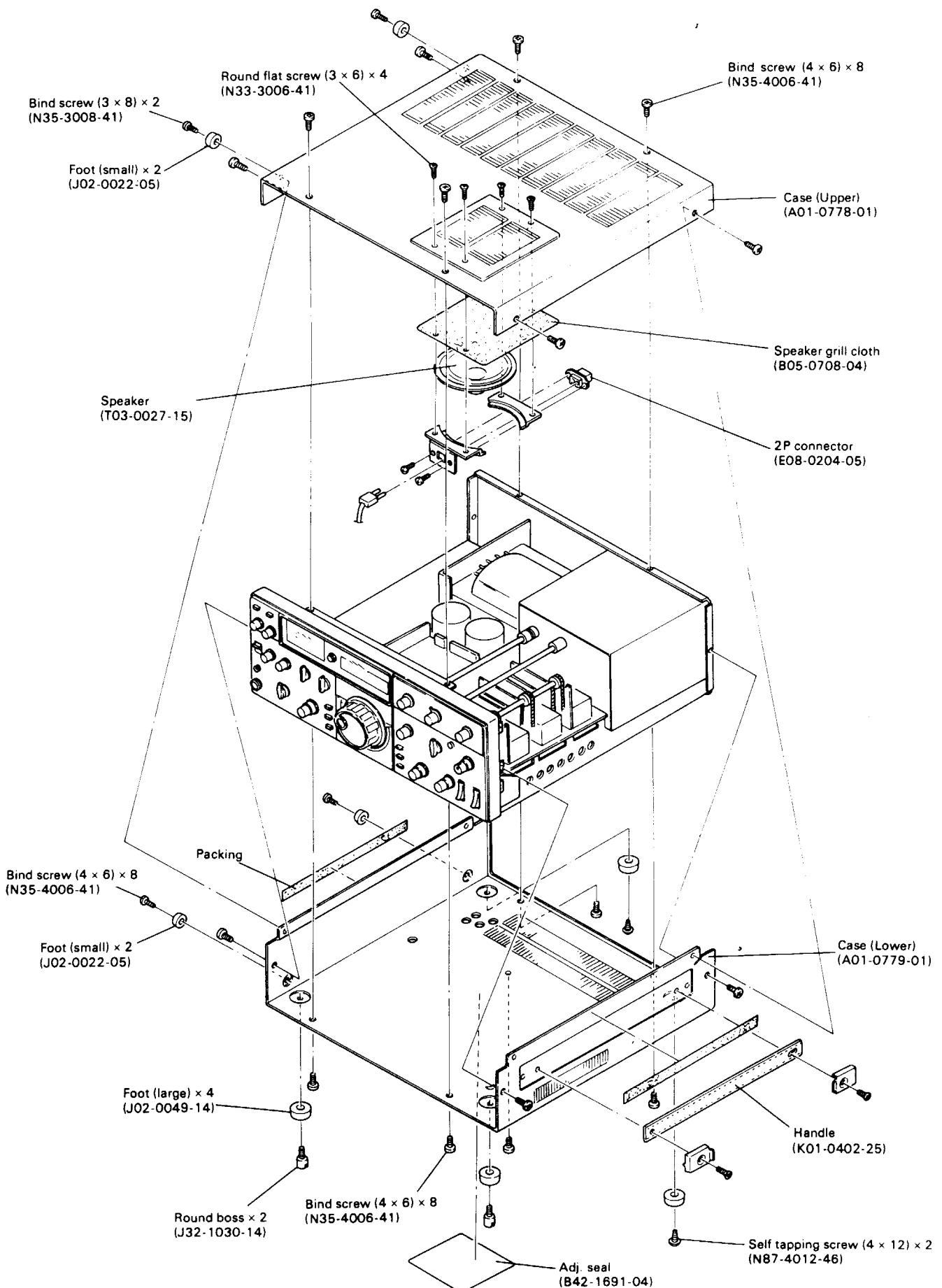
12P plug W, T
(E08-1207-05)

Carton case (inside)
(H01-2692-14)(K)(W)(X)
(H01-2695-14)(T)
Carton case (outside)
(H03-1763-04)

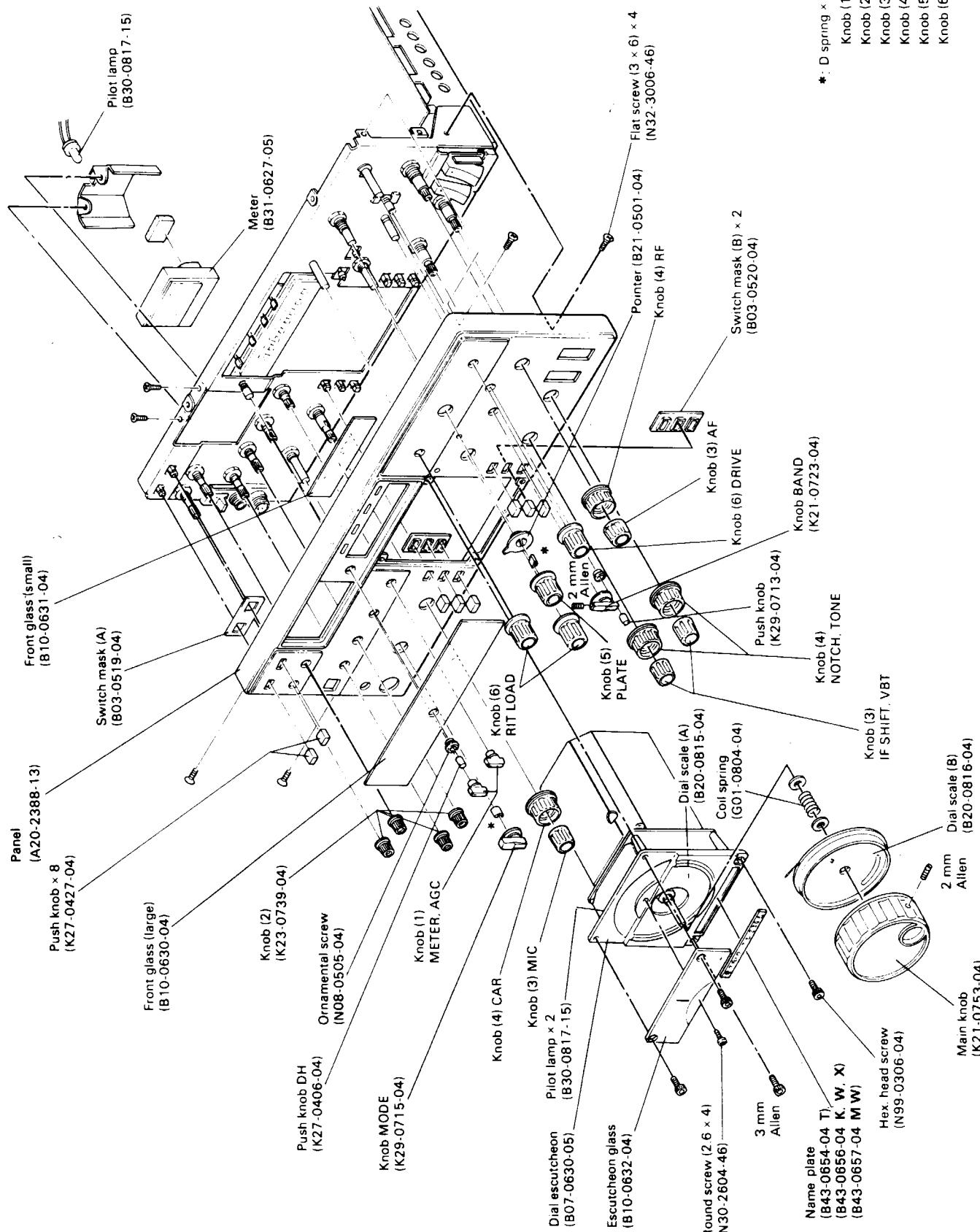


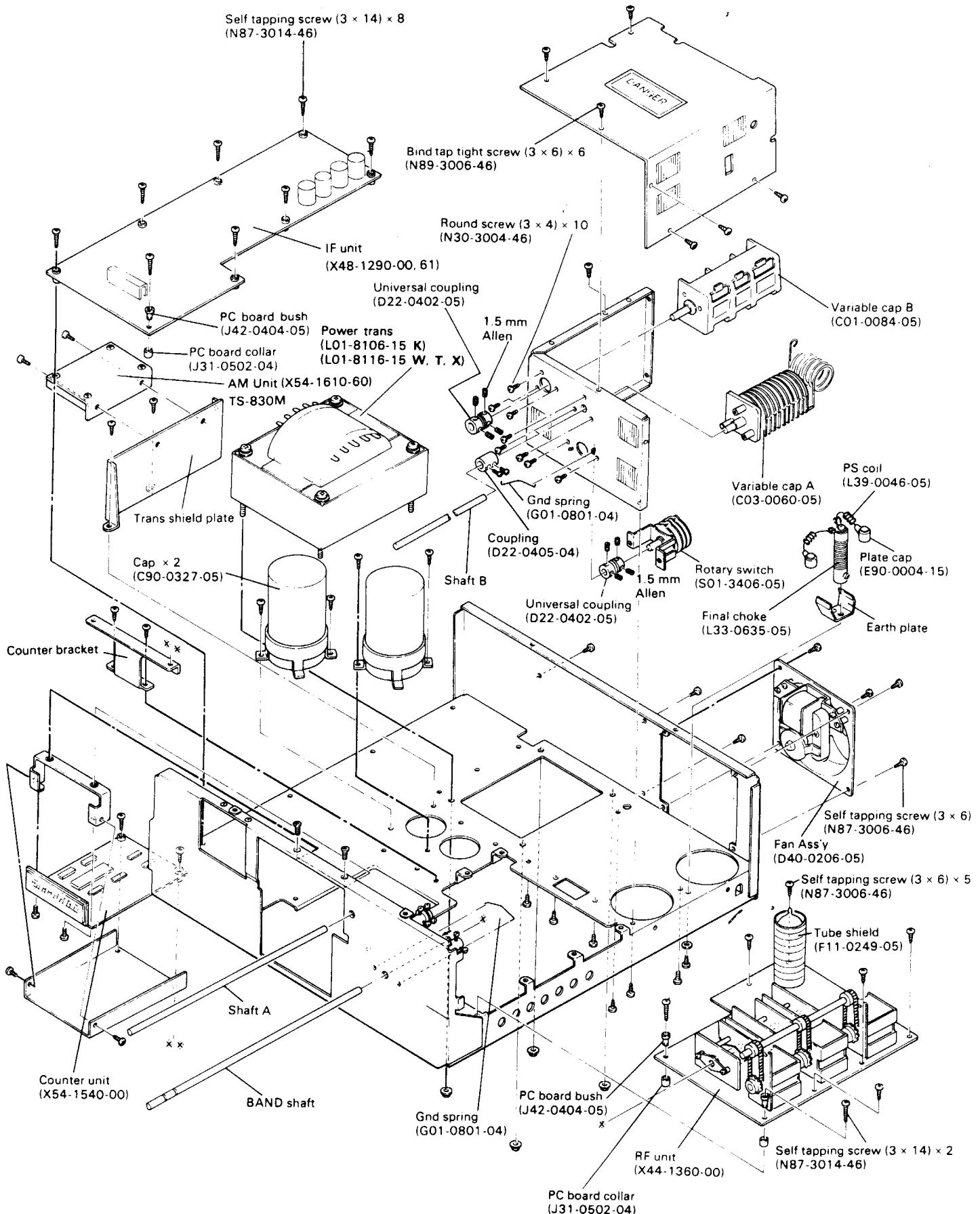
DISASSEMBLY

TS-830S, M

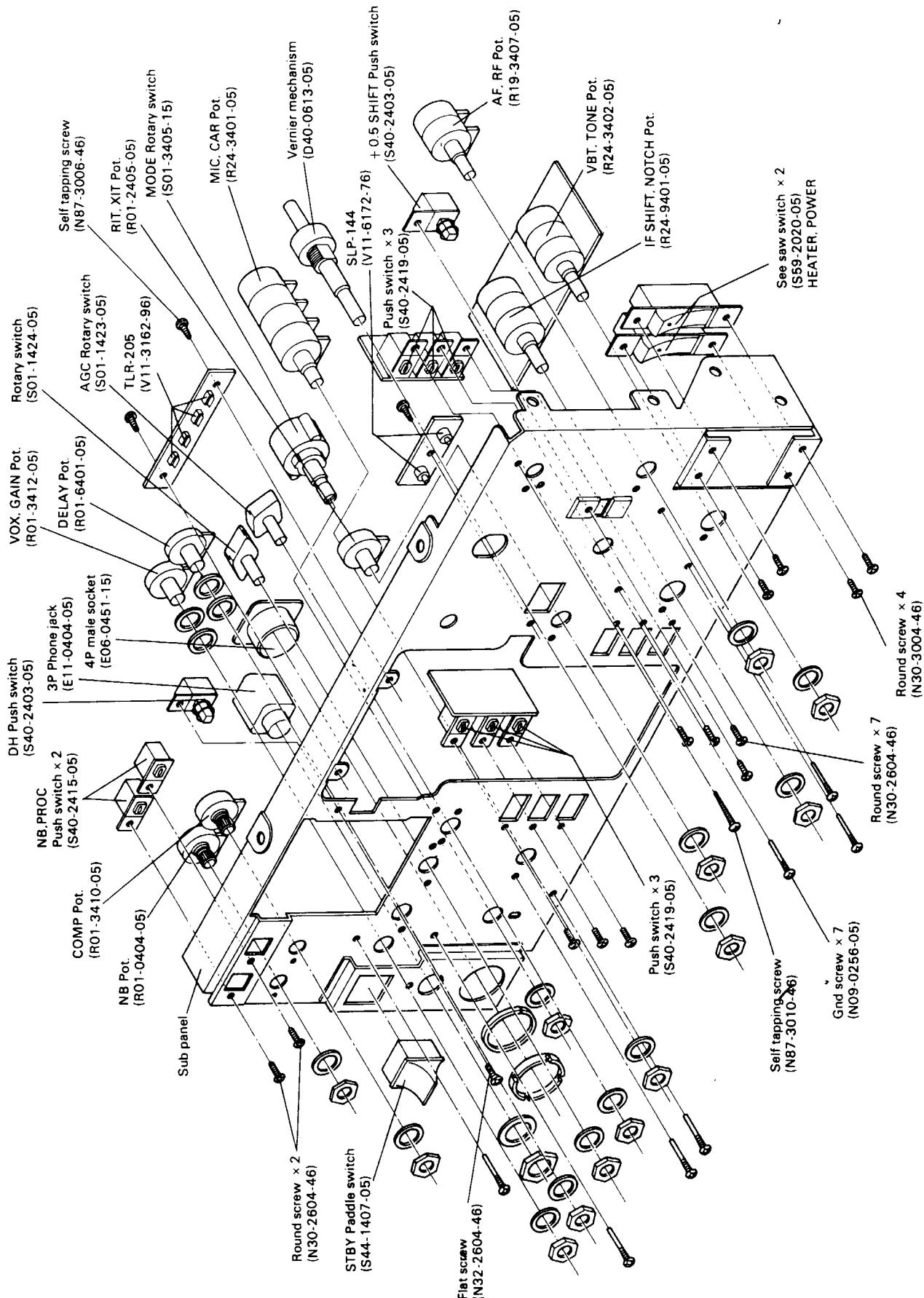


DISASSEMBLY



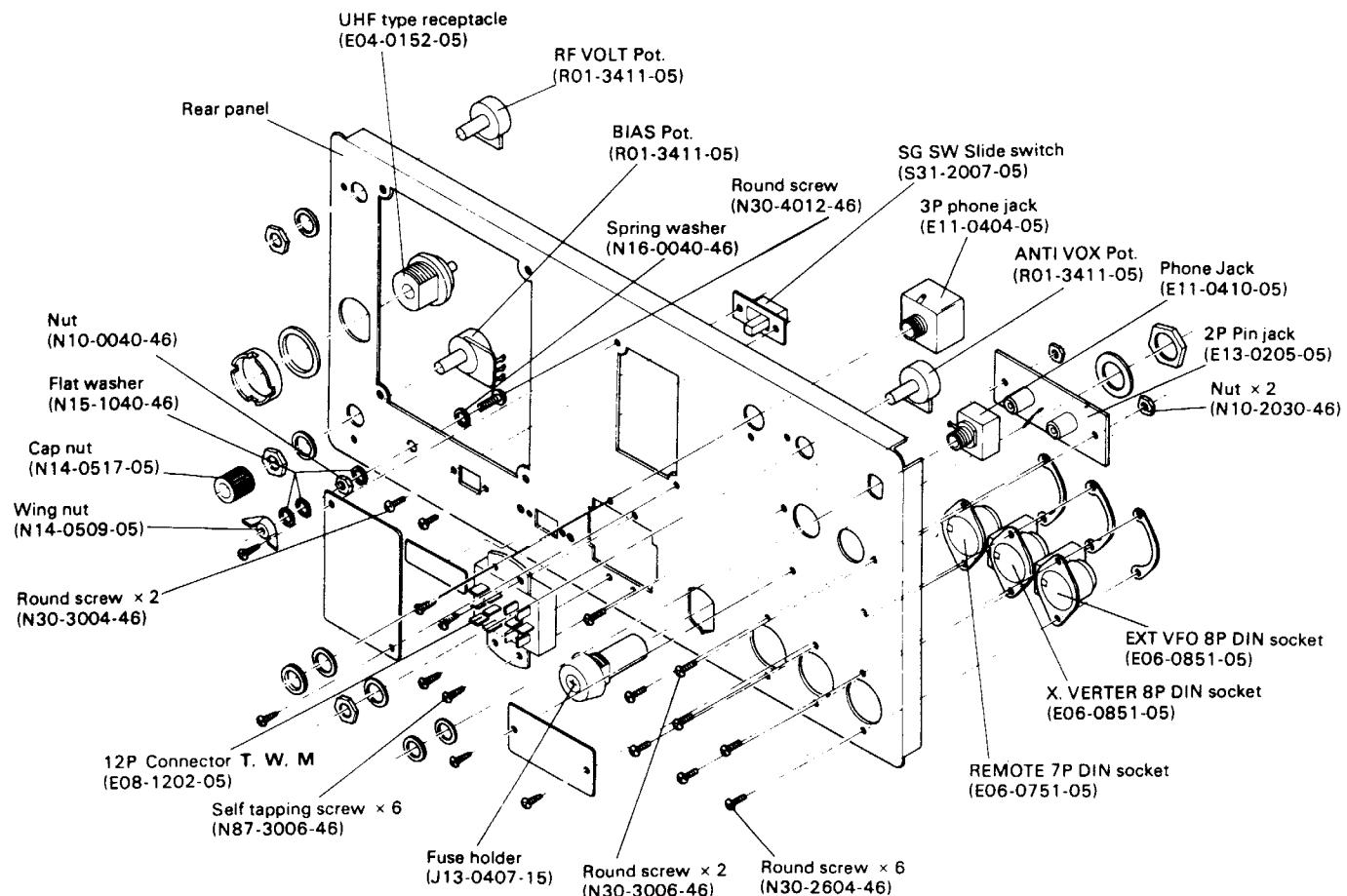
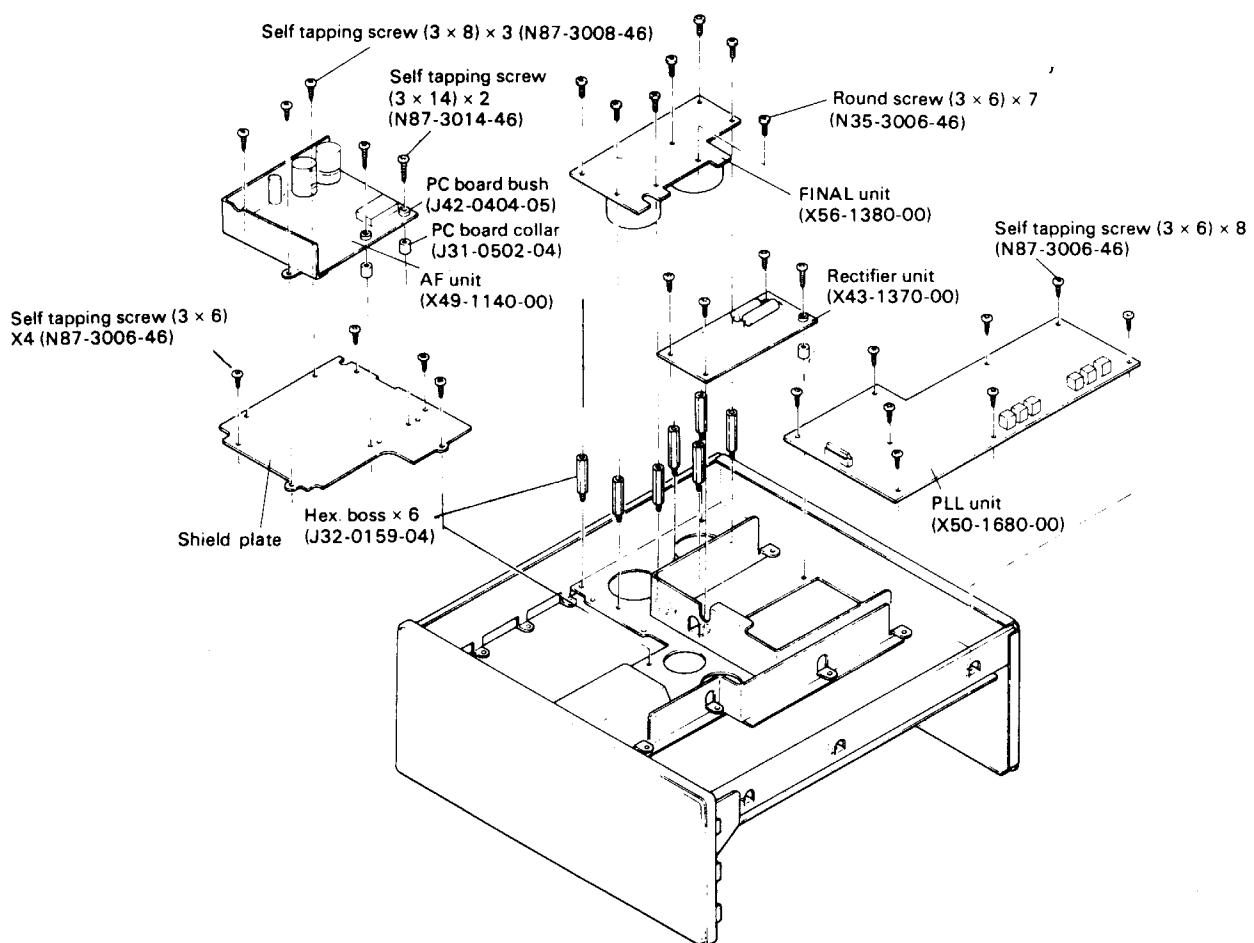


DISASSEMBLY



DISASSEMBLY

TS-830S, M



TEST EQUIPMENT REQUIRED**1. VTVM or DVM**

- 1) Input resistance: More than $1\text{ M}\Omega$
- 2) Voltage range: 1.5 to 1000V AC/DC

NOTE:

A high-precision voltmeter may be used.

However, accurate readings can not be obtained for high-impedance circuits.

2. RF VTVM

- 1) Input impedance: $1\text{ M}\Omega$ and less than 3 pF
- 2) Voltage range: 10 mV to 300V
- 3) Frequency range: 50 MHz or greater

3. AF VTVM

- 1) Frequency range: 50 Hz to 10 kHz
- 2) Input impedance: $1\text{ M}\Omega$ or greater
- 3) Voltage range: 10 mV to 30V

4. AF GENERATOR (AG)

- 1) Frequency range: 200 Hz to 5 kHz
- 2) Output: $2\text{ mV} \sim 1\text{ V}$, low distortion

5. AF DUMMY LOAD

- 1) Impedance: 8Ω
- 2) Dissipation: 3W or greater

6. RF DUMMY LOAD (POWER METER)

- 1) Impedance: 50Ω
- 2) Dissipation: 100W continuous or greater
- 3) Frequency limits: 1.8 to 30 MHz

7. OSCILLOSCOPE

Requires high sensitivity and external synchronization capability.

8. SWEEP GENERATOR

- 1) Center frequency: 8 to 40 MHz
- 2) Sweep bandwidth: Maximum ± 16 MHz
- 3) Output voltage: More than 0.1V

9. STANDARD SIGNAL GENERATOR (SSG)

- 1) Frequency range: 1.8 to 30 MHz
- 2) Output: $-20\text{ dB}/0.1\text{ }\mu\text{V} \sim 120\text{ dB}/1\text{ V}$
- 3) Output Z = 50Ω

Generator must be frequency stable.

10. FREQUENCY COUNTER

- 1) Minimum input voltage: 50 mV
- 2) Frequency range: Greater than 50 MHz

11. NOISE GENERATOR

Must generate ignition-like noise containing harmonics beyond 30 MHz.

12. Spectrum analyzer

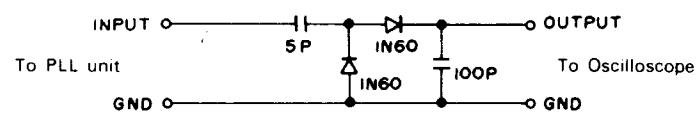
- 1) Frequency range: 100 kHz to 110 MHz
- 2) Bandwidth: 1 kHz to 3 MHz

NOTE:

R-1000 receiver may be used.

13. Detector

For adjustment of PLL unit BPF.

**14. Directional coupler****15. 8P DIN connector**

Refer to Fig. 11 on page 41.

16. FIX-CH Adjusting crystal element

- 1) 5.750 MHz (center 250 kHz)

PREPARATION

Unless otherwise specified, set the controls as follows.

Rear panel

SG SW	OFF
-------	-----

Front panel

MODE	TUNE	TONE	MAX
CAR	MIN	RF GAIN	MAX
VOX	OFF	PROC	OFF
METER	IP	BAND	1.5
RIT/XIT	CENTERED	DH	OFF
HEATER	OFF	MONI	OFF
FIX	OFF	RIT	OFF
RF ATT	OFF	NOTCH	OFF
XIT	OFF	VBT	MAX
IF SHIFT	CENTERED	AF GAIN	MIN

ADJUSTMENTS

TS-830S, M

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
1. 9V AVR and 3.6V adjustment		DVM	AF	TP-6 Pin 5, connector ⑨	AF	VR4 VR3	9V 3.6V	9V±0.05V 3.6V±0.05V	RF1
2. Base current adjustment	SG SW ON MODE USB METER IP HEATER ON FIX ON DRIVE Fully clockwise BIAS VR Rotate	IP meter							
	STBY REC, SG SW OFF						*After checking the above.	Set to 60 mA	
3. Carrier adjustment ① NOTE: When making any one of the adjustments in items 3., 4., 5., or 6., observe the listed order of adjustment.	IF SHIFT VBT Centered Fully clockwise USB	RF VTVM	IF	TP3	PLL	NOTE: T16	0.3V	0.3V±1dB	NOTE: The slug of T16 should be turned counterclockwise for this adjustment after the peak point is determined.
	MODE STBY LSB REC SEND	f. counter				VR2	Obtain the same frequency for both transmission and reception.	±20Hz	
	STBY REC				TC2	8828.50kHz			
	MODE USB				TC3	8831.50kHz			
	MODE STBY CW SEND				VR3	8830.70kHz			
	STBY REC								
4. IF SHIFT check	MODE VBT LSB Fully clockwise Rotate the IF SHIFT and check the variation	f. counter	IF	TP3				Set IF SHIFT to center. More than + 1.1 kHz Less than - 1.1 kHz	
	MODE USB: Same as above								
	STBY SEND Regardless of the IF SHIFT setting							8831.50 kHz	USB
	STBY REC							8828.50 kHz	LSB
	IF SHIFT VBT Centered Fully counter-clockwise						Reference the above frequency Less than - 1.1 kHz	USB, LSB	
5. VBT adjustment and check	VBT Fully clockwise	RF VTVM	IF	TP1	IF	NOTE: L16	1.0V	1.0V±1dB	NOTE: The slug of L16 should be turned counterclockwise for this adjustment after the peak point is determined.
	F.counter				TC2	8375.00kHz			
							Reference the above frequency Less than - 2.4 kHz		
	STBY SEND Regardless of VBT setting						Reference the above frequency Less than ± 70 Hz		
	STBY REC								

ADJUSTMENTS

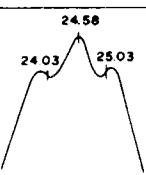
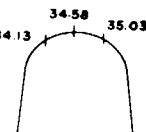
Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
6. Carrier adjustment ^②	IF SHIFT VBT MODE	Centered Fully clockwise USB	RF VTVM F.counter	IF Short jumper	IF	L19 L20	Max. level		
	MODE	LSB		(9)		NOTE: L20			NOTE: The slug of L20 should be adjusted while it is turned counterclockwise
	MODE	USB						0.3V±1 dB	If the voltage is outside the specified limit, adjust L19 and L20 until the voltage is 0.3V for both USB and LSB.
	MODE	LSB						456.5 kHz	Reference value
	MODE	CW						453.5 kHz	
	STBY	SEND						455.7 kHz	
	STBY	REC							
7. VFO, RIT, FIX CH check and adjustment	Scale: 0~500							Should rotate smoothly and have no abnormal noise.	
	Rear panel VFO	SG SW OFF 250	RF VTVM	PLL	Pin 2, connec- tor ⑤ 1P (GND)	When the vol- tage is out side specifi- cation		200mV±1 dB	
	MODE STBY	CW SEND	F.counter	Rear panel	EXT VFO 1P 7P (GND)	VFO	L4 When the fre- quency is outside specifi- cation.	800 Hz UP	800±50 Hz
	Set the VFO dial to 50. At this time set the CAL control to the index.		F.counter	Rear panel	EXT VFO 1P 7P (GND)	VFO	L3	5550.00 kHz ±200 Hz	Repeat the adjustment several times until the frequency is within specification.
	Set the VFO dial to approx. 450. Set the CAL control dial calibrated under this VFO setting exactly to the index.						TC1	5950.00 kHz ±200 Hz	
	Tune in 5550.00 kHz with the main tuning knob.							The 50 kHz point on the dial scale must be aligned to the index.	
	Under the above condition, set the CAL control to the index. Turn the main tuning, and set the calibrated CAL control to the index in the order of 0, 100, 200, 300, 400, and 500 to check frequency deviation at each 100 kHz point.				0 100 200 300 400 500	5.5 MHz 5.6 5.7 5.8 5.9 6.0		Within ±2 kHz	Check the scale (Electrically)
	Set the CAL control back to 250 under the above condition (do not turn excessively), then further set back the CAL control to 0 with respect to the frequency at 250 to check the difference from the reference frequency.							Less than 400Hz	Backlash

ADJUSTMENTS

TS-830S, M

Item	Condition	Measurement			Adjustment			, Specification	Remarks																																																																																												
		Test equipment	Unit	Terminal	Unit	Parts	Method																																																																																														
7. Dial scale and RIT control calibration	Set point 0 on the dial scale and the CAL control to the index. When the CAL control calibrated at each 100 kHz is set at the index, check the deviation of the dial scale at each point.							Should come in contact with the pointer.	Check the scale (mechanically)																																																																																												
	RIT: Centered RIT switch: ON Set the VFO main control to 5750.000 kHz				AF	VR2	5750.000 kHz																																																																																														
	Check that the same frequency is obtained when the RIT switch is turned ON and OFF.							Less than 50 Hz																																																																																													
	RIT switch: ON VFO: 250 RIT control: fully counter-clockwise (\ominus position) RIT control: fully clockwise (\oplus position)							-1.5kHz or less +1.5kHz or more Reference to the center (ϕ) position of the RIT control.																																																																																													
	RIT OFF																																																																																																				
	Install the 5750 kHz quartz crystal into the socket on the PLL unit.				PLL	TC4	Centered	Normal oscillation must be obtained.																																																																																													
	FIX ON	RF VTVM					TC 4 Variable	Variable range: ± 250 Hz																																																																																													
	Remove the quartz crystal from its socket. FIX OFF						Centered	Output level 0.2V ± 2 dB																																																																																													
8. Counter reference oscillator adjustment		F counter	PLL	TP2	PLL	TC1	1000 000 kHz																																																																																														
9. VCO adjustment and check	Check the frequencies at the following points and adjust coils until those given in brackets are obtained.	F.counter DVM	RF(PLL) PLL	TP3(D40) TP1																																																																																																	
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th colspan="2"></th> <th colspan="3">Adjusting point</th> </tr> <tr> <th>VFO BAND</th> <th>0</th> <th>250</th> <th>500</th> <th></th> <th>Unit</th> <th>Part</th> </tr> </thead> <tbody> <tr> <td>1.5</td> <td>10.33 MHz</td> <td>10.58 MHz (4.0V)</td> <td>10.83 MHz</td> <td></td> <td rowspan="6">PLL</td> <td>T2</td> </tr> <tr> <td>3.5</td> <td>12.33</td> <td>12.58</td> <td>12.83</td> <td></td> <td>T1</td> </tr> <tr> <td>7</td> <td>15.83</td> <td>16.08 (4.75)</td> <td>16.33</td> <td></td> <td>T3</td> </tr> <tr> <td>10</td> <td>18.83</td> <td>19.08</td> <td>19.33</td> <td></td> <td>T4</td> </tr> <tr> <td>14</td> <td>22.83</td> <td>23.08 (4.5)</td> <td>23.33</td> <td></td> <td>T6</td> </tr> <tr> <td>18</td> <td>26.83</td> <td>27.08 (5.0)</td> <td>27.33</td> <td></td> <td>T5</td> </tr> <tr> <td>21</td> <td>29.83</td> <td>30.08 (3.5)</td> <td>30.33</td> <td></td><td></td> <td></td> </tr> <tr> <td>24.5</td> <td>33.33</td> <td>33.58</td> <td>33.83</td> <td></td><td></td> <td></td> </tr> <tr> <td>28</td> <td>36.83</td> <td>37.08</td> <td>37.33</td> <td></td><td></td> <td></td> </tr> <tr> <td>28.5</td> <td>37.33</td> <td>37.58</td> <td>37.83 (4.75)</td> <td></td><td></td> <td></td> </tr> <tr> <td>29</td> <td>37.83</td> <td>38.08</td> <td>38.33</td> <td></td><td></td> <td></td> </tr> <tr> <td>29.5</td> <td>38.33</td> <td>38.58</td> <td>38.83</td> <td></td><td></td> <td></td> </tr> </tbody> </table>			Adjusting point			VFO BAND	0	250	500		Unit	Part	1.5	10.33 MHz	10.58 MHz (4.0V)	10.83 MHz		PLL	T2	3.5	12.33	12.58	12.83		T1	7	15.83	16.08 (4.75)	16.33		T3	10	18.83	19.08	19.33		T4	14	22.83	23.08 (4.5)	23.33		T6	18	26.83	27.08 (5.0)	27.33		T5	21	29.83	30.08 (3.5)	30.33				24.5	33.33	33.58	33.83				28	36.83	37.08	37.33				28.5	37.33	37.58	37.83 (4.75)				29	37.83	38.08	38.33				29.5	38.33	38.58	38.83													
		Adjusting point																																																																																																			
VFO BAND	0	250	500		Unit	Part																																																																																															
1.5	10.33 MHz	10.58 MHz (4.0V)	10.83 MHz		PLL	T2																																																																																															
3.5	12.33	12.58	12.83			T1																																																																																															
7	15.83	16.08 (4.75)	16.33			T3																																																																																															
10	18.83	19.08	19.33			T4																																																																																															
14	22.83	23.08 (4.5)	23.33			T6																																																																																															
18	26.83	27.08 (5.0)	27.33			T5																																																																																															
21	29.83	30.08 (3.5)	30.33																																																																																																		
24.5	33.33	33.58	33.83																																																																																																		
28	36.83	37.08	37.33																																																																																																		
28.5	37.33	37.58	37.83 (4.75)																																																																																																		
29	37.83	38.08	38.33																																																																																																		
29.5	38.33	38.58	38.83																																																																																																		
	Check the level at each of the above points.	RF VTVM	RF	TP3				1V ± 3 dB																																																																																													
	Both edges of the VFO frequency in each band							Display should indicate.																																																																																													
10. BPF-A	Disconnect connectors 1 and 3 on the PLL unit. Connect the cathode of D24 (1S1555) to the jumper wire next to R44 with a clip lead. Connect the sweep generator RF output to the EXT-VFO connector.	Sweep generator, Oscilloscope	PLL	Q35 (E)	PLL	T13 T14 T15	Adjust until the response shown to the right is obtained.																																																																																														

ADJUSTMENTS

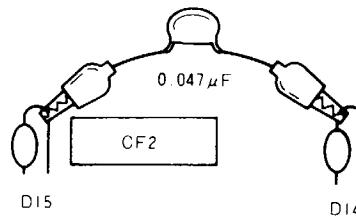
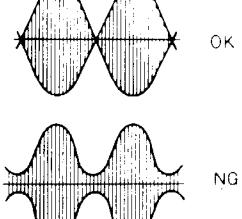
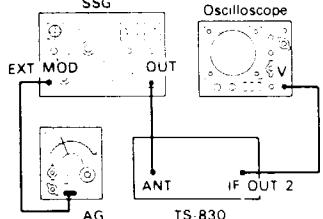
Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
11. BPF-B adjustment	Disconnect connectors 1 and 5 on the PLL unit. Connect the cathode of D27 (1S1555) to the jumper wire next to R44 with a clip lead. Connect the cathode of D50 (1S1587) to the jumper wire next to TC1 with a clip lead. Connect the RF output of the sweep generator to R111 (100Ω) via a 15PF capacitor.	Sweep generator Oscilloscope	PLL	Q35 (E)	PLL	T7 T8 T9 T17	Adjust T7 ~ T9 until the response shown at the right is obtained. Then adjust T17 for maximum amplitude.		
12. BPF-C adjustment	Disconnect connectors 1 and 5 on the PLL unit. Connect the cathode of D26 (1S1555) to the jumper wire next to R44 with a clip lead. Connect the cathode of D50 (1S1587) to the jumper wire next to TC1 with a clip lead.					T10 T11 T12 T18	Adjust T10 through T12 until the band response shown at right is obtained. Then adjust T18 for maximum amplitude.		
13. VFO. MIX spurious adjustment NOTE: This adjustment should be done after completing the adjustment (or check) of BPF-A.	Disconnect connector 1 on the PLL unit. Connect the cathode of D24 (1S1555) to the jumper wire next to R44 with a clip lead. VFO 250 MODE CW	Spectrum analyzer (Monitor) (receiver)				VR1	Minimum (14.99 MHz)	Less than -55dB	
13'. Balance adjustment TS-830M only	BAND: 7 VFO: 150 MODE: AM Connect the SSG output (7.15 MHz, 10dB) to the ANT terminal.	Oscillo-scope, AF VTVM	Rear panel	EXT. SP	RF IF	VR1 VR9	Beat output for minimum		TS-830M only
14. Carrier balance adjustment	IF SHIFT RF GAIN Centered Fully counter-clockwise	RF VTVM	Rear panel	IF OUT 2	IF	TC1	Minimum		
15. IF AMP adjustment	BAND: 1.5 VFO: 400 DRIVE: 12:00 RF GAIN: fully clockwise IF SHIFT: centered RF ATT: OFF MODE: USB AGC: OFF NOTCH SW: OFF NB SW: OFF VBT: fully clockwise TONE: fully clockwise SG SW: OFF Connect the SSG output (1.9 MHz, 40 dB) to the antenna terminal. While adjusting, gradually decrease the SSG output level down to -6 dB.	AF VTVM Oscilloscope	Rear panel	EXT. SP	RF IF	ANT coil 1.8 RF coil 1.8 T2 L2 L3 L4 L5 L6 L7 L9 L11	Max. audio output		
16. Coil pack adjustment	Connect the SSG (40 dB) to the ANT terminal. DRIVE 12:00 While adjusting, gradually decrease the SSG output level down to -6 dB. Adjust at the following points: No. BAND VFO f	AF VTVM Oscilloscope	Rear panel	EXT. SP	RF	ANT coil RF coil 1.8 3.5 7 10 14	Max. audio output		
	1 1.5 400 1.9 MHz								
	2 3.5 250 3.75								
	3 7 150 7.15								
	4 10 125 10.125								
	5 14 175 14.175								

ADJUSTMENTS

TS-830S, M

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
	6 18 125 18.125					18			
	7 21 225 21.225					21			
	8 24.5 450 24.950					24			
	9 28.5 300 28.800					28			
17. ALC "0" adjustment	METER ALC STBY SEND				IF	VR6	ALC meter starting point		
18. Generator, Drive coil adjustment	CAL ON CAR VR Centered HEATER ON MODE CW METER ALC SG SW OFF RF ATT ON Receive the marker frequency in the following bands and obtain the peak level by adjusting the DRIVE control. Set the STBY switch to SEND and adjust each coil.				RF	DRIVE coil	Max. ALC meter reading		
	BAND VFO						() See note Before adjustment		
	1.5 400					1.8			
	3.5 250					3.5			
	7 150					7			
	(10) (125)					10			
	14 175					14			
	(18) (125)					18			
	21 225					21			
	(24.5) (450)					24.5			
	28.5 300					28			
	BAND 14, VFO 175 Adjust the CAR control until the ALC meter reads maximum.				RF IF	T4 L24 L25 L28 L29	Max. ALC meter reading		
	STBY REC								
18' AM adjustment and S/N check	BAND: 14 VFO: 175 MODE: AM Connect the SSG output (14.175MHz, 40dB, MOD: 1kHz, 30%) to the ANT terminal.	AF VTVM, Oscillo-scope	Rear Panel	EXT. SP	AM	T ₁	Max. audio output	TS-830 M only	
TS-830 M only	SSG Output: 12 dB TONE: Centered								
	SSG MOD: OFF								
19. MIX balance adjustment	RF ATT ON BAND 1.5 VFO 0	Oscillo-scope	Rear panel	IF OUT 1	RF	VR1	Minimum		Except TS-830M
20. IF trap adjustment	BAND: Between 1.5 and Aux. Connect the SSG (8.83 MHz, 80 dB) to the ANT terminal.	Oscillo-scope AF VTVM	Rear panel	EXT. SP	RF	L22 L3 L4	Minimum Adjust in the order of L22 ~ L4.		Preset the slugs of L3 and L4 fully clockwise.
21. S meter adjustment	AGC OFF BAND 14 VFO 175 AGC FAST Connect the SSG (14.174 MHz, 8dB) to the ANT terminal	Oscillo-scope AF VTVM	Rear panel	EXT. SP	IF	VR2	Set to the deflection starting point Adjust the DRIVE control for maximum AF output.		

ADJUSTMENTS

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
					IF	L9	Turn the coil slug counterclockwise until S-1 is obtained.		
	SSG output: 40 dB * Repeat S-1, 9 adjustment several times.				VR3	S-9			
22 NB adjustment	NB LEVEL: fully counter-clockwise BAND 14, VFO: 175 Connect the SSG output (14.175 MHz, 60 dB) to the ANT terminal.	DVM	AF	TP4	AF	T1 T2	Minimum		
	SSG output: 20 dB Adjust as described above.								
	AGC FAST Disconnect the SSG output from the ANT terminal, and connect the noise generator output in its place. Set the noise generator output level to S5 ~ 7.	Speaker	Rear panel	EXT. SP					
	NB ON							The NB must provide adequate effect.	
							If adequate effect is not obtained, repeat the adjustment several times.		
23 VBT adjustment with VBT-1	Reduce the noise generator output level to below the threshold of sensitivity. Turn the NB LEVEL control fully clockwise.							Noise must be blanked.	
	NB OFF								
	MODE CW N IF SHIFT Centered VBT Fully clockwise Disconnect connector ⑪ on the IF unit. Connect the VBT-1 output to IF OUT 1, and connect the oscilloscope to IF OUT 2. Connect a 0.047μF capacitor across D14 and D15 on the IF unit. Set the filter switching terminal connection to CW3. Adjust the control on the VBT-1 until the waveform shown at right is observed on the oscilloscope	Oscilloscope VBT-1	IF OUT 1, 2	0.047μF CF2	D15	D14			This adjustment requires the use of the VBT-1. If the VBT-1 is unavailable, this adjustment will be difficult.
23' VBT adjustment with SSG, AG and Oscilloscope	Set the MODE switch to CW W				IF	TC2	Adjust until the waveform shown in the preceding item is obtained.		
	Remove 0.047 μF capacitor. Reconnect connector ⑪								
23' VBT adjustment with SSG, AG and Oscilloscope	BAND 1.5 MODE CW N IF SHIFT Centered VBT Fully clockwise Disconnect connector ⑪ on the IF unit. Connect a 0.047 μF capacitor across D14 and D15 on the IF unit. Filter SW terminal: CW3. Receive SSG signal (1.9 MHz, 60 dB), and set the main tuning to obtain waveform shown at right. MODE CW.W Filter SW terminal: CW1	SSG, AG, Oscilloscope	Rear panel	IF OUT 2	IF	TC2	Adjust TC2 until part A becomes null.		
								$f = 1.2 \text{ kHz}$ Level: Level for max. AM modulation.	

ADJUSTMENTS

TS-830S, M

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
24 NOTCH adjustment	NOTCH OFF BAND 1.5 VFO 400 AGC FAST NOTCH Centered Turn VR1 on the IF unit fully counterclockwise. Couple the SSG output (1.9 MHz, 40 dB) to the ANT terminal	Oscillo-scope AF VTVM F counter	Rear panel	EXT SP			Set beat frequency to 1500 Hz and audio level 0.63V/8Ω		
	NOTCH ON				IF	L10/ VR1	Min. audio level	*Repeat a few times	
	Rotate NOTCH.							The dip point should be located somewhere between 11.00 to 1.00, and audio level 0.1V 8Ω or less	
	NOTCH OFF								
25 Neutralization trimmer adjustment	BAND 28. +0.5 SHIFT ON VFO 300 MODE SW CW SG SW ON STBY SEND Tune the DRIVE, PLATE and LOAD	Power meter Sync'd scope	Rear panel	ANT					
	SG SW OFF							The output must be 0	
	Set the ALC to maximum with the DRIVE control. Increase oscilloscope sensitivity.				Neutralization trimmer TC1	Minimum			
	Reduce oscilloscope sensitivity (5V/div.). SG SW ON							The normal power must be obtained. The signal waveform must be normal	
26 Side tone, semi-break-in function adjustment	Connect a power meter to the ANT terminal. Plug key into the rear Key jack to transmit in any band	Oscillo-scope AF VTVM	Rear panel	EXT SP	AF	VR1	0.63V/8Ω		
	Operate the key							The power must be intermittent	
	STBY REC VOX ON Operated the key.							Semi-break-in operation should be available.	
27 Transmission spurious adjustment	Ground pin 4 of connector 6 on the AF unit. Set the BAND switch to 18 and VFO to 125. Connect a power meter to the ANT terminal. Set the STBY switch to SEND and tune up.	Spectrum analyzer (receive 17.66MHz with a monitor receiver.)	Rear panel	ANT	RF	VR2	Minimum (Monitor level)	Less than -40 dB	
	STBY REC								
28 RF meter adjustment	BAND 14 VFO 175 METER RF Connect a power meter to the ANT terminal. Tune up.				Rear panel	RF VOLT	Set the IP meter reading to 250		
	STBY REC								
29 Carrier suppression adjustment	BAND 14 VFO 175 MODE CW Connect power meter to ANT. STBY SEND Tune up. MODE USB	Sync'ro scope	Rear panel	ANT	IF	VR4 TC3	Adjust alternately until the minimum point is obtained		
	MODE LSB USB						Adjust until no level difference exists between LSB and USB.		

ADJUSTMENTS

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
	MODE CW Sync'ed scope level calibration								
	MODE USB, LSB							Less than -50 dB	Compare with CW
	STBY REC								
30. SSB frequency response adjustment	BAND 14 VFO 175 MODE USB Connect the AG (1500Hz, 7mV) to the MIC input connector. STBY SEND Tune up. Set to 50W with MIC GAIN control.	Power meter Sync'ed scope	Rear panel	ANT					
	AG 300Hz ↓ 2700Hz				PLL	TC3	Adjust until the same level is obtained for both 300 Hz and 2700Hz (equal audio rolloff).		
	MODE LSB same as above					TC2			
	AG 400Hz AG 2600 Hz							More than 1/2 with respect to the 1500Hz signal level observed on the scope.	
	After completing the above adjustment, readjust the carrier suppression (see item 29.)				IF	VR4 TC3	Minimum	Less than -50dB	
31. ALC adjustment	BAND 14, VFO 175 MIC GAIN MIN METER ALC Connect power meter to the ANT terminal. STBY SEND				(When misaligned.) IF	VR6	Set to the deflection start point.	The ALC meter must be aligned to its exact zero point.	
	Connect an AG output (1.5 kHz, 5 mV) to the MIC jack. Set the MIC GAIN control to maximum. Tune up. Reduce the power by 5 watts with the MIC GAIN control.							No ALC deflection	
	Increase the AG output to 10 mV.				IF	VR7	Obtain the maximum ALC on-scale reading.		
	Repeat the above three adjustment steps several times.								
32. Speech processor adjustment	SG SW OFF MODE USB METER COMP MIC GAIN MIN PROC ON Connect the AG output (1.5 kHz, 5 mV) to the MIC jack. Set the STBY switch to SEND. Adjust the COMP LEVEL control until the meter reading is obtained.				IF	L26	Max. meter reading		
	Set VR5 on the IF unit fully clockwise. Adjust the COMP LEVEL control until the meter indicates S-1. Set the AG output to 50 mV.				VR5		Meter indicator 20 dB		
	METER ALC Adjust the MIC GAIN control until the maximum meter reading is obtained.				L27		Max. meter reading		

ADJUSTMENTS

TS-830S, M

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
	Set the PROC to OFF and the COMP LEVEL control to maximum. Set the AG output to 10 mV. Adjust the MIC GAIN control until the meter indicates the maximum on-scale ALC reading. Set the PROC to ON.				L27		Adjust until the same meter reading is obtained (MAX ALC on-scale).	MIC input providing the same ALC meter reading: $10\text{mV} \pm 3\text{ dB}$	NOTE: L27 should be adjusted while it is turned counterclockwise.
	STBY REC								
33. Monitor level adjustment	BAND 14 VFO 175 AGC FAST CAL ON Set the marker level to 0.63V with the AF GAIN control.	Oscilloscope	Rear panel	EXT. SP					
	Connect a power meter to the ANT terminal. SG SW ON STBY SEND Tune up MODE USB Connect the AG (1 kHz, 10 mV) to the MIC jack. METER ALC Deflect ALC meter by MIC GAIN control. MONI ON			IF	VR8		Monitor output 0.63V/ 8Ω		
	Disconnect AG. AF GAIN MAX.							Less than 8mV/ 8Ω	Monitor hum
	MONI OFF, STBY REC								

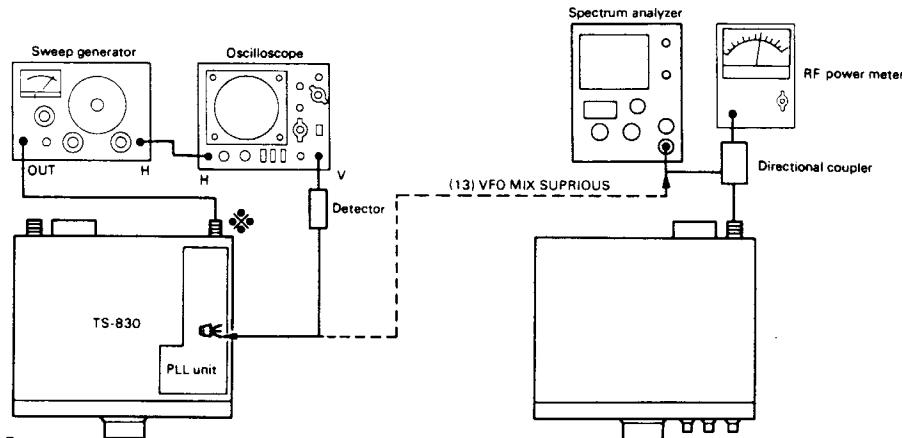


Fig. 9
(10) BPF-A, (11) BPF-B, (12) BPF-C
(13) VFO MIX SPURIOUS

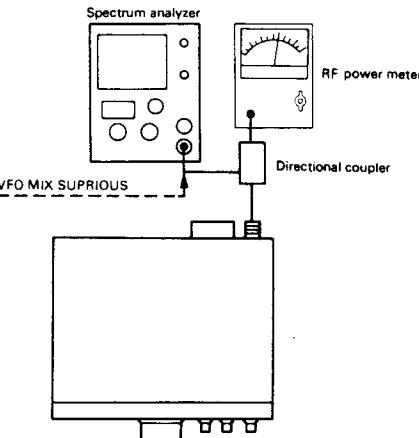


Fig. 10 (27) TX SPURIOUS

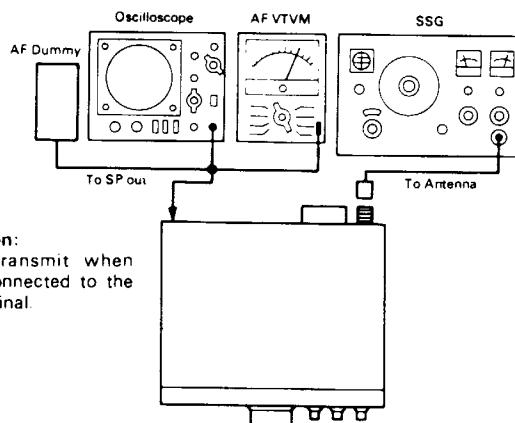


Fig. 12 (15) IF AMP, (16) COIL PACK, (20) IF TRAP,

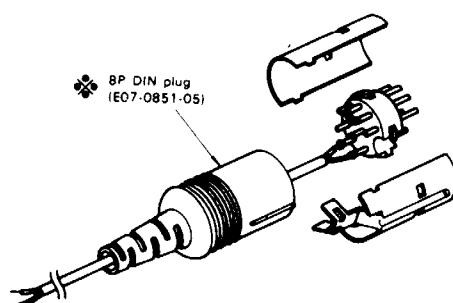
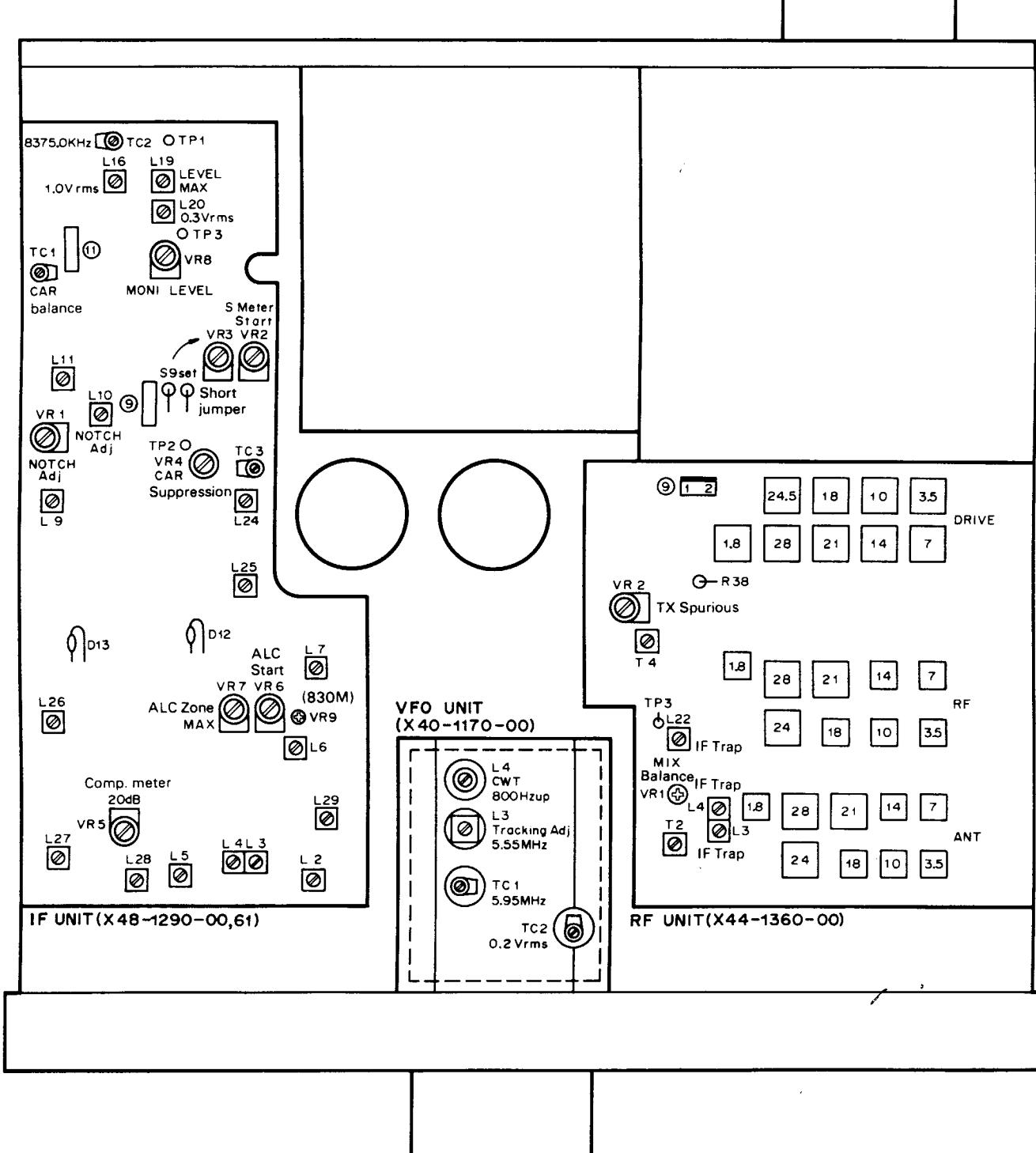


Fig. 11 8P DIN connector

LOCATION OF ADJUSTMENTS

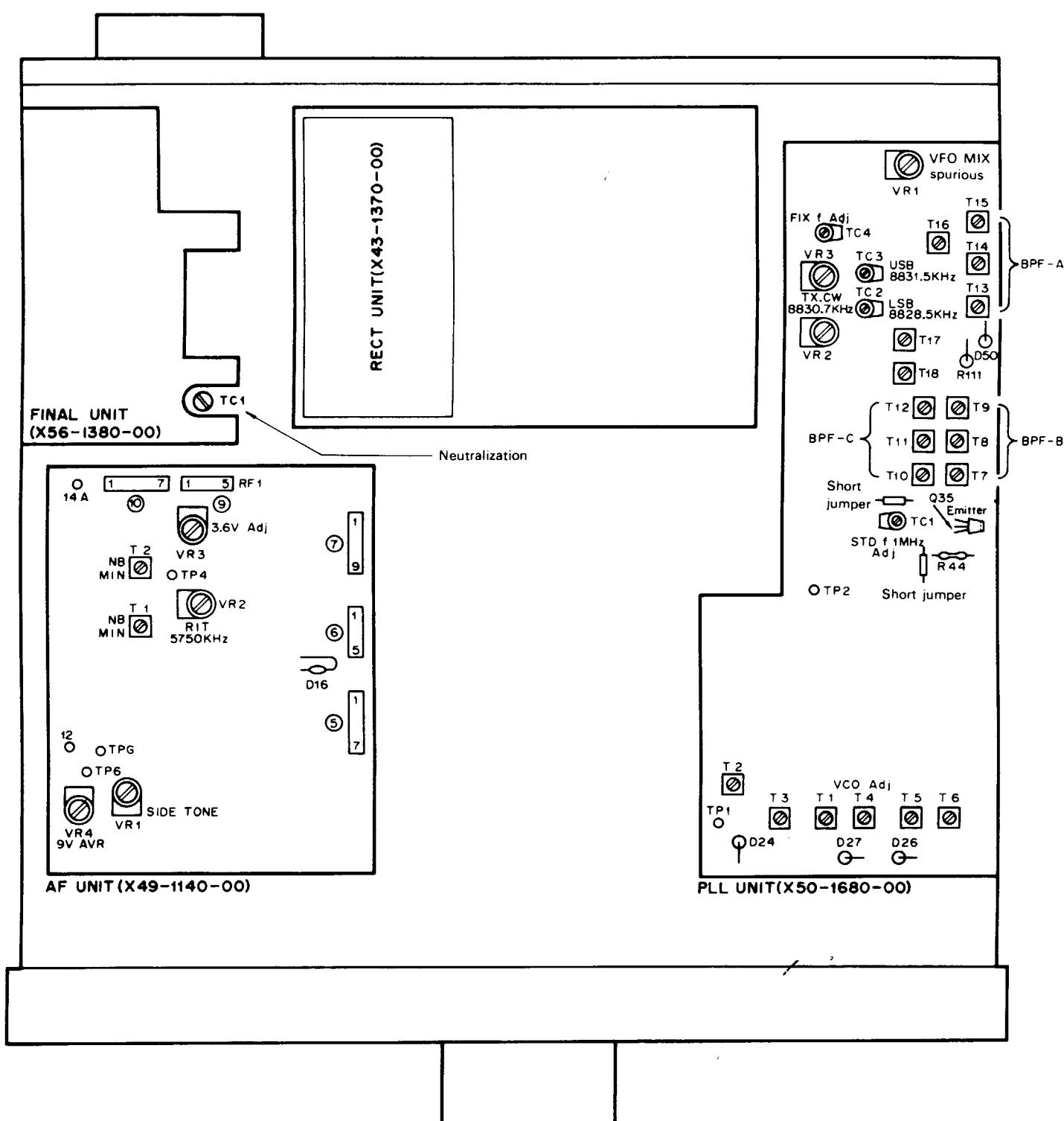
▼ TOP VIEW



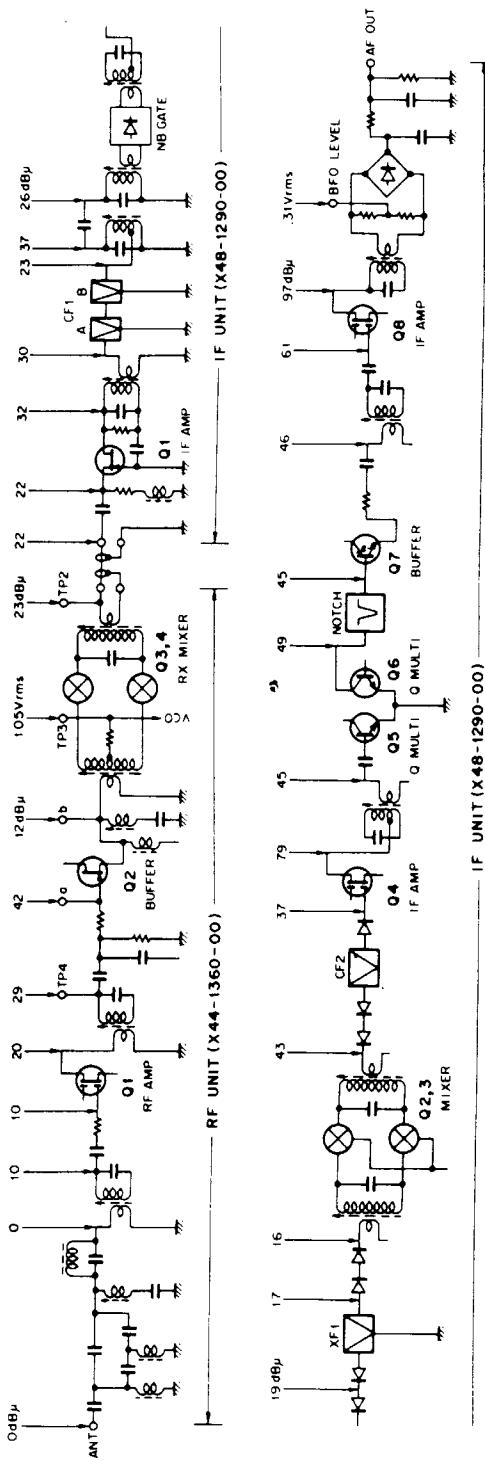
LOCATION OF ADJUSTMENTS

TS-830S, M

▼ BOTTOM VIEW

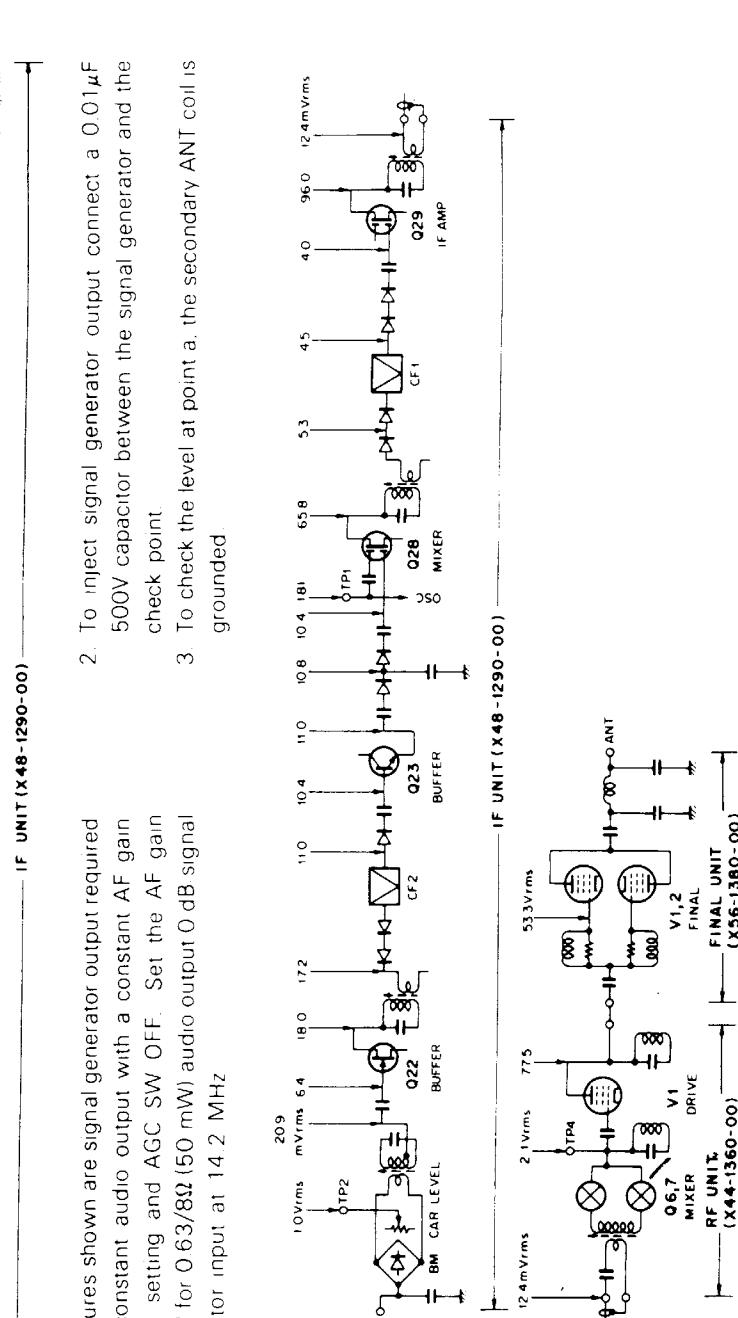


LEVEL DIAGRAM



NOTES:

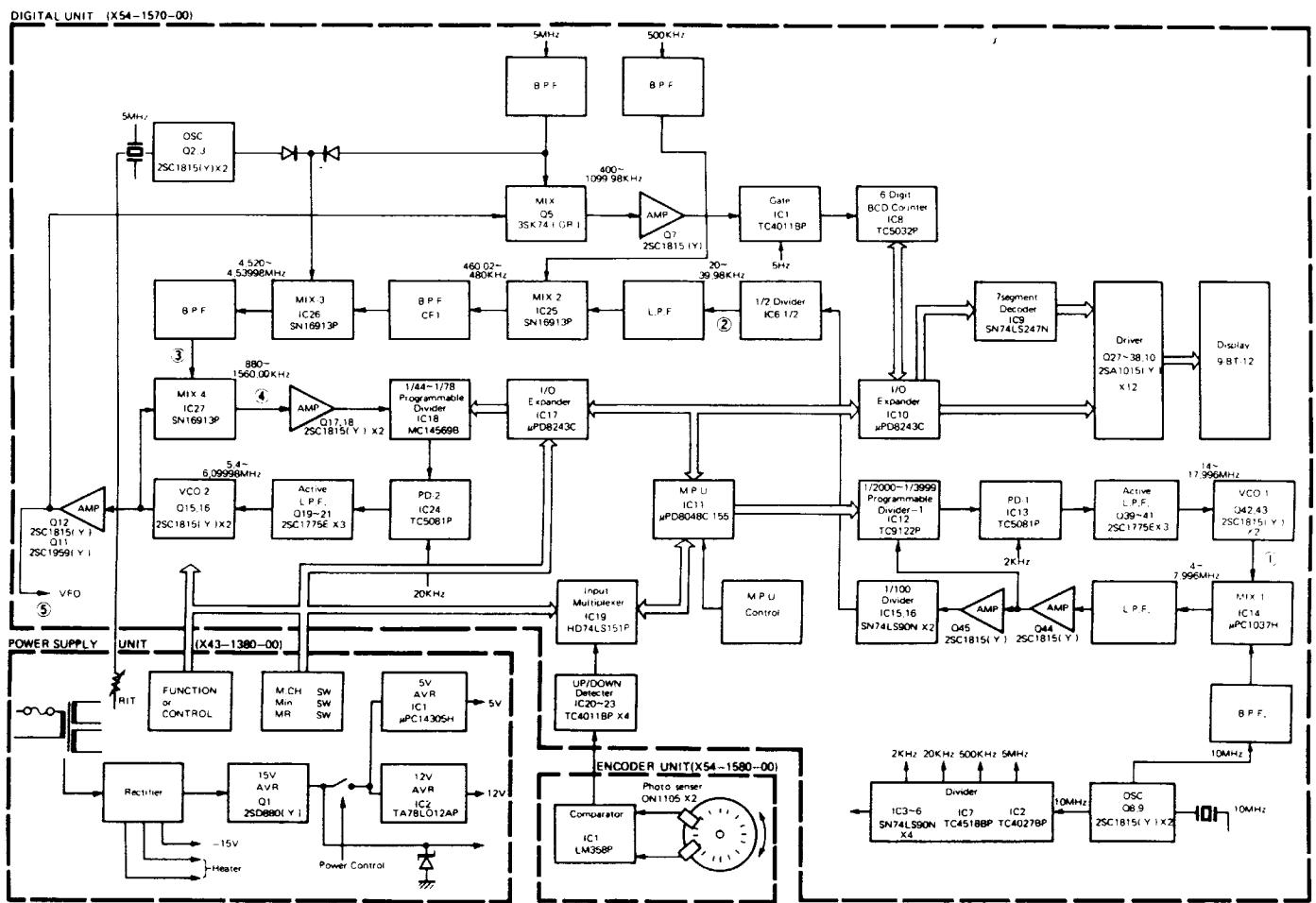
1. The figures shown are signal generator output required for a constant audio output with a constant AF gain control setting and AGC SW OFF. Set the AF gain control for 0.63/8Ω (50 mW) audio output 0 dB signal generator input at 14.2 MHz



NOTES:

1. Levels are measured at 14.2 MHz in the CW MODE and SG SW OFF. Carrier level is adjusted until the meter indicates the maximum on-scale ALC reading
2. All voltage measurements are read from an RF VTVM
3. A probe with a capacitance of less than 3PF should be used, and the ground should be made near the point of measurement

VFO-230



VFO-230 BLOCK DIAGRAM

Refer to Block Diagram

Display f. (kHz)	VFO f. (MHz) ⑤	IC12 14 Bit BCD data (1/M)	Q42VCO f. (MHz) ①	IC6 (Pin 12) output f. (kHz) ②	IC27 (Pin 5) input f. (kHz) ③	IC18 7 Bit binary (1/N)	IC27 output f. (kHz) ④
L 900.0	5.400.00	2 0 0 0 0	14.000	20.00	4520.00	N 44 0 1 0 1 1 0 0	880
L 900.0	5.400.02	2 0 0 0 2	14.004	20.02	4520.02	44 0 1 0 1 1 0 0	880
L 900.1	5.400.10	2 0 1 0 0	14.020	20.10	4520.10	44 0 1 0 1 1 0 0	880
L 900.8	5.401.60	2 0 8 0 0	14.160	20.80	4520.80	44 0 1 0 1 1 0 0	880
L 901.0	5.401.00	2 1 0 0 0	14.200	21.00	4521.00	44 0 1 0 1 1 0 0	880
L 909.9	5.409.98	2 9 9 8 8	15.896	29.98	4529.98	44 0 1 0 1 1 0 0	880
L 915.0	5.415.00	3 5 0 0 0	17.000	35.00	4535.00	44 0 1 0 1 1 0 0	880
L 919.9	5.419.98	3 9 9 8 8	17.996	39.98	4539.98	44 0 1 0 1 1 0 0	880
L 920.0	5.420.00	2 0 0 0 0	14.000	20.00	4520.00	45 0 1 0 1 1 0 1	900
L 950.0	5.450.00	3 0 0 0 0	16.000	30.00	4530.00	46 0 1 0 1 1 1 0	920
L 999.0	5.499.98	3 9 9 8 8	17.996	39.98	4539.98	48 0 1 1 0 0 0 0	960
0.0	5.500.00	2 0 0 0 0	14.000	20.00	4520.00	49 0 1 1 0 0 0 1	980
10.0	5.510.00	3 0 0 0 0	16.000	30.00	4530.00	49 0 1 1 0 0 0 1	980
100.0	5.600.00	2 0 0 0 0	14.000	20.00	4520.00	54 0 1 1 0 1 1 0	1080
200.0	5.700.00	2 0 0 0 0	14.000	20.00	4520.00	59 0 1 1 1 0 1 1	1180
300.0	5.800.00	2 0 0 0 0	14.000	20.00	4520.00	64 1 0 0 0 0 0 0	1280
400.0	5.900.00	2 0 0 0 0	14.000	20.00	4520.00	69 1 0 0 0 1 0 1	1380
500.0	6.000.00	2 0 0 0 0	14.000	20.00	4520.00	74 1 0 0 1 0 1 0	1480
599.9	6.099.98	3 9 9 8 8	17.996	39.98	4539.98	78 1 0 0 1 1 1 0	1560

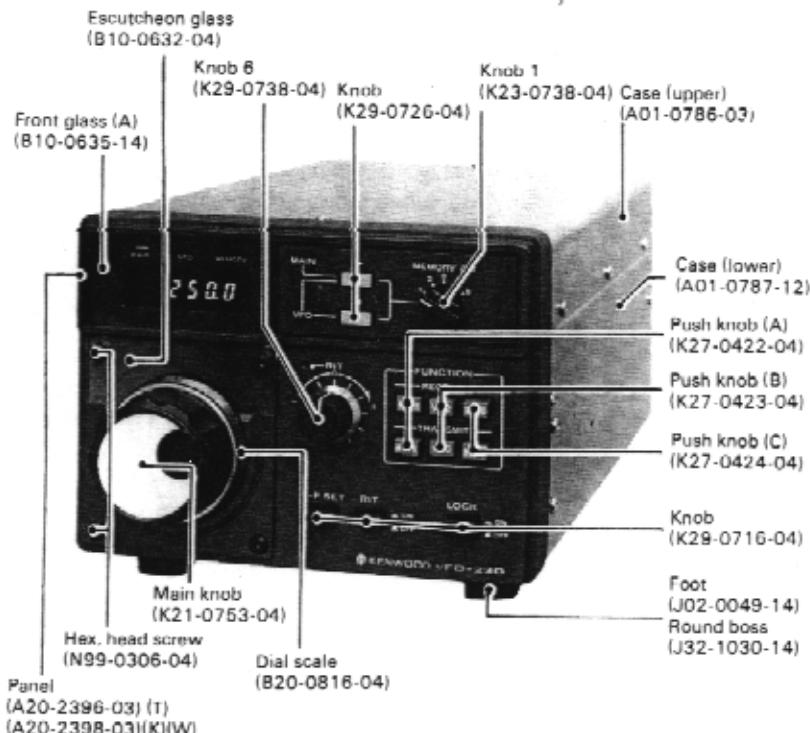
(Note) BAND: ○ ○ .0 MHz (ex. 14.0 MHz)

Table 13. Frequency chart

VFO-230

SPECIFICATIONS

Oscillation frequency	5.40 ~ 6.10 MHz
Output voltage	0.2V ±3 dB
Frequency stability	Within $\pm 1 \times 10^{-7}$ at 0 ~ 50°C within $\pm 3 \times 10^{-7}$ at room temp.
Power consumption	13W
Power requirement	AC 120V 50/60Hz (in U.S.A.) AC 220V/240V (Selectable) 50/60Hz (in Europe)
Semiconductors used	CPU LSI 1 FET 1 IC 29 Transistors 55 Diodes 57
Dimensions	180 (182) W 7.09" 133 (147) H 5.24" 287 (330) D mm 11.30" (Figures in () include projections.)
Weight	Approx. 3 kg (6.61 lbs.)



CIRCUIT CONFIGURATION

The VFO-230 consists of a double PLL loop: the 1st loop covering a 20 kHz bandwidth at 20 Hz step, and the 2nd loop covering a 700 kHz bandwidth at 20 kHz step.

As the tuning dial is turned, an optical encoder (using a photo sensor) converts the dial rotation and its direction of rotation into an electrical signal, which is coupled to microprocessor IC11 (μ PD8048C-155).

The microprocessor internally processes this dial signal and outputs the processed result as frequency-division data for the PLL and, at the same time, provides the corresponding dial display data.

In the 1st PLL loop, MIXER-1 (IC14: μ PC1037H) combines the output of VCO-1 (Q42, 43) at 14~17.998 MHz with a 10 MHz reference frequency to convert the output to 4.0~7.996 MHz. This output is amplified and coupled to Programmable Divider 1 (IC12), where it is frequency-divided according to the division data from the microprocessor into a 2 kHz signal.

The phase of this 2 kHz signal is compared with that of the reference 2 kHz signal by phase comparator PD-1 (IC13), and this output is fed back to control the output frequency of VCO-1.

The 4~7.998 MHz signal (at 4 kHz steps) generated in the 1st PLL loop is further frequency-divided by a 1/100 (IC15, 16) and 1/2 (IC6 1/2) divider into a 20~39.98 kHz signal (at a 20 Hz step). This signal is coupled to MIXER-2 (IC25), where it is mixed with a reference 500 kHz signal which converts the signal to 480~460.02 kHz. The output of the second mixer goes through a narrow band ceramic filter (CF1), then couples to a third mixer, MIX-3 (IC27), where it is further mixed with a 5 MHz signal. The signal is now converted to 4520~4539.98 kHz. A reference 5 MHz signal is usually used for the other input of this third mixer.

However, it is supplied from the crystal oscillator (Q2) for RIT operation or CW transmission. When the RIT feature is ON, the voltage applied across the varicap diode in the crystal oscillator is varied by the RIT control to obtain a frequency variation range of ± 900 Hz. During CW transmission, the voltage across the varicap diode is adjusted by variable resistor VR1 on the digital unit to provide this oscillator with an output frequency 800 Hz higher than its original frequency. The output of MIX-3 goes through a band-pass filter (T2, T3, T4), and then couples to a fourth mixer, MIX-4 (IC-26). Here it is mixed with the output of VCO-2 (5.4~6.09998 MHz) and is converted to 880~1560 kHz. This output, after amplification, is coupled to Programmable Divider 2 where it is frequency-divided to an output frequency of 20 kHz according to the division data supplied from the microprocessor. This output goes to phase detector PD-2 (IC24), where its phase is compared with the 20 kHz reference signal to create the frequency control signal for VCO-2.

All the circuits hitherto described are always operating. However, the digital VFO provides its output intermittently to control the buffer amplifier. When the VFO in the main unit is operating, this VFO output is mixed with a 5 MHz reference signal by MIX 5 (Q5), of which output at 400 kHz to 1099.98 kHz is amplified, then counted up by a 6-digit frequency counter (IC8). The output data of this counter is stored into the microprocessor's memory.

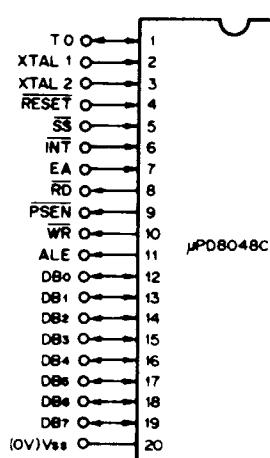
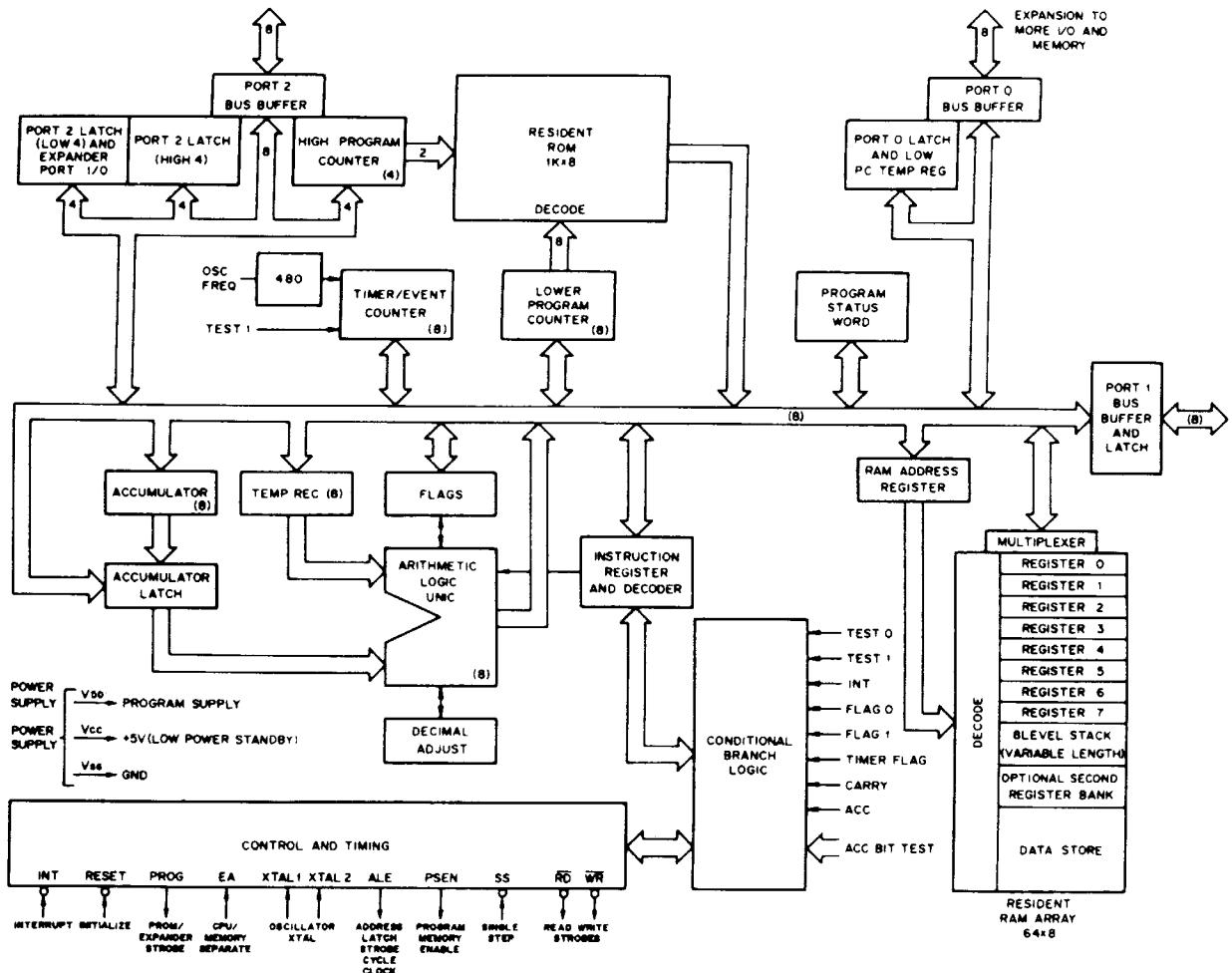
As seen above, all frequencies except the RIT and CW transmission shift frequencies are composed of combinations of reference frequencies. As a result, the accuracy and stability of the digital VFO output frequency is identical to that of the 10 MHz reference oscillator.

VFO-230

The frequency display resolution is identical to that of the main unit (down to 100 Hz) except for the RIT and CW transmission frequency display. This is accomplished by dynamically driving the fluorescent display tube based on the display and column data furnished by the microprocessor. The power to the VFO-230 is interlocked with that of the main unit. If, however, the VFO-230 is plugged into an AC outlet, the display, memory and output of the VFO remain live even when the power switch on the main unit is OFF. Therefore, when the main unit is again powered-on, the operating state set prior to power-off will reappear. Back-up power consumption for the VFO-230 is approximately 10 mA at 5V.

Item	Rating
Center frequency	470 kHz
6 dB bandwidth	+12.5 kHz or more
50 dB bandwidth	+25 kHz or less
Ripple (within 455 ± 8 kHz)	3 dB or less
Loss	4 dB or less
Guaranteed attenuation	35 dB or less within 455 ± 100 kHz
Input and output impedance	1.5 kΩ

Fig. 11 Ceramic filter (L71-0321-05) CFW470C
(DIGITAL UNIT, CF 1)



Terminals	
P10~P17	Input and output port (PORT 1)
P20~P27	Input and output port (PORT 2)
DB ₀ ~DB ₇	Data bus
T0, T1	Test
INT	Interrupt
RD	Read
WR	Write
ALE	Address latch enable
PSEN	Program store enable
RESET	Reset
SS	Single step
EA	External access
XTAL 1, 2	Crystal input

Maximum rating (Ta = 25°C)

Item	Symbol	Rating
Operating voltage	Vcc	-0.5 ~ +7.0V
	Vdd	-0.5 ~ +7.0V
Input voltage	Vi	-0.5 ~ +7.0V
Output voltage	Vo	-0.5 ~ +7.0V
Operating temperature	Topt	0 ~ +70°C
Storage temperature	Tstg	-65 ~ +150°C

Fig. 13 μPD8048C-155 (DIGITAL UNIT, IC11)

VFO-230

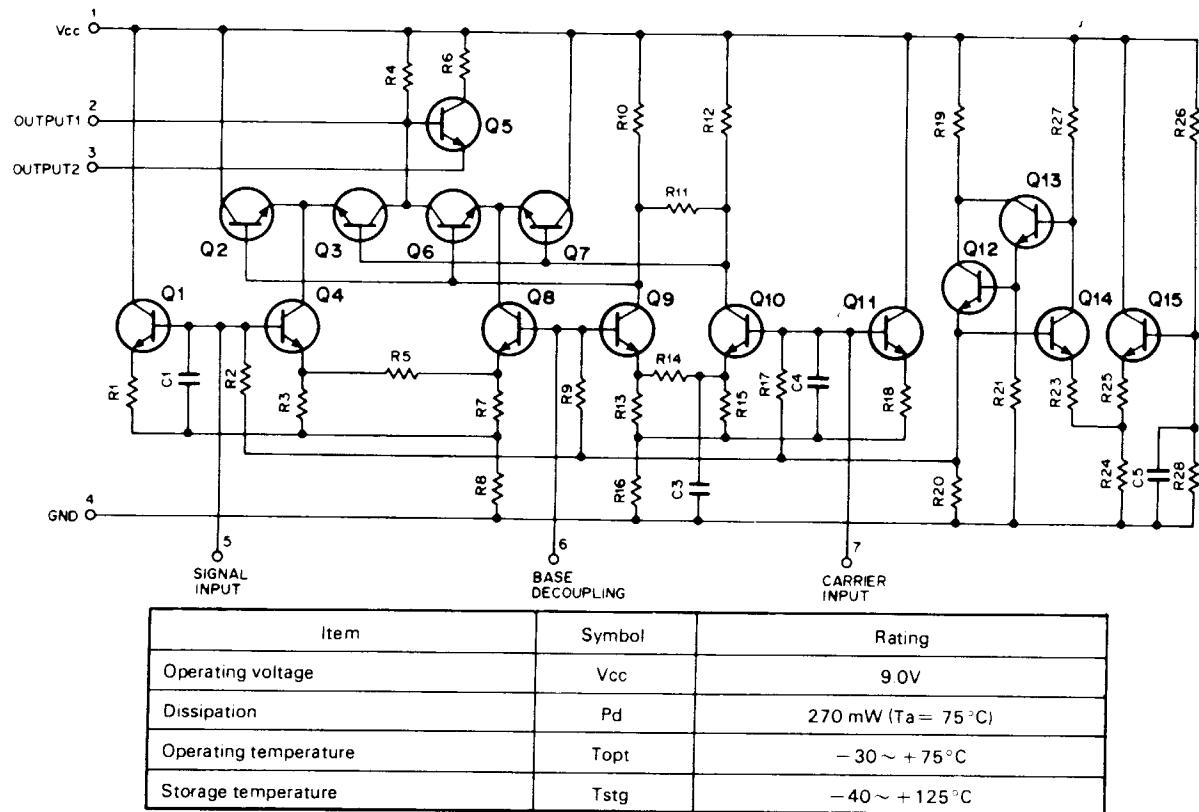
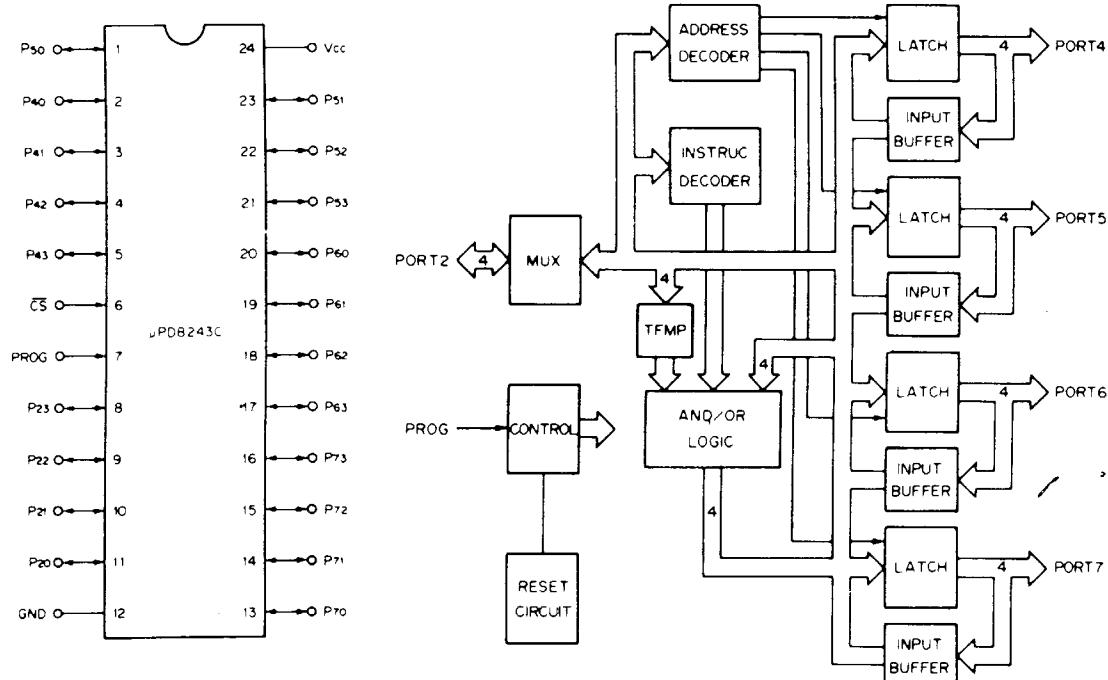


Fig. 14 μ PC1037H Double Balanced Modulator (DIGITAL UNIT, IC14)



Terminals

- $P_{20} \sim P_{23}$: Input Output (Port 2)
- $P_{40} \sim P_{43}$: Input Output port (Port 4)
- $P_{50} \sim P_{53}$: Input Output port (Port 5)
- $P_{60} \sim P_{63}$: Input Output port (Port 6)
- $P_{70} \sim P_{73}$: Input Output port (Port 7)
- \bar{CS} : Chip Select
- PROG : Program pulse
- Input Output port (Port 2)

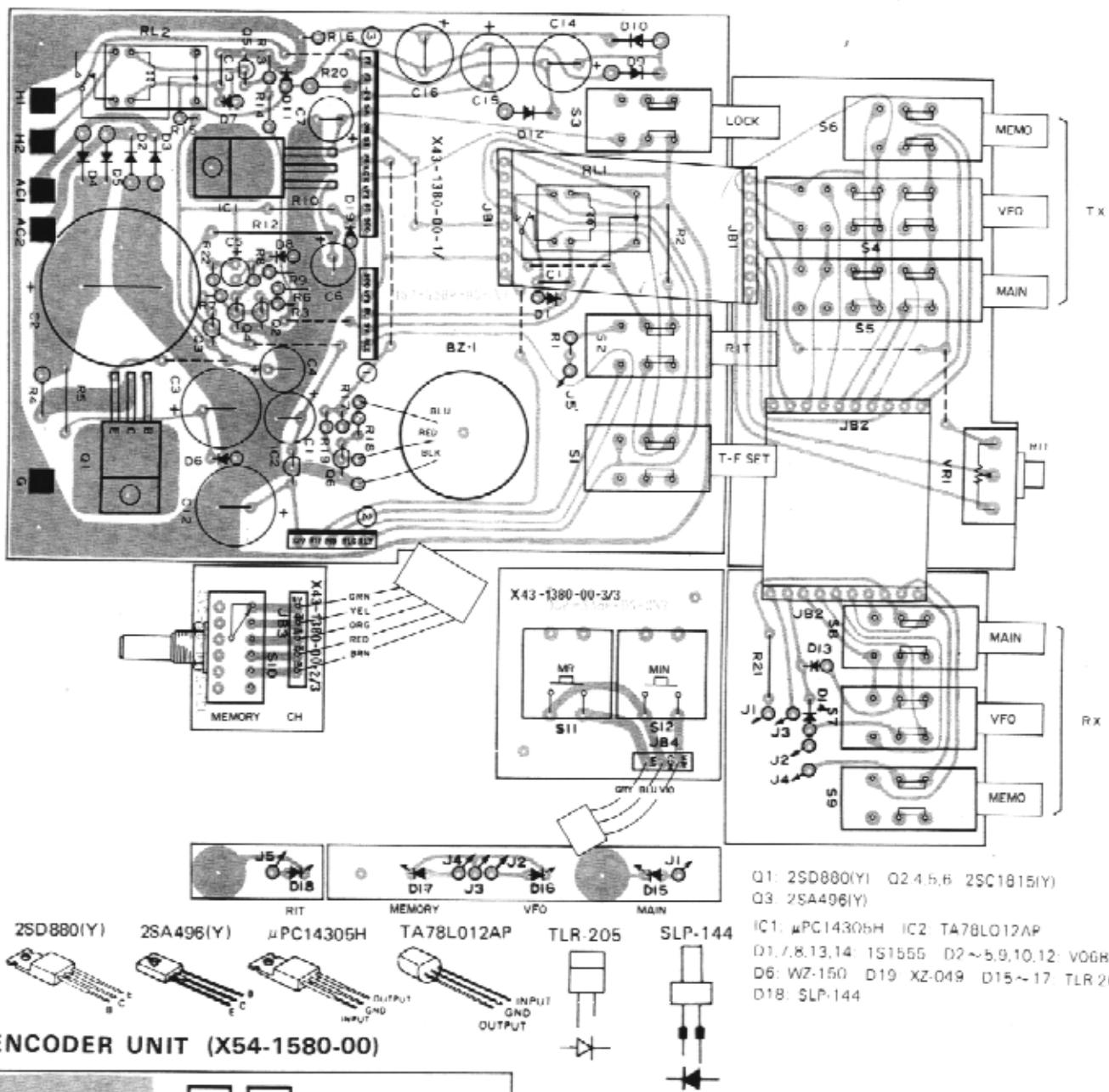
Maximum Rating (Ta = 25°C)

Item	Symbol	Rating
Operating voltage	V _{cc}	-0.5 ~ +7V
Input voltage	V _i	-0.5 ~ +7V
Output voltage	V _o	-0.5 ~ +7V
Operating temperature	T _{opt}	0 ~ +70°C
Storage temperature	T _{stg}	-60 ~ +150°C

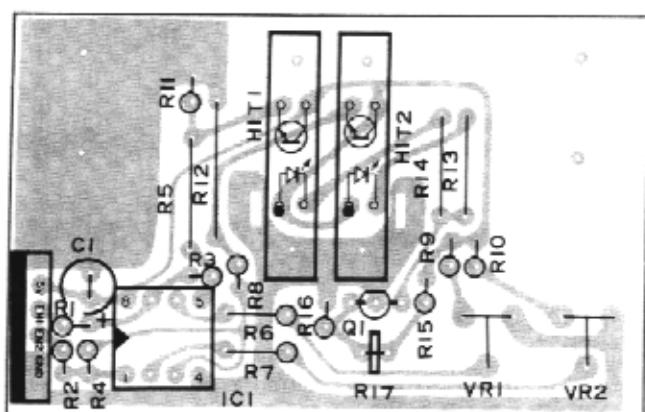
Fig. 15 μ PD8243C (DIGITAL UNIT, IC10, 17)

VFO-230

▼ POWER SUPPLY UNIT (X43-1380-00)

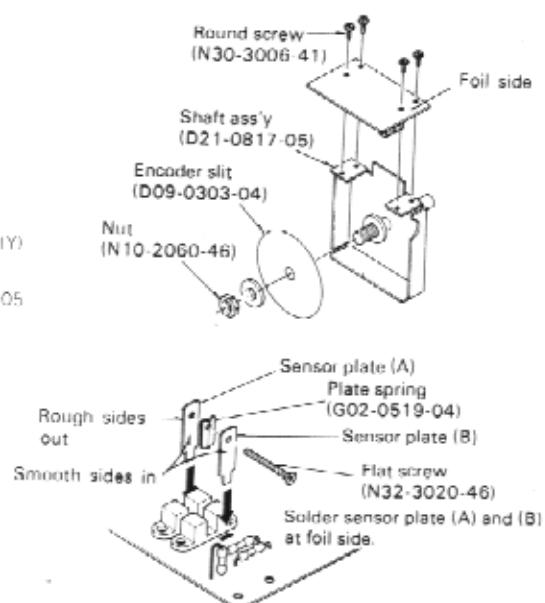


▼ ENCODER UNIT (X54-1580-00)



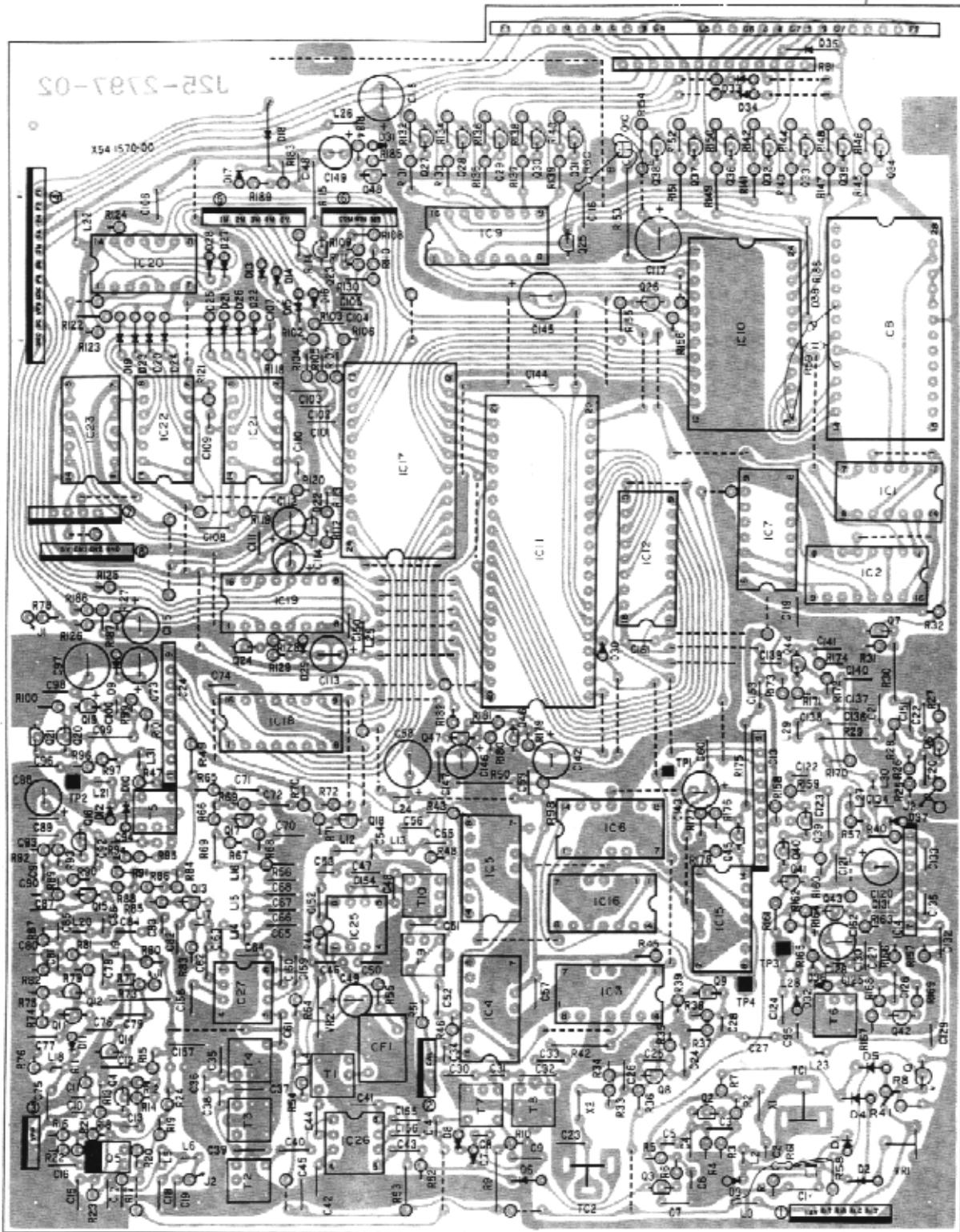
Max. Rating							
VR	I _F	I _D	I _C	V _{CE(sat)}	V _{CEO}	P _{DC}	T _{case}
3V	1V	75mA	20mA	30V	5V	100mW	-25 ~ +85

Component Identifications:
 2SD880(Y) 2SA496(Y) μPC14305H TA78L012AP TLR-205 SLP-144
 Round screw (N30-3006-41)
 Shaft ass'y (D21-0817-05)
 Encoder slit (D09-0303-04)
 Nut (N10-2060-46)



VFO-230

DIGITAL UNIT (X54-1570-00)



Q1~4,6~9,12,13,15~18,23~26,42~46: 2SC1815(Y) Q5 3SK73(GR) Q10,22,27~30,47,48: 2SA1015(Y) Q11,14: 2SC1959(Y)
Q19~21,39~41: 2SC1776(E)

IC1,20~23: TC4011BP IC2: TC4027BP IC3~6,15,16: SN74LS90N IC7: TC4518BP IC8: TC5032P IC9: SN74LS247N IC10,17: μPD8243C
IC11: μPD8048C-15S IC12: TC9122P IC13,24: TC5061P IC14: μPC103/H IC18: MC14569B IC19: HD74LS151P IC25~27: SN16913P
D1,2,4,5,13~31,33~35,30: 1S1555 D3,10,12,32,36: 1SV54GC D6~8,11: 1S1587 D9: W2-050 D37: WZ-061

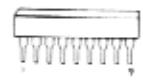
2SA1015(Y)
2SC1775(E)
2SC1815(Y)
2SC1959(Y)



3SK73(GR)



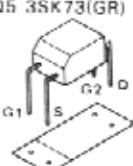
TC5081P



μPC103H



Attachment direction of
Q5 3SK73(GR)



VFO-230

VFO-230 Semiconductor

☆: New Parts

VFO-230 General

Item	Name	Parts No.	Re-marks
Diode	1S1555	V11-0076-05	
	1S1587	V11-0370-05	
	VO6B	V11-0219-05	
Vari-cap diode	1SV54GC	V11-4173-46	☆
Thermistor	25D29	V11-3360-16 500Ω at 25°C	
Zener diode	XZ-049	V11-4175-46	
	WZ-050	V11-4102-10	
	WZ-061	V11-0243-05	
	WZ-110	V11-4161-46	☆
	WZ-150	V11-0307-05	
LED	SLP-144	V11-6172-76	
	TLR-205	V11-3162-96	
Photo interrupter	ON 1105	V11-1173-76	☆
Display tube	9-BT-12	V40-7760-86	
TR	2SA496 (Y)	V01-0113-05	
	2SA1015 (Y)	V01-1015-06	
	2SC1775 (E)	V03-1775-06	
	2SC1815 (Y)	V03-1815-06	
	2SC1959 (Y)	V03-1959-06	
	2SD880 (Y)	V04-0880-16	
FET	3SK73 (GR)	V09-1002-46	
IC	HD74LS151P	V30-1008-26	
	LM358P	V30-1024-56	
	MC14569B	V30-1100-06	
	SN16913P	V30-1048-06	
	SN74LS90N	V30-1005-26	
	SN74LS247N	V30-1030-56	
	TA78L012AP	V30-1189-16	
TC4011BP		V30-0301-70	
TC4027BP		V30-1050-06	
TC4518BP		V30-1039-06	
TC5032P		V30-1091-06	
TC5081P		V30-1132-06	
TC9122P		V30-1036-16	
μPC1037H		V30-1179-16	☆
μPC14305H		V30-1029-36	
μPD8048C-155		V30-1176-26	☆
μPD8243C		V30-1177-16	☆

Ref No.	Parts No.	Description	Re-marks
	A01-0786-03	Case (Upper)	☆
	A01-0787-12	Case (Lower)	☆
	A20-2398-03	Panel (K)(W)	☆
	A20-2396-03	Panel (T)	☆
	B09-0011-04	Cap	
	B10-0632-04	Escutcheon glass	
	B10-0635-14	Front glass (A)	☆
	B10-0636-04	Front glass (B)	☆
	B20-0816-04	Dial scale (B)	
	B46-0058-10	Warranty card (K)	
	B50-2753-00	Operating manual (K)(W)	☆
	B50-2754-00	Operating manual (T)	☆
	E06-0852-05	8P DIN socket	
	E14-0101-05	Pin plug	
	E13-0101-05	Pin jack	
	E30-0181-05	AC cord with plug (K)	
	E30-0585-05	AC cord with plug (W)	
	E30-0602-05	AC cord with plug (T)	
	E30-1632-05	Gnd cable	
	E30-1672-05	VFO cable	☆
	F05-3011-05	Fuse 0.3A (T)(W)	☆
	F05-5011-05	Fuse 0.5A (K)	
	G01-0804-04	Coil spring	
	G09-0410-05	D spring	
	H01-2711-04	Carton case (inside) (K)(W)	☆
	H01-2712-04	Carton case (inside)(T)	☆
	H10-2545-02	Packing fixture (F)	
	H10-2525-02	Packing fixture (R)	
	H20-0276-03	Protective cover	
	H25-0117-04	Accessories bag	
	J02-0049-14	Foot (large)	
	J13-0407-15	Fuse holder	
	J32-0714-04	Hex. boss	
	J32-1030-14	Round boss (foot)	
	J41-0006-05	Cord bush (K)	
	J41-0024-15	Cord bush (T)(W)	
	K21-0753-04	Main knob	
	K23-0738-04	Knob 1	
	K27-0422-04	Push knob (A), Main	
	K27-0423-04	Push knob (B), VFO	☆
	K27-0424-04	Push knob (C), Memo	☆
	K29-0716-04	Knob	
	K29-0726-04	Knob, Memo	
	K29-0738-04	Knob 6	
	L01-8044-05	Power trans	☆
L2, 4	L40-1011-04	Ferri-inductor 100μH	
L1, 3, 5	L40-1511-03	Ferri-inductor 150μH	
L5, 6	L40-6891-13	Ferri-inductor 6.8μH	
	N09-0256-05	Gnd screw	
	N14-0115-05	Flange nut (Hex bolt)	
	N14-0404-04	Flange nut (Pow trans)	
	N14-0509-05	Wing nut, GND	
	N14-0519-05	Fastener (Hex bolt)	☆

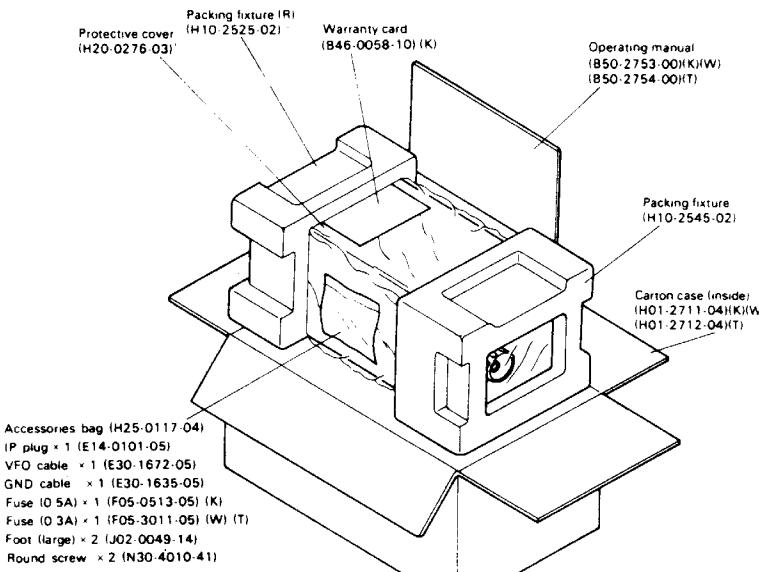
VFO-230

Encoder unit (X54-1580-00)

Ref. No.	Parts No.	Description			Re-marks
C137	CC45SL1H470J	C	47pF		
C142	CE04W1H2R2M	E	2.2 μ F	50V	
C143	CE04W1A101M	E	100 μ F	10V	
C144	C91-0456-05	C	0.047 μ F		
C145	CE04W1A221M	E	220 μ F	10V	
C146	CE04W1A470Q	E	47 μ F	10V	
C147	C91-0456-05	C	0.047 μ F		
C149	CE04W1H4R7Q	E	4.7 μ F	50V	
C152	CQ92M1H473K	ML	0.047 μ F	50V	
C153	CC45SL1H150J	C	15pF		
C161	C91-0456-05	C	0.0047 μ F		
C162	CE04W1C470M	E	47 μ F	16V	
		E23-0046-04	Square terminal		
		G13-0635-04	Cushion		
TC1	C05-0048-05	Ceramic trimmer	70pF		
TC2	C05-0035-05	Ceramic trimmer	50pF		
L1	L40-1021-03	Ferri-inductor	1mH		
L2	L40-4701-03	Ferri-inductor	47 μ H		
L3	L40-1511-03	Ferri-inductor	150 μ H		
L4	L40-4711-03	Ferri-inductor	470 μ H		
L5, 6	L40-2211-03	Ferri-inductor	220 μ H		
L7 ~ 11	Not Used				
L12-13	L40-3325-04	Ferri-inductor	3.3mH		
L14 ~ 16	L40-1011-03	Ferri-inductor	100 μ H		
L17 ~ 19	L40-4711-03	Ferri-inductor	470 μ H		
L20	L40-1501-03	Ferri-inductor	15 μ H		
L21	L40-4711-03	Ferri-inductor	470 μ H		
L22	L40-1511-04	Ferri-inductor	150 μ H		
L23	L40-1501-03	Ferri-inductor	15 μ H		
L24, 25	L40-1511-03	Ferri-inductor	150 μ H		
L26	L40-1511-04	Ferri-inductor	150 μ H		
L27	L40-1021-03	Ferri-inductor	1mH		
L28	L40-1511-03	Ferri-inductor	150 μ H		
L29, 30	L40-1001-03	Ferri-inductor	10 μ H		
L31	L40-1021-03	Ferri-inductor	1mH		
T1	L34-0991-05	Tuning coil	470kHz	☆	
T2 ~ 4	L34-0996-15	Tuning Coil	4.5 MHz	☆	
T5	L32-0636-05	Oscillating Coil		☆	
T6	L32-0193-05	Oscillating coil		☆	
T7, 8	L34-0996-15	Tuning coil	4.5MHz	☆	
T9, 10	L34-0991-05	Tuning coil	470kHz	☆	
CF1	L72-0321-05	Ceramic filter	CFW470C	☆	
X1	L77-0874-05	Crystal	5MHz	☆	
X2	L77-0482-05	Crystal	10MHz	☆	
VR1	R12-3045-05	Trim. pot	10k Ω (B)		
VR2	R12-5014-05	Trim. pot	100k Ω (B)		
RB1	R90-0523-05	Rosistor block	47k Ω × 12		
	R92-0150-05	Short jumper			

Ref. No.	Parts No.	Description			re-marks
C1	CE04W1A470Q	E	47 μ F	10V	
	D09-0303-04	Encoder slit			☆
	D21-0817-05	Shaft ass'y			☆
	G02-0519-04	Plate spring			☆
VR1, 2	R12-1040-05	Trim pot 4.7 k Ω (B)			

PACKING



ADJUSTMENTS

TEST EQUIPMENT REQUIRED

1. VTVM or DVM

- 1) Input resistance: More than 1 M Ω
- 2) Voltage range: 1.5 to 1000V AC/DC

NOTE:

A high-precision voltmeter may be used.
However, accurate readings can not be obtained for high-impedance circuits.

2. RF VTVM

- 1) Input impedance: 1 M Ω and less than 3 pF.
- 2) Voltage range: 10 mV to 300V
- 3) Frequency range 50 MHz or greater

3. OSCILLOSCOPE

Requires high sensitivity and external synchronization capability.

4. STANDARD SIGNAL GENERATOR (SSG)

- 1) Frequency range: 1.8 to 30 MHz
- 2) Output: -20dB/0.1 μ V ~ 120 dB/1V
- 3) Output Z = 50 Ω
Generator must be frequency stable

5. FREQUENCY COUNTER

- 1) Minimum input voltage: 50 mV
- 2) Frequency range: Greater than 50 MHz

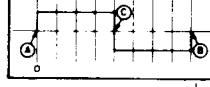
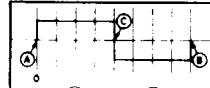
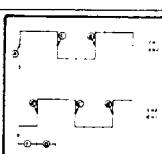
VFO-230

ADJUSTMENTS

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
Setting	<p>VFO-230: Front panel</p> <ul style="list-style-type: none"> • MEMORY CH : 1 • FUNCTION RECEIVE : VFO TRANSMIT : VFO • RIT : Centered • RIT SW : OFF • LOCK SW : OFF • T.F SET : OFF <p>TS-830S</p> <ul style="list-style-type: none"> • BAND : 14 • MODE : CW • POWER : ON • SG SW rear panel : OFF • CAL SW : OFF 								
Use an insulated tuning tool for adjusting trimmer capacitors, coil slugs, etc.									

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
1. Reference oscillator adjustment		F.counter	Digi-tal	TP1	Digi-tal	TC2		5.000MHz±5 Hz	
	[Calibration using the TS-830S] Interconnect the TS-830S ANT terminal and the VFO-230 CAL terminal. Set the FUNCTION (REC) switch on the VFO-230 to MAIN. With the TS-830, receive a beat frequency at around 14.000, then set the CAL switch to ON (a double beat will be obtained.).						Obtain a zero beat between the double beats.		
	VFO-230: Set the FUNCTION (REC) switch to VFO. Turn the dial to check the following frequencies: L900.0, 0.0, 100.0, 200.0, 300.0, 400.0, 500.0						Check the frequency departure from the display on the TS-830.	Within ±100 Hz.	Check
2. PLL-1 adjustment	Turn the control until 19.9 is obtained on the display.	F.counter	Digi-tal	TP4				798 kHz	Check
		DVM		TP3	Digi-tal	T6	Set to 9.5V		
	Turn the control to scan the displayed frequency from 0.0 to 19.9.		Digi-tal					2~9.5V±0.5V	Check
3. PLL-2 adjustment	Set the dial to display a frequency of 10.0	RF VTVM	Digi-tal	IC26 ②	Digi-tal	T9 T10	Maximum (There should be a peak.)		
				IC26 ①		T1 T7 T8	Maximum (Broad at T1)		
				Q17 ⑧		T2 T3 T4	Maximum		
	Set the dial to a frequency display of 599.9.	DVM	Digi-tal	TP2		T5	Set to 9.5V		
								2.0~9.5V±0.5V	Check
	Turn the dial to scan displayed frequency from L900.0 to 599.9.	F.counter	Rear panel	DIN connector ①				5.4~6.1 MHz	
		RF VTVM	Digi-tal	Q17 ⑧	Digi-tal	T4	Reset to maximum		

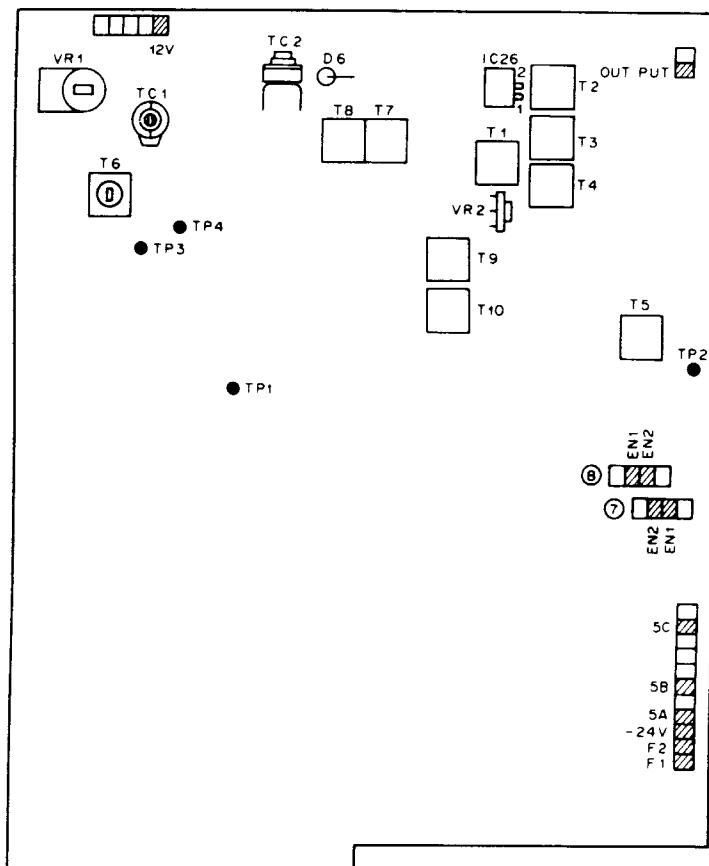
VFO-230

Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
4. RIT adjustment	RIT control: Centered DISPLAY: 500.0	F.counter	Digital	Cathode of D6	Digital	TC1	Adjust so that the frequency counter reading does not change at RIT SW ON/OFF		
	RIT SW : ON RIT Control: Fully clockwise						Bracketed values are the TS-830S display indication.	More than 5,000.9 kHz (More than 14,500.9)	Check
	RIT SW : ON RIT Control: Fully counter-clockwise							Less than 4,999.1 kHz (Less than 14,499.1)	
	RIT SW : OFF							5,000.0 kHz (14,500.0)	
5. CW SHIFT adjustment	TS-830 MODE : TUN VFO-230 RIT : OFF FUNCTION (REC) : VFO	F.counter	Digital	Cathode of D6	Digital	VR1	TS-830 MODE: Adjust until a frequency 800 Hz higher than the CW frequency is obtained.	800Hz±50 Hz	
	TS-830 MODE : CW N STBY SW : SEND VFO-230 RIT : OFF FUNCTION (TRA) : VFO						TS-830 STBY: The frequency is shifted by 800 Hz with respect to REC freq.		
	TS-830 STBY SW : REC								
6. Encoder adjustment	Remove the VFO knob and motor-drive the encoder at approx 300 rpm.	Oscillo-scope	Digital	EN 1 terminal of Connectors ⑦ or ⑧	Encoder	VR1			Point ④ may be located anywhere. When a motor is not available, manually turn the VFO control to check the duty ratio.
	EN 1 duty ratio adjustment: Turn a motor clockwise and counterclockwise.								
	EN 2 duty ratio adjustment: Turn a motor in the both direction.					VR2	Adjust until intervals ④ and ⑤ are equal to each other with point ③ placed at the center.		After adjusting with the VFO control turned clockwise, check that intervals ④ and ⑤ are also identical when the VFO control is turned counterclockwise
	EN1-EN2 phase difference alignment: Same as above.							EN1 (EN2): Within 90 deg ± 10% (The difference between clockwise and counterclockwise rotation must also be within this specification.)	

VFO-230 PARTS LAYOUT/AC VOLTAGE CONVERSION

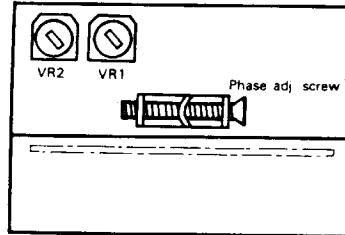
Item	Condition	Measurement			Adjustment			Specification	Remarks
		Test equipment	Unit	Terminal	Unit	Parts	Method		
7 Beat adjustment	TS-830 AGC FAST RIT OFF RF ATT OFF VBT NORM IF SHIFT Centered MODE USB VFO-230 FUNCTION (REC) VFO DISPLAY (Dial) 200 Connect the SSG output to the ANT terminal on the TS-830S						Adjust the SSG output frequency until a maximum S-meter reading is obtained on the TS-830S		
	Adjust the SSG attenuator until the S meter on the TS-830S indicates 20 dB								
	Set the VFO-230's dial to 20.8 (zero beat)	Obtain a beat from the TS-830	Digital	VR2	Adjust to the minimum beat level				
	Turn the dial on the VFO-230 from 0.0 to around 50.0 while listening to the signals						An abnormal beat should not be heard	Check	

▼ DIGITAL UNIT (X54-1570-00)



DISPLAY TUBE

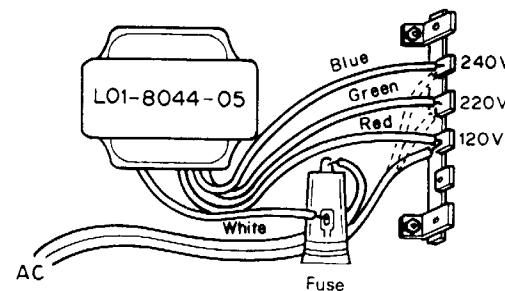
▼ ENCODER UNIT (X54-1580-00)

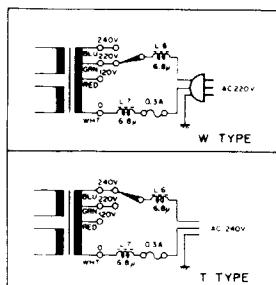


VFO-230 (K) AC Voltage conversion

To operate the VFO-230 (K) on 240V AC, the power transformer primary tap must be rewired from 120V to either the 220V or 240V tap.

1. Unplug the AC power cable and VFO interconnecting cable.
2. Remove the top cover.
3. Move the AC line from the 120V (Red) to either the 220V (Green) or 240V (Blue) transformer winding.
4. Change the AC fuse from 0.5A to 0.3A. Tag the power cord at the back of the unit to indicate that the transformer is wired for 240V AC, and the power fuse should be 0.3A and not 0.5A.
5. Replace the top cover and cable up to verify your work.

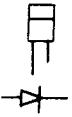




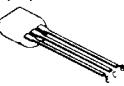
SLP-144



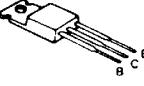
TLR-205



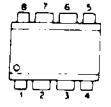
2SA496(Y)

2SA1015(Y)
2SC1775(E)
2SC1815(Y)
2SC1959(Y)

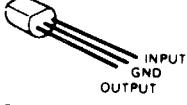
2SD880(Y)



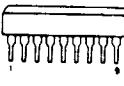
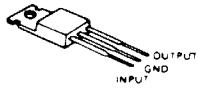
3SK73(GR)

LM358P
SN16913P

TA78L012AP



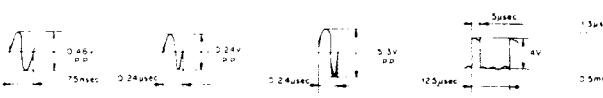
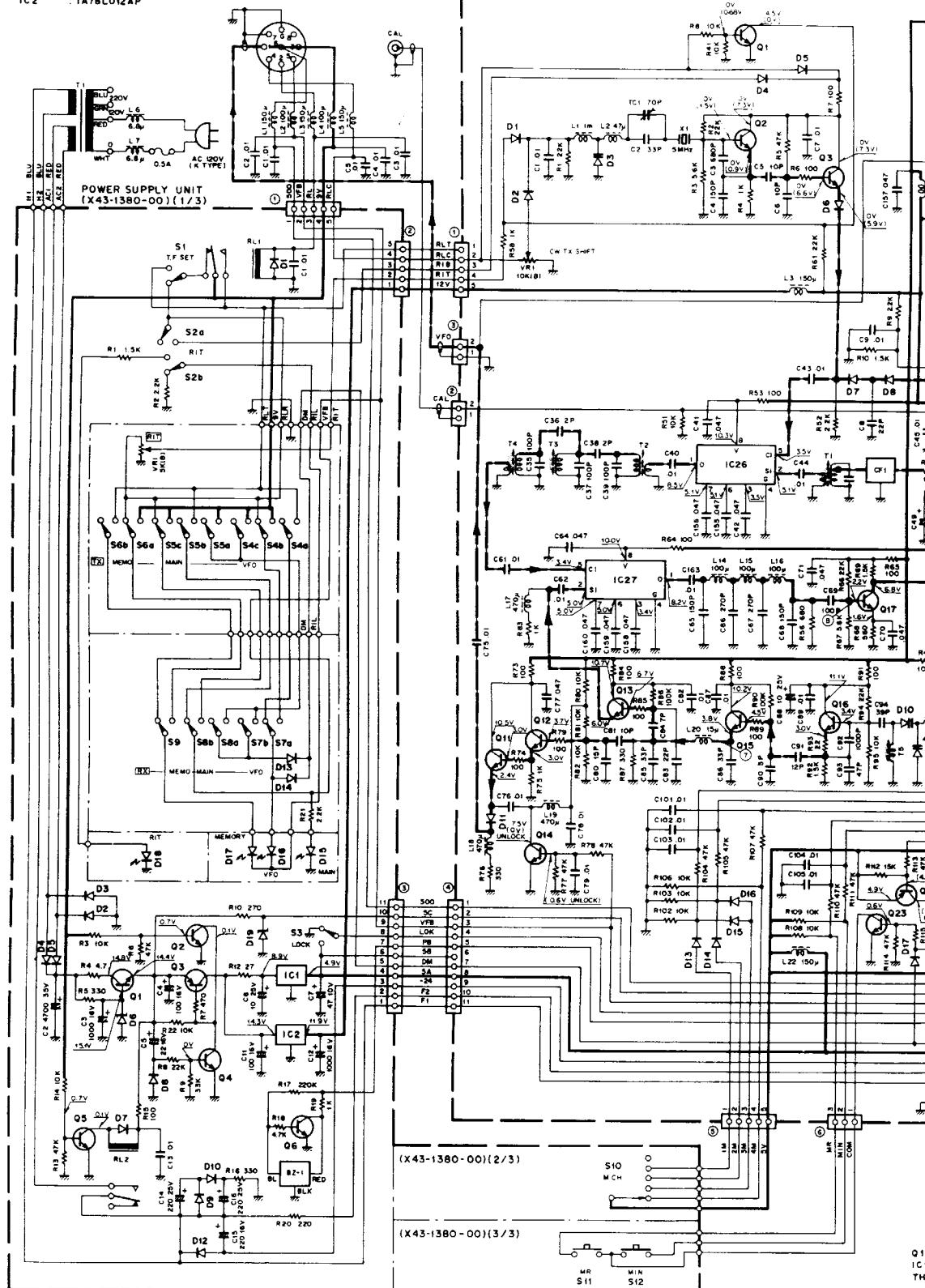
TC5081P

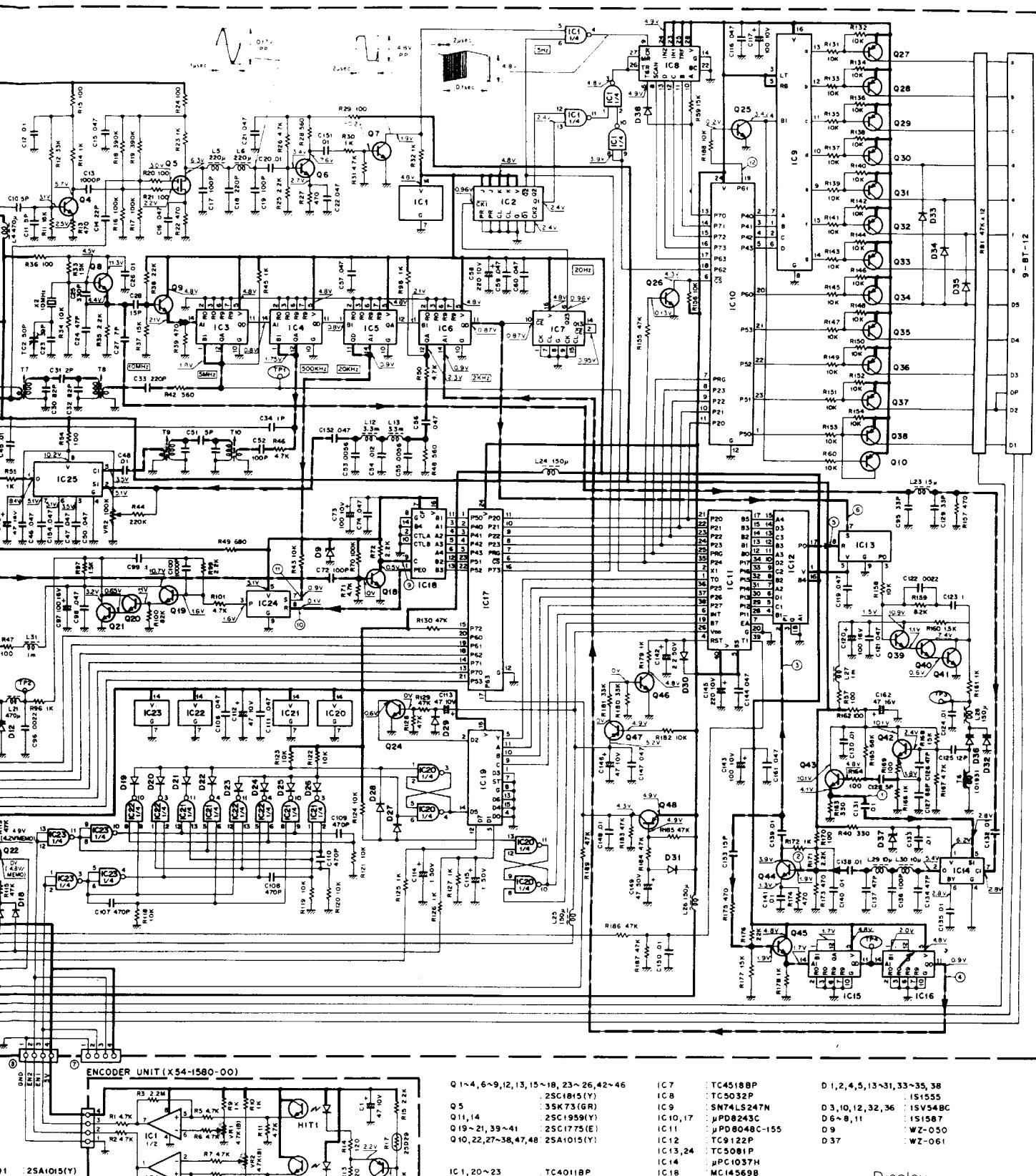
 μ PC1037H μ PC14305H

D1 2SD880(Y) D1,7,8,13,14 IS1555 D15~17 TLR-205
 Q2,4~6 2SC1815(Y) D2~5,9,10,12 V06B D16 SLP-144
 Q3 2SA496(Y) D6 WZ-150 D19 XZ-049

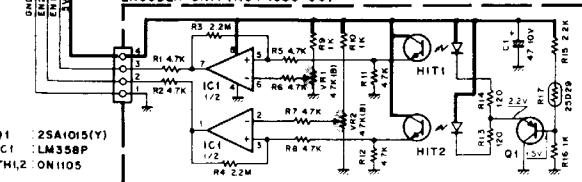
IC1 μ PC14305H
 IC2 TA78L012AP

DIGITAL UNIT (X54-1570-00)





ENCODER UNIT (X54-1580-00)



Q 1~4, 6~9, 12, 13, 15~18, 23~26, 42~46
 . 2SA1015(Y)
 . 2SA105(Y)
 . 2SC1959(Y)
 . 2SC1775(E)
 . 2SA1015(Y)
 . 2SC1959(Y)
 . 2SC1775(E)
 . 2SA1015(Y)

IC 1, 20~23
 IC 2
 IC 3~6, 15, 16

IC 7

TC45188P

TC5032P

SN74LS247N

IC 9

IC 10, 17

μPD8243C

μPD8048C-155

TC9122P

IC 12

TC5081P

IC 13, 24

μPC1037H

IC 14

MC14569B

IC 18

HD74LS151P

IC 19

SN16913P

IC 25~27

D 1, 2, 4, 5, 13~31, 33~35, 38

IS1555

D 3, 10, 12, 32, 36

ISV54BC

D 6~8, 11

IS1587

D 9

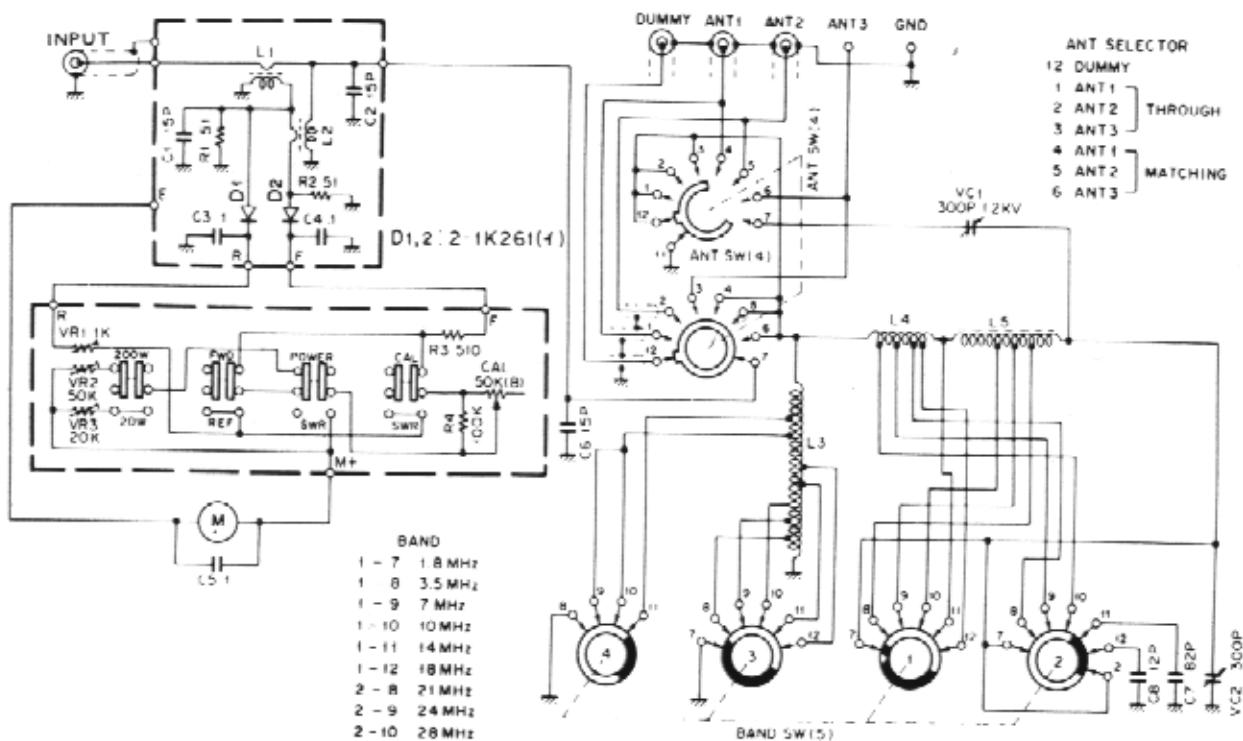
WZ-050

D 37

WZ-061

Display

< Note > Wave forms are displayed at 0.0 (VFO output 5.5 MHz)

**SPECIFICATIONS****ANTENNA COUPLER**

Frequency Range	9 amateur bands from 1.8 to 29.7 MHz
Input Impedance	50Ω
Output Impedance	10 to 500Ω, unbalanced
Through Power	200W at max.
Insertion Loss	Less than 0.5 dB in matched state
Pi-L	1.8 MHz only
Output Impedance	20 to 500Ω, unbalanced
Through Power	100W at max.

WATTMETER

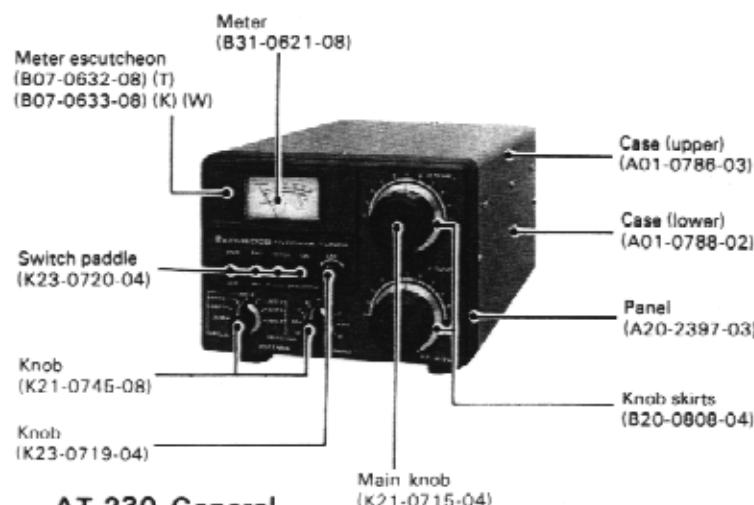
Type	Through-line wattmeter
Frequency Range	1.8 to 30 MHz
Measurable RF Power	Up to 20/200W, switched
Kinds of RF Power	Forward or reflected power, switched
Net weight	3.4 kg (7.5 lbs.) approx.
Impedance	50Ω
Accuracy	Better than ±10% of full scale

SWR METER

SWR Detection	Toroid core direction coupler
Measurable Range	1.1 to 10
Min. Power Required	4W

GENERAL**Connectors**

INPUT	UHF type, 50Ω
ANT-1	UHF type
ANT-2	UHF type
ANT-3	Stud and wing nut
Dimensions	W 180 mm (7 3/32") H 133 mm (5-15/64") D 288 mm (11-11/32")

**AT-230 General**

Ref. No.	Parts No.	Description	Re marks
	A01-0788-03	Case (upper)	☆
	A01-0788-02	Case (lower)	☆
	A20-2397-02	Panel	☆
	B07-0632-08	Meter escutcheon (T)	☆
	B07-0633-08	Meter escutcheon (K)(W)	☆
	B20-0808-04	Knob skirt	
	B31-0621-08	Meter	
	B46-0058-00	Warranty card (K)	
	B50-2756-08	Operating manual (K)(W)	☆
	B50-2757-08	Operating manual (T)	☆
VC1, 2	C02-0016-08	Variable cap 300pF 1.2kV	
C7	C91-0419-05	C 82pF 3kV	☆
C8	C91-0402-05	C 12pF 3kV	☆
	D23-0061-04	Bearing	

AT-230

Ref. No.	Parts No.	Description	Re-marks
	E04-0102-05	UHF type receptacle	
	E23-0015-04	Gnd lug ϕ 3	
	E23-0408-05	Terminal	
	F09-0402-05	Insulating cover	
	F29-0402-08	Insulator	
	G13-0621-04	Cushion	
	H01-2714-04	Carton case (inside)(K)(W)	☆
	H01-2715-04	Carton case (inside)(T)	☆
	H10-2525-02	Packing fixture (R)	☆
	H10-2545-02	Packing fixture (F)	☆
	J02-0049-14	Foot (large)	
	J32-1030-04	Round boss	
	K21-0715-04	Main knob R, X Tune	
	K21-0745-08	Knob, BAND, ANT	
	K23-0719-04	Knob, CAL	
	K23-0720-04	Switch paddle	
L3	L34-0992-08	Coil	☆
L4	L34-0993-08	Coil	☆
L5	L34-0994-08	Coil	☆
	N14-0506-08	Wing nut, M5 ANT3. GND	
	S01-2418-08	Rotary switch, ANT	
	S01-5404-08	Rotary switch, BAND	☆
	W02-0302-08	Switch module	
	W02-0303-08	Detector module	

Switch module (W02-0302-08)

Ref. No.	Parts No.	Description	Re-marks
VR1	R12-1024-05	Trim. pot $1k\Omega$ (B)	
VR2	R12-4020-05	Trim. pot $50k\Omega$ (B)	
VR3	R12-3420-08	Trim. pot $20k\Omega$ (B)	
VR4	R05-3407-08	Pot. $50k\Omega$ (B) CAL	
R3	RD14BB2E511J	Carbon resistor $510\Omega \pm 5\% 1/4W$	
R4	RD14BB2E104J	Carbon resistor $100k\Omega \pm 5\% 1/4W$	
	S36-2026-15	Paddle switch x 4	

Detector module (W02-0303-08)

Ref. No.	Parts No.	Description	Re-marks
C1, 2	FM05ZC150J5	Mica $15pF$ $500V$	
C6	FM05ZC150J5	Mica $15pF$ $500V$	
R1, 2	RD14BB2E510J	Carbon resistor $51\Omega \pm 5\% 1/4W$	
D1, 2	V11-7763-26	Diode 2-1K261 x 2	
L1, 2	L39-0403-08	Detector coil	

1. INSTRUMENTS

1) Terminated Power Meter

1. Frequency range: 50 MHz or more
2. Input impedance 50Ω
3. Power range: 20W and 200W

2) HF Transmitter or Transceiver

- 1) Output: 100W CW
Variable to 10W
Example: TS-830, TS-820S

2. POWER METER ADJUSTMENT

1) Test Equipment Connection

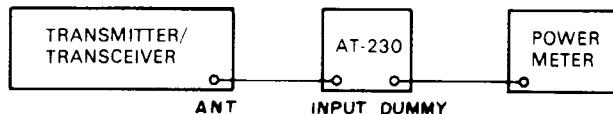


Fig. 1

2) Adjustment

1. Connect as in Fig. 1.
2. Unless otherwise specified, controls should be set as follows:

ANT SW	DUMMY
BAND SW	14
200W/20W SW	200W
FWD/REF SW	FWD
POWER/SWR SW	POWER
CAL/SWR SW	SWR

Tune up the transceiver at 14.175 MHz

3. Adjust the transceiver for 100W output at the power meter (adjusted by the carrier level control).
Adjust VR2 on the switch unit for a meter reading of 100 on the AT-230.

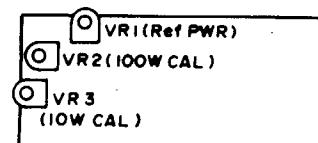


Fig. 2

4. As in step 3, lower transceiver output to 10W and adjust VR3 for a meter indication of 10 on the 20W scale

3. CALIBRATION OF REF POWER

1) Connection

As in Fig. 1.

2) Adjustment

1. Set controls as described in 2.2.
Confirm 100W CW output reading.
2. Unkey the transceiver and reverse the coaxial cable between the INPUT and DUMMY terminals on the AT-230. Move the FWD/REF switch to the REF position.
3. Transmit and adjust VR1 for previously confirmed 100W reading.

DS-2 (W type only)

SPECIFICATIONS

Semiconductor..... 2N4280 × 2

Rated Final Stage Input

Power at 13.8 VDC.

SSB..... 130W PEP

CW..... 100W

Power Consumption

Receive:

Heaters OFF..... 0.8A

Heaters ON..... 4.5A

Transmit..... 15A

Power Supply..... 12 – 16 VDC (standard: 13.8V)

Dimensions..... W 80 mm (3.15")

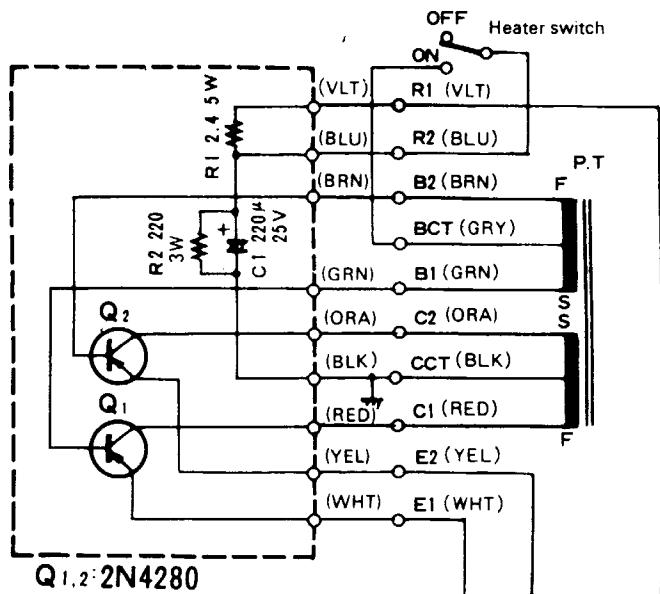
H 37 mm (1.46")

D 69 mm (2.72")

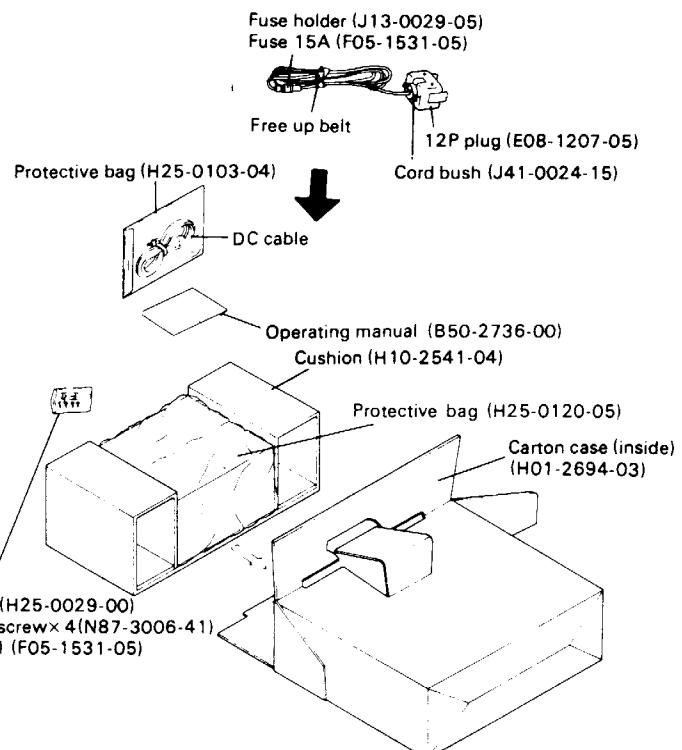
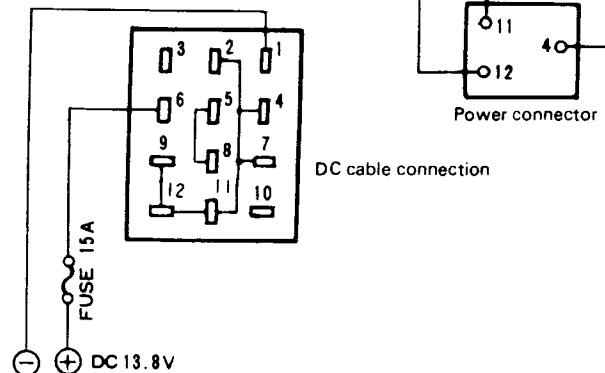
Weight..... 240g

PARTS LIST

Ref. No.	Parts No.	Description	Re-marks
	B50-2736-00	Operating manual	☆
C1	CE02W1E221	C 220μF 25V	
	E08-1207-05	12P plug	
	E20-0513-05	5P terminal	
	F05-1531-05	Fuse 15A	
	F29-0414-04	Insulating washer	
	H01-2694-03	Carton case (inside)	☆
	H10-2541-04	Cushion	☆
	H25-0029-04	Protective bag	
	H25-0103-04	Protective bag	
	H25-0120-04	Protective bag	
	J13-0029-05	Fuse holder	
	J41-0024-15	Cord bush	
R1	R92-0121-05	Cement resistor 2.4Ω 5W	
R2	R92-0120-05	Cement resistor 220Ω 2.5W	
Q1,2	V30-1161-06	TR 2N4280	

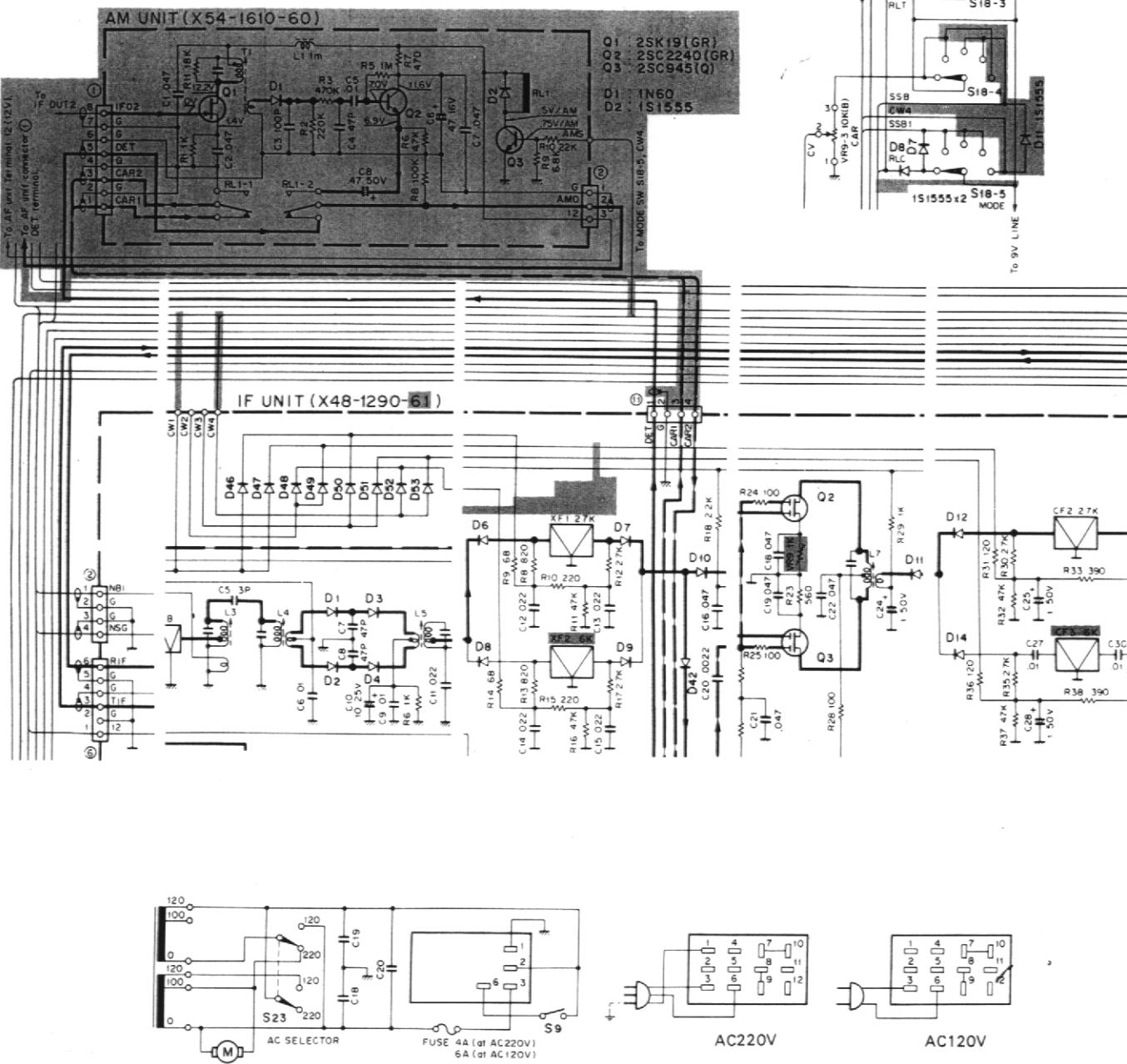


Q1,2: 2N4280



TS-830M SCHEMATIC DIAGRAM (EXCEPT U.S.A. MARKET)

The shaded parts indicate the changed circuits from TS-830S to TS-830M.



AF Unit (X49-1140-00)

Con- nector No.	Termin- inal No.	Termi- nal	Remarks	Destination			
				Unit/switch	Con- nector No.	Termin- inal No.	Termin- inal
①	1	GND	Detector input Bias voltage for SSB	IF unit	⑪ ⑨	1	DET SSB
	2	DET		IF unit		1	DET SSB
	3	SSB		IF unit		3	VXI NBI STB
	4	GND	VOX input Noise blanker input	IF unit	⑨ ②	1	VXI NBI STB
	5	VXI		IF unit			
	6	NBI		Rectifier unit			
②	1	AV2	Arm of AF gain control	AF gain control			
	2	GND	Top of AF gain control	AF gain control			
	3	AV1	To ground in CW and CW NAR	MODE SW S18-6			STE
	4	GND	To ground in Tune, USB and LSB	MODE SW S18-6			KEY
	5	STE					
	6	KEY					
③	1	SP	Speaker internal	REMOTE socket		1	SP
	2	GND		KEY Jack		7	STS
	3	STS	Side tone SW				
④	1	SS	Stand by SW VOX switch to ground VOX on	REMOTE socket		3	SS
	2	VXS		VOX SW			
	3	VXD		VOX delay control			
	4	VXG	VOX gain control	VOX gain control			
	5	GND		Speaker output		2	
	6	SP		Phone jack			
⑤	1	VR3	Final bias control Key jack VR1	Bias control		3	VR3
	2	KEY		KEY jack		2	KEY
	3	VR1		Bias control		1	VR1
	4	AV	Anti VOX arm	Anti VOX control		2	AV
	5	SS	Stand by SW	Stand by SW S17		SS	
	6	RL	Relay Voltage +12.3V on TX +9V	IF unit	⑩	3	RL
	7	9		EXT VFO socket		9	
⑥	1	RB1	Bias for Q1 RF amp on RF unit	RF unit	⑧	2	RB1
	2	GND	From Q18 on AF unit bias to MIXer	RF unit		3	MXB
	3	MXB	TX stop signal for WARC	RF unit	⑥	1	TOF
	4	TOF	Bias for control grid V1	RF unit		1	DRB
	5	DRB					
⑦	1	XIT	Transmitter incremental tuning Meter select switch S13 arm	XIT SW S2-1			XIT
	2	TM		Meter SW S13			TM
	3	RLT		MODE SW S18-3			RLT
	4	RSC	Receiver RIT ON	RIT SW S3-1			RSC
	5	RLR	Relay +9V on RX	MODE SW S18-5			RLR
	6	M	Meter voltage	Meter			
	7	-6	Bias voltage -6V				
	8	RTI	RIT SW	RIT SW S3-1		2	RTI
	9	RIT	RIT to VFO	VFO unit			RIT
		FG	Bias for control grid Final	Final unit			FG
⑧	1	XAL	Transmitter ALC ALC input remote socket Arm of TX bias control	X VTER socket		6	XAL
	2	RAL		REMOTE socket		6	RAL
	3	VR2		Bias control			
⑨	1	NBV	Top of noise blanker VR Noise blanker SW +9V	NB control			
	2	NBS		NB SW S16			
	3	9		MODE SW S18-4			
	4	SSB	+9V in USB/LSB	RF gain control			SSB
	5	RF1	Top of RF gain control				
⑩	1	NSG	Noise blanker gate signal	IF unit	②	4	NSG
	2	GND		IF unit			
	3	ALC	ALC voltage from Q19 on AF unit	IF unit	⑤	3	ALC
	4	RB2	Receiver bias for IF unit	IF unit		5	RB2
	5	TB	TX bias	IF unit		1	TB
	6	SM	S meter signal	IF unit		2	SM
	7	RS	Ground on RX	IF unit		2	RS

IF unit (X48-1290-00)

①	1	IFO1		IF OUT 1 jack			IFO1
②	1	NBI	Noise blanker input	AF unit	①	6	NBI
②	2	G					
②	3	G					
②	4	NSG	Noise blanker gate signal	AF unit	⑩	1	NSG
③	1	-6					
③	2	PRO	-6V				
③	3	COT	Processor SW				
③	4	COM	Arm of VR9-2 processor gain control				
			Compression meter signal	Meter SW S13			

TS-830S SCHEMATIC ABBREVIATION

Con- nector No.	Ter- mi- nal No	Ter- mi- nal	Remarks	Destination			
				Unit/switch	Con- nector No.	Ter- mi- nal No.	Ter- mi- nal
④	1 2 3 4	RLT SSB1 ALM 9	ALC meter DC signal +9V	MODE SW S18-3 MODE SW S18-5 Meter SW S13			RLT SSB1 ALM
⑤	1 2 3	TB RS ALC	Transmit bias Ground on TX ALC input from AF unit Q19	AF unit AF unit AF unit	⑩ ⑩ ⑩	5 7 3	TB RS ALC
		CW1		MODE SW S 18-5			CW1
⑥	1 2 3 4 5 6	12 G TIF G G RIF	+ 12V TX IF output RX IF input	RF unit RF unit	⑦ ⑤	2 2	TIF RIF
⑦	1 2 3 4 5 6	MIC G G MV1 MV2 G	MIC input Top of MIC gain control Arm of MIC gain control	MIC jack MIC gain control VR9-1 PRO SW S15-2		1	
⑧	1 2 3 4 5	CV RFG AGS AGO NCH	Arm of carrier control Arm of RF gain control AGC SW slow AGC OFF Notch frequency control voltage	CAR control VR9-3 RF gain control VR3-1 AGC SW S11-2 AGC SW S11-1 Notch SW S1-1			CV RFG AGS AGO NCH
⑨	1 2 3 4 5	SSB SM VXI G RB2	Bias voltage for SSB Smeter DC signal To VOX circuit RX bias	AF unit AF unit AF unit AF unit	① ⑩ ① ⑩	3 6 5 4	SSB SM VXI RB2
⑩	1 2 3	IFO2 G RL	IF output for monitor TX bias for Q22 on IF unit from relay drive voltage	IF OUT 2 jack			IFO2
⑪	1 2 3 4	DET G CAR1 CAR2	Detector output	AF unit	①	2	DET
⑫	1 2 3 4	MX MOE MO VBT	Monitor ON SW Monitor signal ground Monitor signal output Arm of VR1-1	Monitor SW S5 AF gain control VR3-2 VBT control VR1-1			MO VBT
⑬	1 2	CAR G	CAR input	PLL unit	⑦	2	CAR
⑭	1 2 3 4	210A G 210B 300B	210 B + 210 B + 300 B +	Rectifier unit Rectifier unit Rectifier unit			210A 210B 300B
⑮	1 2 3	300A G AGC	300 B + Output for AGC	RF unit RF unit	⑦ ⑤	4 1	300A AGC

PLL Unit (X50-1680-00)

①			Counter unit	③			
②			Counter unit	②			
③			Counter unit	②			
④	1 2	G MKR	Marker output	RF unit RF unit	④ ④	1 2	G MKR
⑤	1 2	G VFO	VFO input	VFO unit VFO unit		4 5	GND VFO
		VCO	VCO output	RF unit			VCO
⑥	1 2 3	VFO G TXC	VFO input EXT VFO Socket	EXT. VFO socket EXT VFO Socket		1 4	VFO TXC
⑦	1 2	G CAR	Carrier output	IF unit	⑪	1	CAR
⑧	1 2 3 4 5 6 7 8 9	9 RLR MS LSB USB IFS TXC FXB RLS	+ 9V Relay + 9V on RX Calibrator SW S10 + 9V on LSB mode + 9V on TUN ~ USB Arm of VR2-2 IF shift control MODE SW S18-3 For FIX indicator MODE SW S18-3	CAL SW S10 MODE SW S18-1 MODE SW S18-1 IF SHIFT VR2-2 MODE SW S18-3 FIX IND. D2 MODE SW S18-3			LSB USB IFS TXC RLS

Counter Unit (X54-1540-00)

Con- nector No.	Termin- inal No.	Termin- inal	Remarks	Destination			
				Unit/switch	Con- nector No.	Termin- inal No.	Termin- inal
①	1 2 3	DH 0.5 12	Display hold 0.5 MHz shift + 12V	DH SW S12 EXT VFO socket		6	0.5
②				PLL unit	②		
③				PLL unit	①		
④				RF unit	①		
⑤	1 2 3 4	28 28.5 29 29.5	To + 0.5 SW S7-1 To + 0.5 SW S7-2	RF unit RF unit	② ②	1 2	28 29

VFO Unit (X40-1170-00)

1	RLC	From MODE SW S18-5	MODE SW S18-5	⑦	9	RLC
2	RIT	RIT Voltage input	AF unit			RIT
3	VFB	VFO B + 9V	FIX SW S4			VFB
4	GND		PLL unit	⑤	2	VFO
5	VFO	VFO output				

FINAL Unit (X56-1380-00)

	FG H SG IPM DRV	Control grid bias Heater voltage AC 12.3V Screen grid voltage IP meter voltage Drive input	AF unit Rectifier unit Meter SW S13 RF unit			FG PD2 DRV
--	-----------------------------	--	--	--	--	------------------

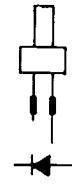
RF unit (X44-1360-00)

①				Counter unit	④	
②	1 2 3	28 29 9V	To + 0.5 SW S7-1 To + 0.5 SW S7-2 + 9V	+ 0.5 SW S7-1 + 0.5 SW S7-2		
③	1 2 3	RLR G ANT	+ 9V on RX Receiver antenna input	RLR Line RF ATT SW S6-1		
④	1 2	G MKR	Marker input	PLL unit	④	2 MKR
⑤	1 2 3	AGC RIF G	AGC voltage RX IF output	IF unit IF unit	⑤ ⑥	3 6 AGC RIF
⑥	1 2 3	TOF 12 MXB	TX turn off signal + 12V Bias for TX MIXer	AF unit AF unit	⑥ ⑥	4 3 TOF MXB
⑦	1 2 3 4	12 TIF G 300A	+ 12V TX IF input 300 B +	IF unit IF unit	⑥ ⑩	3 1 TIF 300A
⑧	1 2	DRB RB1	Control grid bias for V1 Bias for Q1 RF amp	AF unit AF unit	⑥ ⑥	5 1 DRB RB1
⑨	1 2	H 210A	Heater voltage AC 12.3V 210 B +			
⑩	1 2	G UV	Transverter output	X. VTER socket	7	UV
	NC DRV VCO NFB		Neutralization Drive output VCO input Negative feed back	Final tank circuit Final unit PLL unit Final tank circuit		DRV VCO

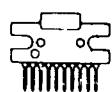
Rectifier unit (X43-1370-00)

	HV 800 300B 210B 210A STB - C SG2 TUN 12 SGS SG1 PD2 PD1	High voltage signal to METER 800 B + 300 B + 210 B + 210 B + Side tone voltage Common B - Approx. - 100V Screen grid voltage Screen grid low voltage for TUN + 12V To SG SW To MODE SW Screen grid voltage	METER SW S13 AF unit MODE SW S18-2 MODE SW S18-2 SG SW S20 MODE SW S18-2 Final unit			HV STB SG2 TUN SGS SG1 SG
--	---	---	---	--	--	---

SLP-144



HA1368R



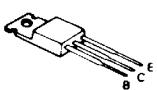
TLR-205



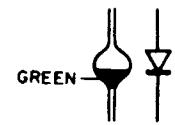
TA7302P



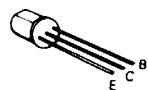
2SA473 (Y)



MV-13



2SA562 (Y)



3SK73 (GR)



2SA1015 (Y)

2SC945 (Q)

2SC1515 (K)

2SC1675 (L)

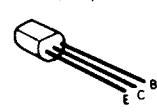
2SC1775 (E)

2SC1815 (BL). (GR). (Y)

2SC1923 (O)

2SC1959 (Y)

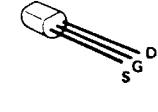
2SC2240 (GR)



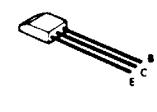
2SK19 (BL). (GR). (Y)

2SK30A (GR). (O)

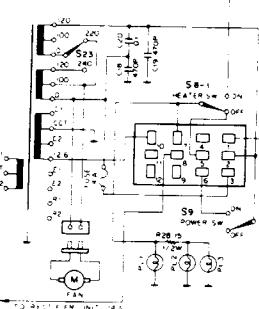
2SK125



2SC460 (B)

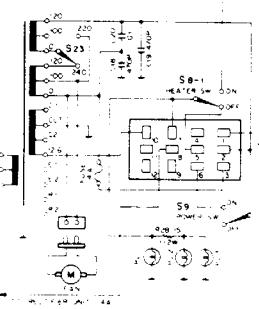


W TYPE



AC220V

T, X TYPE



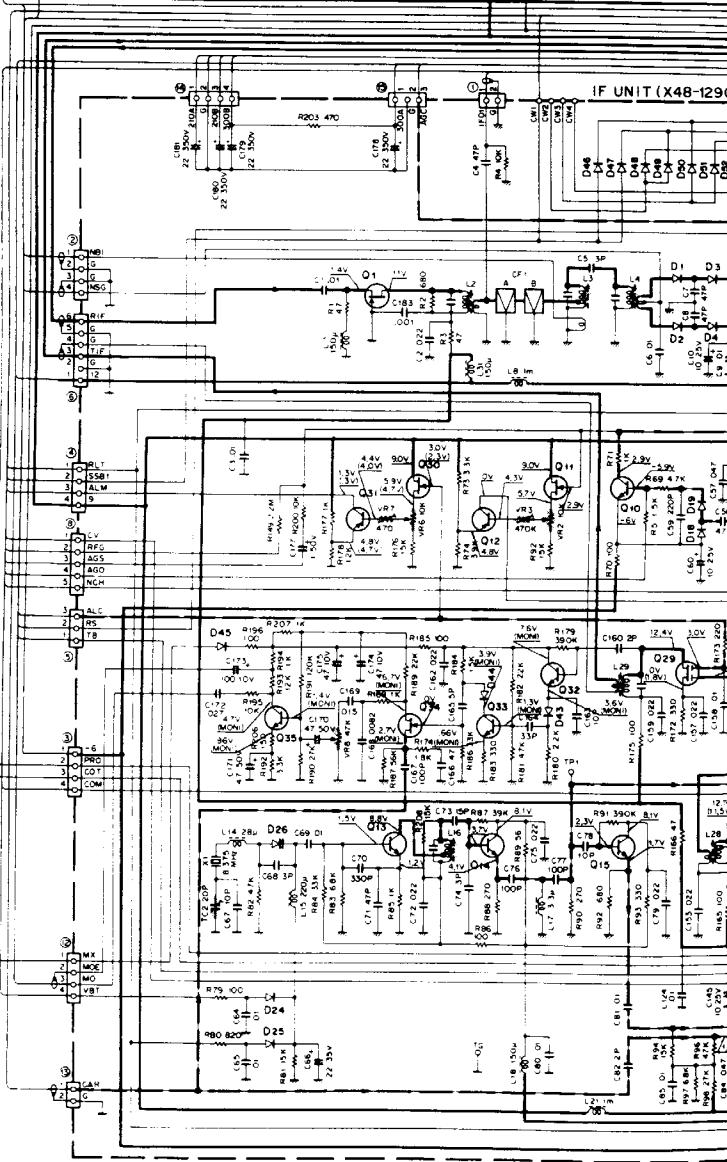
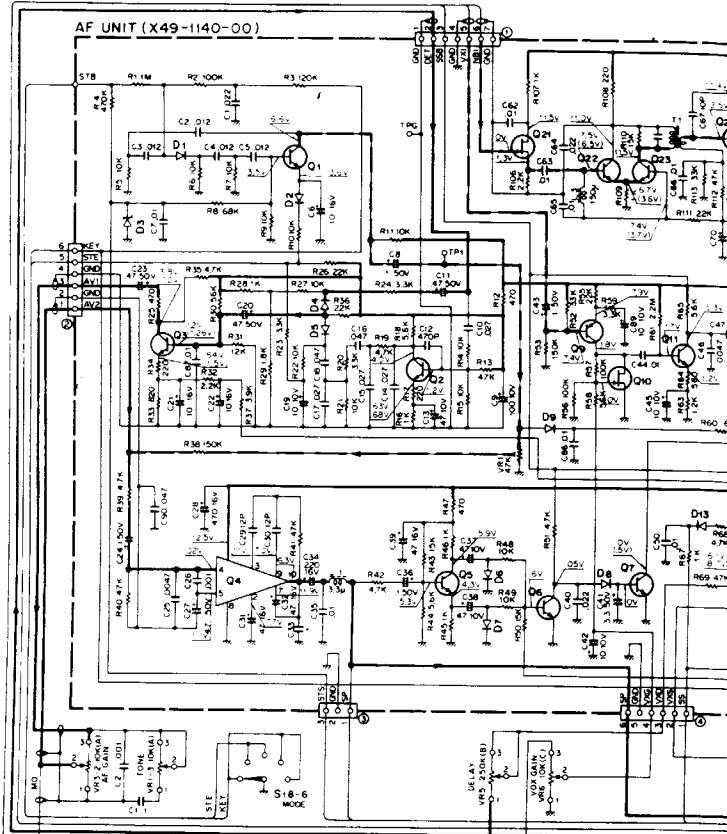
AC240V

T TYPE



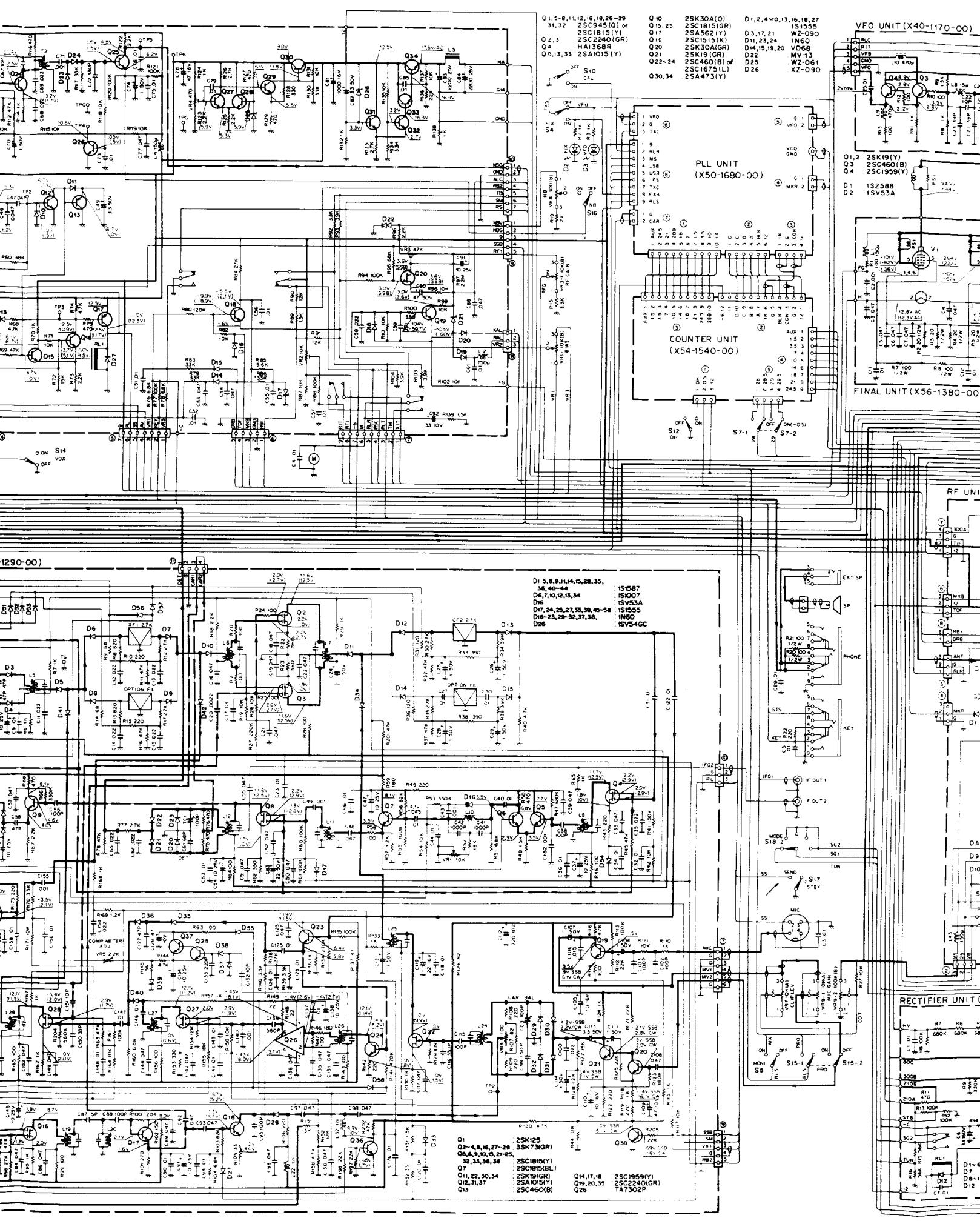
AC240V

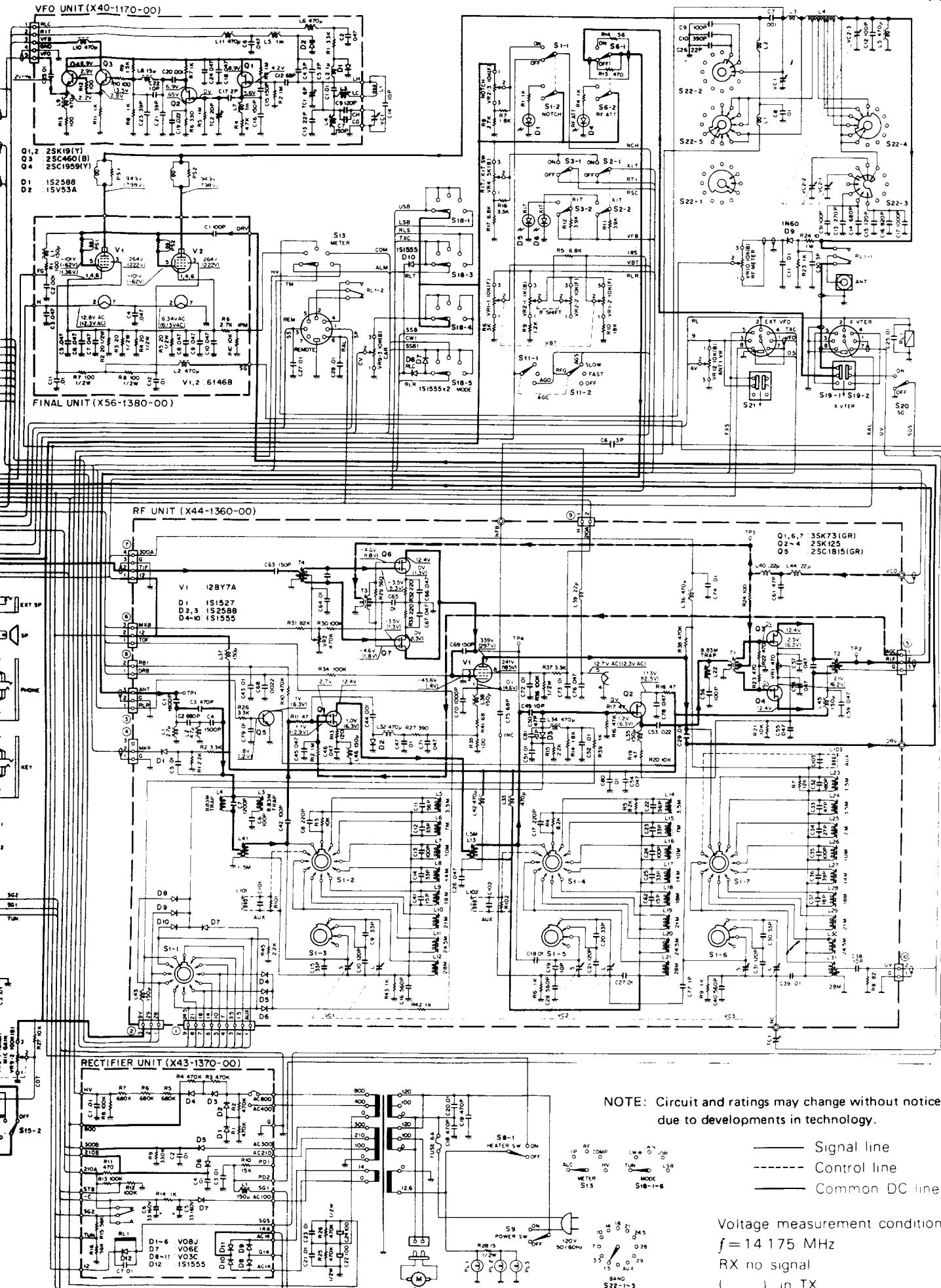
X TYPE

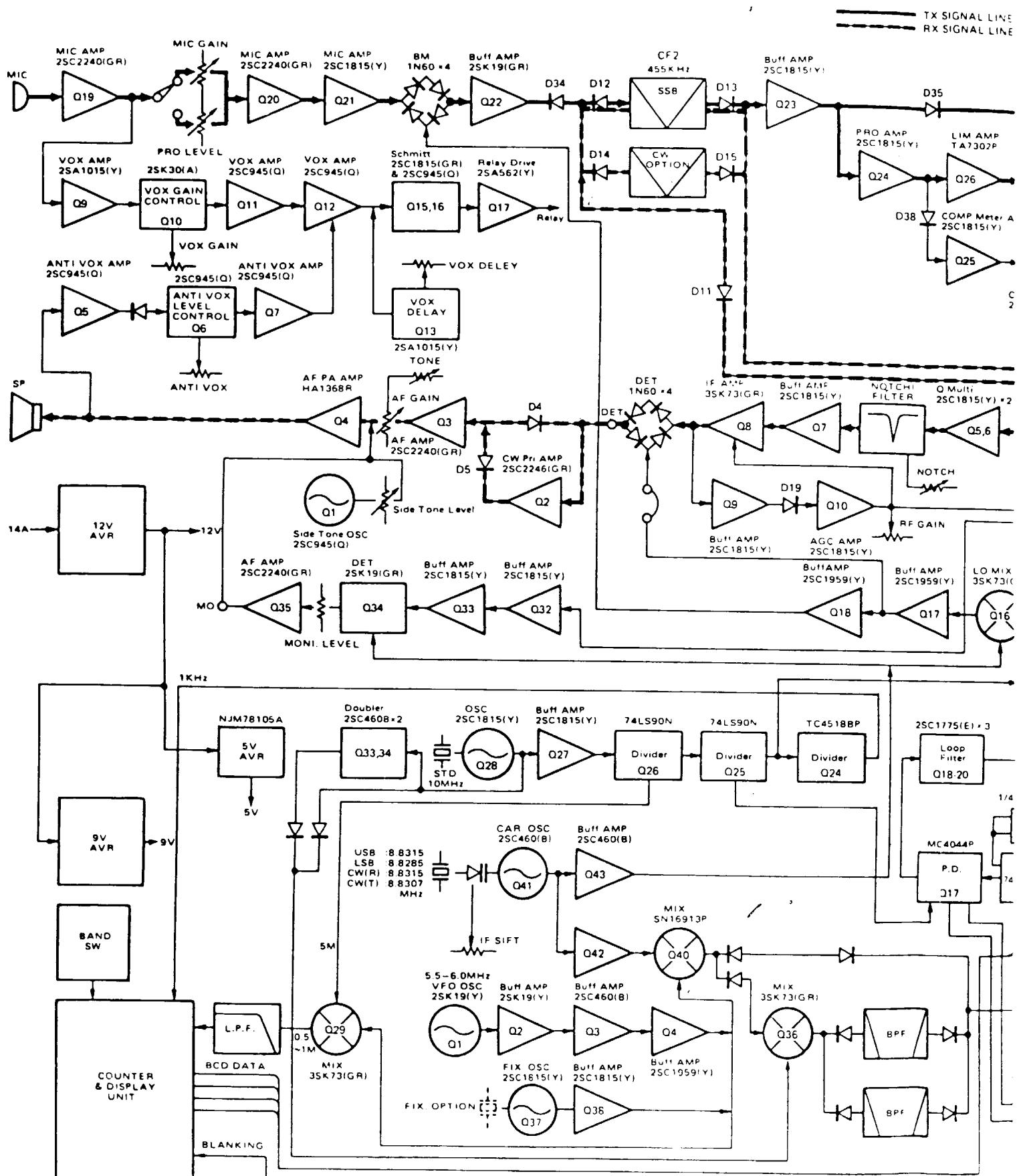


Downloaded by □

Amateur Radio Directory

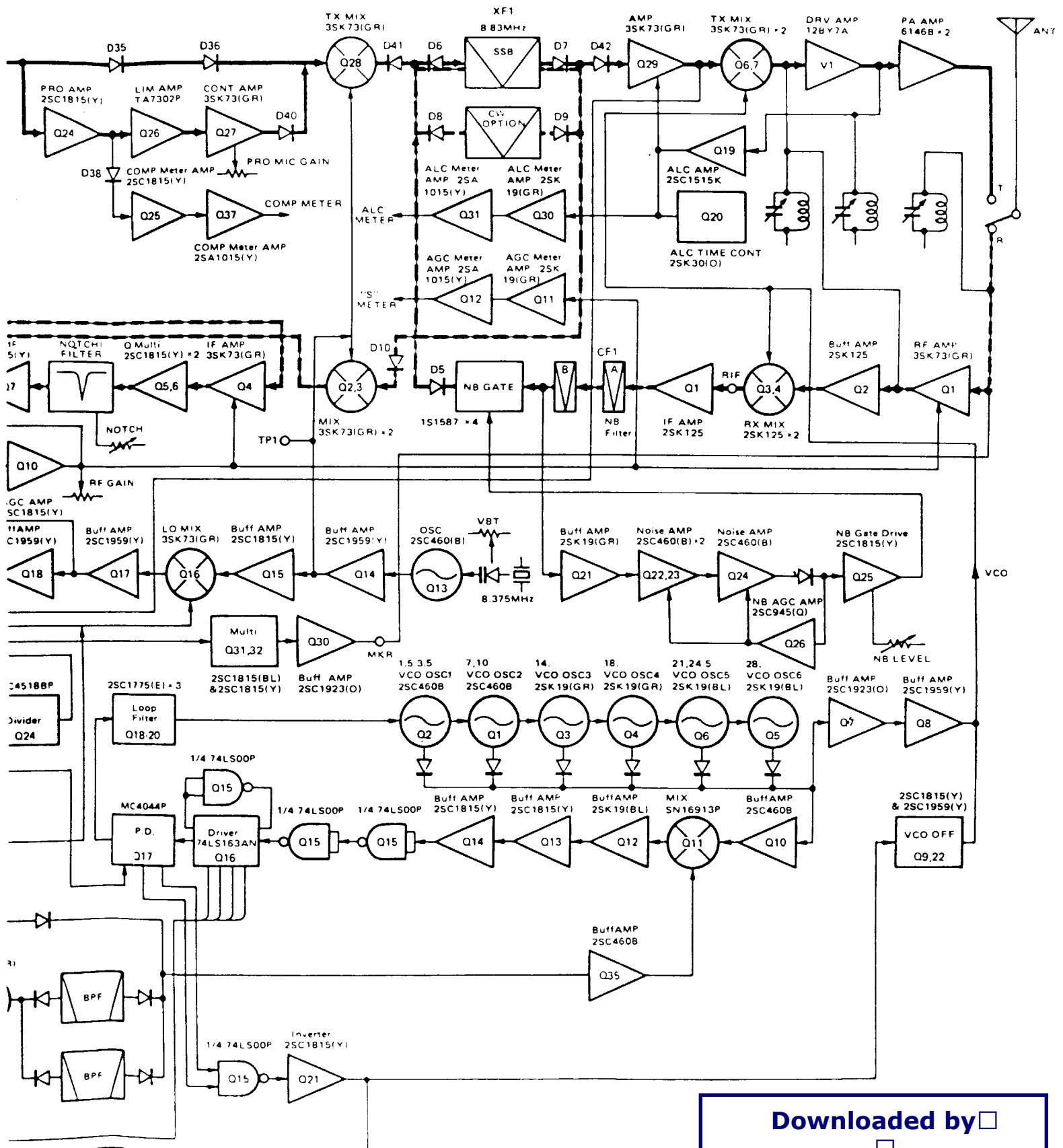






S-830S BLOCK DIAGRAM

— TX SIGNAL LINE
— RX SIGNAL LINE



SP-230

SPECIFICATIONS

Speaker used: 12 cm dia.
Rated Input: 2 Watts
Impedance: 8Ω
Frequency response: 100Hz to 5kHz.
Filter cut-off frequency,
 LOW: 400Hz, -3dB.
 HIGH 1: 3kHz, -3dB.
 HIGH 2: 1.5kHz, -3dB.
 HIGH 1 + HIGH 2: 1 kHz, -3dB.
Filter attenuation: -6dB/oct.
Dimensions: W 180 mm (7-1/16")
 H 133 mm (5-1/4")
 D 287 mm (11-5/16")
Net weight: 1.8 kg. (4.0 lbs.)
Accessories furnished: Speaker cord, 1 pc.
 (E14-0101-05)
 Extension foot, 2 pcs.
 (J02-0049-14)
 Screw, M4 x 12, 2 pcs.
 (N30-4012-41)
 1 pin plug, 2 pcs.
 (E20-1610-05)

PARTS LIST

Ref. No.	Parts No.	Description	Re-marks
	A01-0786-03	Case (upper)	★
	A01-0789-02	Case (lower)	★
	A20-2399-05	Panel (T)	★
	A20-2400-05	Panel (K)(W)	★
	B46-0058-00	Warranty card (K)	
	B50-2759-00	Operating manual (K)(W)	★
	B50-2760-00	Operating manual (T)	★
	E11-0404-05	Phone jack	
	E12-0001-05	Phone plug	
	E13-0361-05	3P Pin jack	
	E14-0101-05	1P Pin plug	
	E30-1610-05	Connector with lead	
	G53-0502-04	Packing	
	H01-2723-04	Carton (inside)(K)(W)	★
	H01-2724-04	Carton case (inside)(T)	★
	H10-2523-02	Packing fixture (F)	
	H10-2525-02	Packing fixture (R)	
	H20-0276-03	Protective cover	
	H25-0049-03	Protective bag	
	J02-0049-14	Foot	
	K29-0716-04	Push knob	
	L79-0443-25	Filter	
	RS14AB3D8R2J	Solid 8.2Ω 2W	
	S40-2414-05	Push switch INPUT	
	S42-3401-05	Push switch LOW, HIGH 1, 2	
	T06-0011-05	Speaker ✓	

A product of
TRIO-KENWOOD CORPORATION
 17-5, 2-chome, shibuya, shibuya-ku Tokyo 150, Japan

TRIO-KENWOOD COMMUNICATIONS

1111 West Walnut Street Compton, California 90220, U.S.A.

TRIO-KENWOOD COMMUNICATIONS, GmbH

D-6374 Steinbach TS, Industriestrasse 8A, West Germany

TRIO-KENWOOD ELECTRONICS, N.V.

Leuvensesteenweg 504, B-1930 Zaventem, Belgium

TRIO-KENWOOD (AUSTRALIA) PTY. LTD.

4E Woodcock Place, Lane Cove N.S.W. 2066, Australia